TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

### **TA76L431FT,TA76L431S**

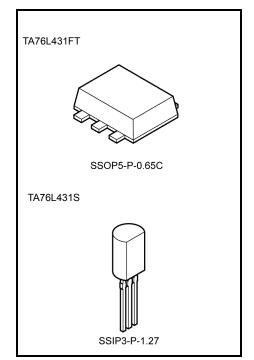
### 2.49-V Adjustable High-Precision Shunt Regulators

These devices are adjustable high-precision shunt regulators whose output voltage ( $V_{KA}$ ) can be set arbitrarily using two external resistors.

The devices have a precise internal reference voltage of 2.49 V, enabling them to operate at low voltage. In addition, they can be used as zener diodes to perform temperature compensation.

### **Features**

- Precision reference voltage
   : V<sub>REF</sub> = 2.49V ± 1.0% (Ta = 25°C)
- Adjustable output voltage
  - :  $V_{REF} \le V_{OUT} \le 19 V$
- Minimum cathode current for regulation
   I<sub>kmin</sub> = 0.5 mA (max.)
- Operating temperature:  $Ta = -40 \sim 85^{\circ}C$
- Packages: UFV (TA76L431FT), TO-92MOD (TA76L431S)
- The TA76L431FT is housed in an ultra-thin UFV package. (thickness: 0.7 mm typ.)



Weight

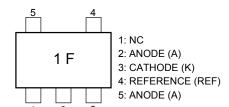
SSOP5-P-0.65C: 0.007 g (typ.) SSIP3-P-1.27: 0.36 g (typ.)



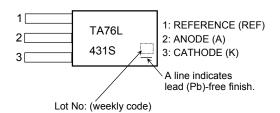
### Pin Assignment/Marking



TOSHIBA



#### TA76L431S



### **How to Order**

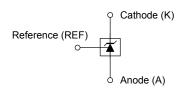
| Product No.         | Package Type             | Packing Type and Capacity | Minimum Order |
|---------------------|--------------------------|---------------------------|---------------|
| TA76L431FT (TE85L,F | UFV (surface-mount type) | Embossed tape: 3000/tape  | 1 tape        |
| TA76L431S(Q)        | TO-92MOD                 | Loose in bag: 200/bag     | 1 bag         |
| TA76L431S (TPE6,Q)  | (lead type)              | Radial tape: 2000/tape    | 1 tape        |

Note: The lead pitch for the TA76L431S(Q) and TA76L431S(TPE6,Q) may vary.

### **Functional Block Diagram**

# Cathode (K) Reference (REF) 2.49V Anode (A)

### **Circuit Symbol**

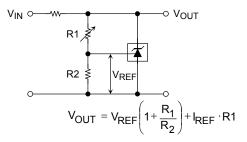


### **Typical Application Circuits**

### (1) 2.49 V Reference $(V_{KA} = V_{REF})$

## V<sub>IN</sub> V<sub>OUT</sub> = 2.49 V

### (2) Shunt regulator $(V_{KA} > V_{REF})$



### **Precautions during Use**

1. TA76L431FT, TA76L431S

These products contain MOS elements. Please take care to avoid generating static electricity when handling these devices.

2. TA76L431FT, TA76L431S

The oscillation frequency of these devices is determined by the value of the capacitor connected between the anode and the cathode.

When establishing maximum operating condition parameters, please derate the maximum rating values specified in these datasheets so as to allow an operational safety margin.

Use of a laminated ceramic capacitor is recommended

3. Precautions when handling anode pin of TA76L431FT

Pin 2 and pin 5 should normally be shorted together. If only pin 5 is used, pin 2 should either be left open or always kept at a lower potential than pin 5. Do not leave pin 5 open and use pin 2 only.

### Absolute Maximum Ratings (Ta = 25°C)

| Characteristics                 |            | Symbol            | Rating        | Unit |  |
|---------------------------------|------------|-------------------|---------------|------|--|
| Cathode voltage                 |            | $V_{KA}$          | 20            | ٧    |  |
| Cathode current                 |            | ΙK                | 50            | mA   |  |
| Cathode-anode reverse current   |            | $-I_{K}$          | 50            | mA   |  |
| Reference voltage               |            | V <sub>REF</sub>  | 7             | V    |  |
| Reference current               |            | I <sub>REF</sub>  | 50            | μΑ   |  |
| Reference-anode reverse current |            | -I <sub>REF</sub> | 10            | mA   |  |
| Power dissipation               | TA76L431FT | P <sub>D</sub>    | 0.45 (Note 1) | W    |  |
|                                 | TA76L431S  |                   | 0.8           |      |  |
| Thermal resistance              | TA76L431FT | D.                | 277 (Note 1)  | °C/W |  |
|                                 | TA76L431S  | R <sub>th</sub>   | 156           |      |  |
| Operating temperature           |            | T <sub>opr</sub>  | -40~85        | °C   |  |
| Junction temperature            |            | Tj                | 150           | °C   |  |
| Storage temperature             |            | T <sub>stg</sub>  | -55~150       | °C   |  |

Note 1: Glass epoxy substrate mounting: 30 mm  $\times$  30 mm  $\times$  0.8 mmt (Cu pad area 35 mm $^2$ )

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Recommended Operating Conditions**

TOSHIBA

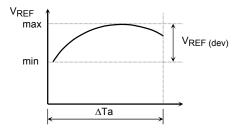
| Characteristics       | Symbol           | Min       | Тур. | Max | Unit |
|-----------------------|------------------|-----------|------|-----|------|
| Cathode voltage       | $V_{KA}$         | $V_{REF}$ | _    | 19  | V    |
| Cathode current       | Ι <sub>Κ</sub>   | 0.5       | _    | 40  | mA   |
| Operating temperature | T <sub>opr</sub> | -40       |      | 85  | °C   |

### Electrical Characteristics (Unless otherwise specified, Ta = 25°C, $I_K = 10$ mA)

| Characteristics   | Symbol                 | Test Condition  | Min   | Тур. | Max   | Unit |
|---|------------------------|---|-------|------|-------|------|
| Reference voltage   | $V_{REF}$              | $V_{KA} = V_{REF}$  | 2.465 | 2.49 | 2.515 | V    |
| Deviation of reference input voltage over temperature                       | V <sub>REF (dev)</sub> | $0^{\circ}\text{C} \le \text{Ta} \le 70^{\circ}\text{C}, V_{KA} = V_{REF}$  | _     | 5    | 15    | mV   |
| Ratio of change in reference input voltage to the change in cathode voltage | ΔV <sub>REF</sub> /ΔV  | $V_{REF} \le V_{KA} \le 10 \text{ V}$   | _     | 0.8  | 2.4   | mV/V |
|   |                        | $10V \le V_{KA} \le 19 V$   | _     | 0.8  | 2.0   |      |
| Reference Input current   | I <sub>REF</sub>       | V <sub>K</sub> A = V <sub>REF</sub>   | _     | 0.6  | 3     | μА   |
| Deviation of reference input current over temperature                       | I <sub>REF (dev)</sub> | $\begin{array}{l} 0^{\circ}C \leqq Ta \leqq 70^{\circ}C, \ V_{KA} = V_{REF}, \\ R_1 = 10 \ k\Omega, \ R_2 = \infty \end{array}$ | _     | 0.3  | 1.2   | μА   |
| Minimum cathode current for regulation                                      | I <sub>Kmin</sub>      | V <sub>KA</sub> = V <sub>REF</sub>  | _     | 0.2  | 0.5   | mA   |
| Off-State cathode current   | I <sub>Koff</sub>      | $V_{KA} = 19 \text{ V}, V_{REF} = 0 \text{ V}$  | _     | _    | 1.0   | μΑ   |
| Dynamic impedance   | Z <sub>KA</sub>        | $V_{KA} = V_{REF}, f \le 1 \text{ kHz},$<br>0.5 mA $\le I_K \le 40 \text{ mA}$  | _     | 0.2  | 0.5   | Ω    |

The deviation parameters  $V_{REF\,(dev)}$  and  $I_{REF\,(dev)}$  are defined as the maximum variation of the  $V_{REF}$  and  $I_{REF}$  over the rated temperature range(Ta=0 to 70°C).

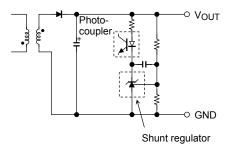
The average temperature coefficient of the VREF is defined as:



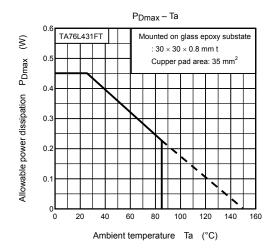
$$\left| \alpha V_{REF} \right| = \frac{\left( \frac{V_{REF \; (dev)} \times 10^6}{V_{REF \; @25^{\circ}C}} \right)}{\Delta Ta} \left( ppm/^{\circ}C \right)$$

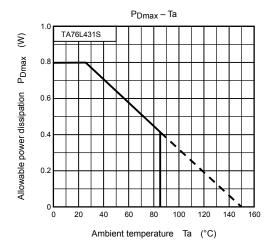
### **Application Circuit Example**

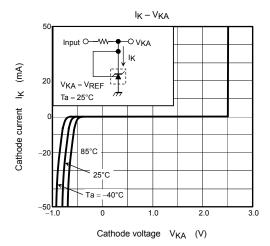
Error amplification circuit for switching power supply

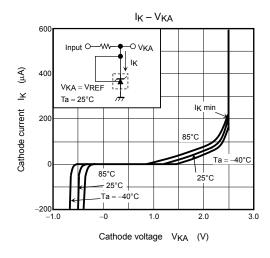


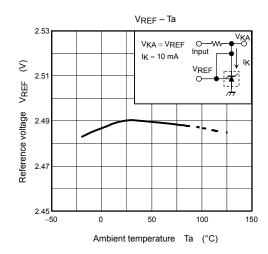
This circuit amplifies the difference between the switching power supply's secondary output voltage and the shunt regulator's reference voltage. It then feeds the amplified voltage back to the primary input voltage via the photocoupler.

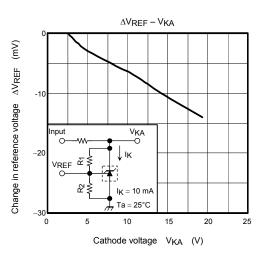




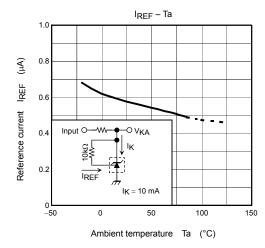


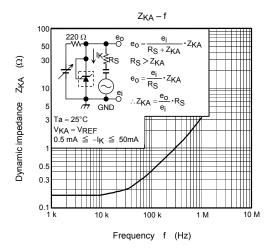


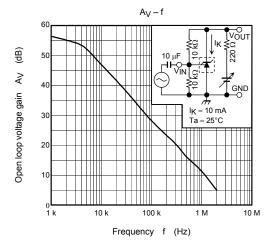


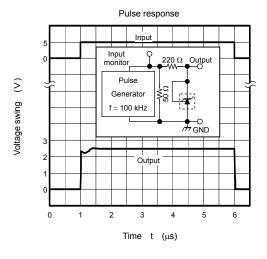


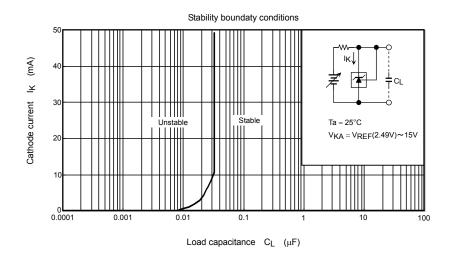
5 2006-11-06







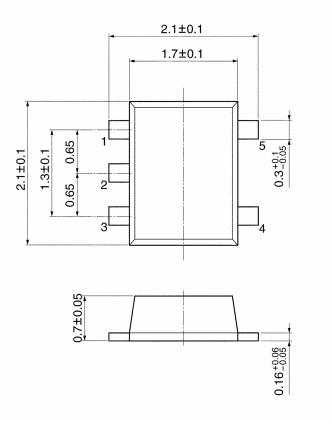




6 2006-11-06

### **Package Dimensions**

SSOP5-P-0.65C Unit: mm

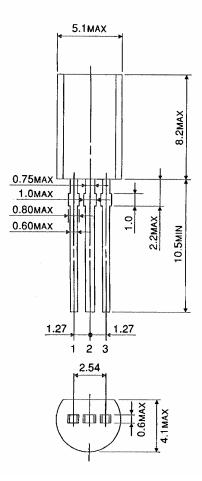


TA76L431FT (UFV)

Weight: 0.007 g (typ.)

### **Package Dimensions**

SSIP3-P-1.27



8

TA76L431S (TO-92MOD)

Weight: 0.36 g (typ.)

### **RESTRICTIONS ON PRODUCT USE**

20070701-EN

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
  In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in his document shall be made at the customer's own risk.
- The products described in this document shall not be used or embedded to any downstream products of which
  manufacture, use and/or sale are prohibited under any applicable laws and regulations.
- The information contained herein is presented only as a guide for the applications of our products. No
  responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which
  may result from its use. No license is granted by implication or otherwise under any patents or other rights of
  TOSHIBA or the third parties.
- Please contact your sales representative for product-by-product details in this document regarding RoHS
  compatibility. Please use these products in this document in compliance with all applicable laws and regulations
  that regulate the inclusion or use of controlled substances. Toshiba assumes no liability for damage or losses
  occurring as a result of noncompliance with applicable laws and regulations.

2006-11-06