

# LM8050I, LM8050J

## Amplifier Transistors

NPN Silicon



ON Semiconductor™

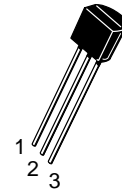
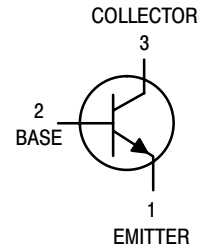
<http://onsemi.com>

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	25	Vdc
Collector-Base Voltage	$V_{CBO}$	30	Vdc
Emitter-Base Voltage	$V_{EBO}$	6.0	Vdc
Collector Current – Continuous	$I_C$	800	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	625 5.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to +150	°C

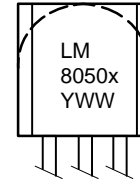
### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	°C/W



TO-92  
CASE 29  
STYLE 1

### MARKING DIAGRAMS



LM8050x = Specific Device Code  
 x = I or J  
 Y = Year  
 WW = Work Week

### ORDERING INFORMATION

Device	Package	Shipping
LM8050I	TO-92	5000 Units/Box
LM8050J	TO-92	5000 Units/Box

# LM8050I, LM8050J

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 1.0 mA <sub>dc</sub> , I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	25	–	–	V <sub>dc</sub>
Collector–Base Breakdown Voltage (I <sub>C</sub> = 0.5 mA <sub>dc</sub> , I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	30	–	–	V <sub>dc</sub>
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 0.1 mA <sub>dc</sub> , I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	6.0	–	–	V <sub>dc</sub>
Collector Cutoff Current (V <sub>CB</sub> = 15 V <sub>dc</sub> , I <sub>E</sub> = 0)	I <sub>CBO</sub>	–	–	50	nA <sub>dc</sub>

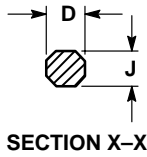
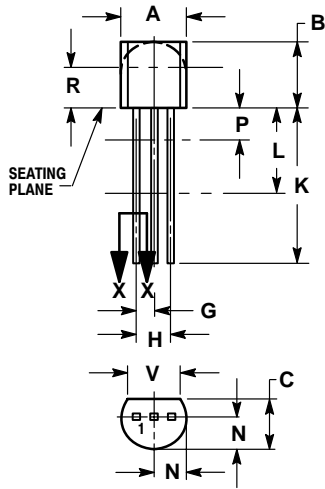
## ON CHARACTERISTICS

DC Current Gain (I <sub>C</sub> = 50 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> )  (I <sub>C</sub> = 350 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> )	LM8050I LM8050J	h <sub>FE</sub>	100	–	200	–
			150	–	300	
			60	–	–	
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 500 mA <sub>dc</sub> , I <sub>B</sub> = 50 mA <sub>dc</sub> )		V <sub>CE(sat)</sub>	–	–	0.5	V <sub>dc</sub>
Base–Emitter Saturation Voltage (I <sub>C</sub> = 500 mA <sub>dc</sub> , I <sub>B</sub> = 50 mA <sub>dc</sub> )		V <sub>BE(sat)</sub>	–	–	1.2	V <sub>dc</sub>

# LM8050I, LM8050J

## PACKAGE DIMENSIONS

TO-92 (TO-226)  
CASE 29-11  
ISSUE AL



NOTES:


1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	---	12.70	---
L	0.250	---	6.35	---
N	0.080	0.105	2.04	2.66
P	---	0.100	---	2.54
R	0.115	---	2.93	---
V	0.135	---	3.43	---

STYLE 1:

- PIN 1. EMITTER
2. BASE
3. COLLECTOR

# LM8050I, LM8050J

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**LM8050I/D**