

Voltage Regulator

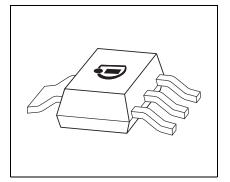
TLE 4274 / 3.3V;2.5V





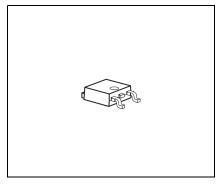
Features

- Output voltage: 3.3 V/2.5 V ± 4%
- Current capability 400 mA
- Very low current consumption
- Short-circuit proof
- Reverse polarity proof
- Suitable for use in automotive electronics
- Green Product (RoHS compliant)
- AEC Qualified



Functional Description

The TLE 4274 / 3.3V;2.5V is a voltage regulator available in a SOT223 and TO252 package. The IC regulates an input voltage up to 40 V to $V_{\rm Qrated}=3.3$ V/2.5 V. The maximum output current is 400 mA. The IC is short-circuit proof and has a shutdown circuit protecting it against overtemperature. The TLE 4274 is also available as 5 V, 8.5 V and 10 V version. Please refer to the data sheet TLE 4274.



Dimensioning Information on External Components

The input capacitor $C_{\rm I}$ is necessary for compensating line influences. Using a resistor of approx. 1 Ω in series with $C_{\rm I}$, the oscillating of input inductivity and input capacitance can be damped. The output capacitor $C_{\rm Q}$ is necessary for the stability of the regulation circuit. Stability is guaranteed for capacities $C_{\rm Q} \geq$ 10 $\mu \rm F$ with an ESR of \leq 2.5 Ω within the operating temperature range.

Туре	Package
TLE 4274 GSV33	PG-SOT223-4
TLE 4274 DV33	PG-TO252-3-11
TLE 4274 GSV25	PG-SOT223-4



Circuit Description

The control amplifier compares a reference voltage to a voltage that is proportional to the output voltage and drives the base of the series transistor via a buffer. Saturation control as a function of the load current prevents any oversaturation of the power element. The IC also includes a number of internal circuits for protection against:

- Overload
- Overtemperature
- Reverse polarity

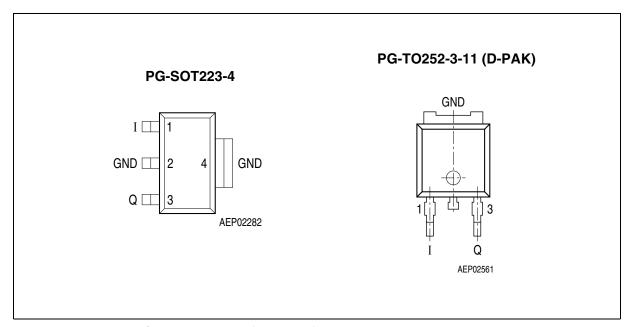


Figure 1 Pin Configuration (top view)

Table 1 Pin Definitions and Functions

Pin No.	Symbol	Function
1	I	Input; block to ground directly at the IC with a ceramic capacitor.
2, 4	GND	Ground; PG-TO252-3-11: internally connected to heatsink
3	Q	Output; block to ground with capacitor $C_Q \ge 10 \mu F$, ESR $\le 2.5 \Omega$

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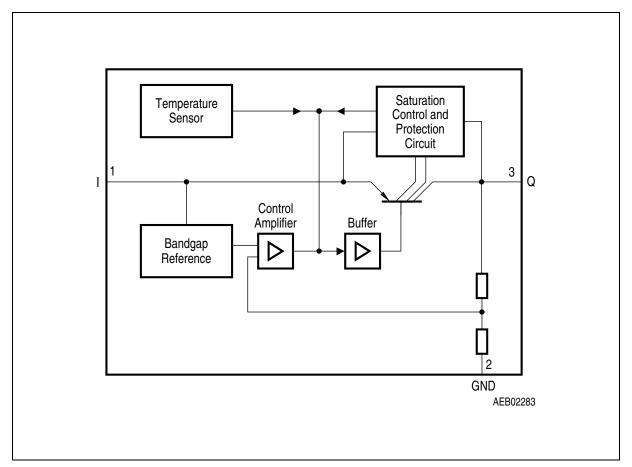


Figure 2 Block Diagram



Table 2 Absolute Maximum Ratings

 $T_{\rm i}$ = -40 to 150 °C

Parameter	Symbol	Limit Values		Unit	Test Condition
		Min.	Max.		
Input	•	•	•	•	•
Voltage	V_{I}	-42	45	V	_
Current	I_{l}	_	_	_	Internally limited
Output	<u> </u>			•	
Voltage	V_{Q}	-1.0	40	V	_
Current	I_{Q}	_	_	_	Internally limited
Ground					
Current	I_{GND}	_	100	mA	_
Temperature					
Junction temperature	$T_{\rm j}$	_	150	°C	_
Storage temperature	$T_{ m stg}$	-50	150	°C	_

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.

Table 3 Operating Range

Parameter	Symbol	Limit Values		Unit	Remarks	
		Min.	Max.			
Input voltage	V_{I}	4.7	40	V	_	
Junction temperature	T_{j}	-40	150	°C	_	
Thermal Resistance						
Junction ambient	$R_{\rm thja}$	_	100	K/W	SOT223 ¹⁾	
Junction ambient	$R_{\rm thja}$	_	70	K/W	TO252 ²⁾	
Junction case	R_{thjc}	_	25	K/W	SOT223	
Junction case	R_{thjc}	_	4	K/W	TO252	

¹⁾ Soldered in, 1 cm² copper area at pin 4, FR4

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²⁾ Soldered in, minimal footprint, FR4



Table 4 Characteristics

 $V_{\rm I}$ = 6 V; -40 °C < $T_{\rm j}$ < 150 °C (unless otherwise specified)

Parameter	Symbol	Limit Values			Unit	Measuring Condition
		Min.	Тур.	Max.		
Output voltage V33-Version	V_{Q}	3.17	3.3	3.44	V	$5 \text{ mA} < I_{\text{Q}} < 400 \text{ mA}$ $4.7 \text{ V} < V_{\text{I}} < 28 \text{ V}$
Output voltage V33-Version	V_{Q}	3.17	3.3	3.44	V	$5 \text{ mA} < I_{\text{Q}} < 200 \text{ mA}$ $4.7 \text{ V} < V_{\text{I}} < 40 \text{ V}$
Output voltage V25-Version	V_{Q}	2.4	2.5	2.6	V	$5 \text{ mA} < I_{\text{Q}} < 400 \text{ mA}$ $4.7 \text{ V} < V_{\text{I}} < 28 \text{ V}$
Output voltage V25-Version	V_{Q}	2.4	2.5	2.6	V	$5 \text{ mA} < I_{\text{Q}} < 200 \text{ mA}$ $4.7 \text{ V} < V_{\text{I}} < 40 \text{ V}$
Output current limitation ¹⁾	I_{Q}	400	600	_	mA	_
Current consumption; $I_q = I_l - I_Q$	I_{q}	_	100	220	μΑ	$I_{\rm Q}$ = 1 mA
Current consumption; $I_{q} = I_{l} - I_{Q}$	I_{q}	_	8	15	mA	$I_{\rm Q}$ = 250 mA
Current consumption; $I_{q} = I_{l} - I_{Q}$	I_{q}	_	20	30	mA	$I_{\rm Q}$ = 400 mA
Drop voltage ¹⁾ V33-Version	V_{dr}	_	0.7	1.2	V	$I_{\rm Q}$ = 300 mA $V_{\rm dr}$ = $V_{\rm I}$ - $V_{\rm Q}$
Drop voltage ¹⁾ V25-Version	V_{dr}	_	1.0	2.0	V	$I_{\rm Q}$ = 300 mA $V_{\rm dr}$ = $V_{\rm I}$ - $V_{\rm Q}$
Load regulation	ΔV_{Q}	_	40	70	mV	$I_{\rm Q}$ = 5 mA to 300 mA; $V_{\rm I}$ = 6 V
Line regulation	ΔV_{Q}	_	10	25	mV	$\Delta V_{\rm I}$ = 12 V to 32 V $I_{\rm Q}$ = 5 mA
Power supply ripple rejection	PSRR	_	60	_	dB	$f_{\rm r}$ = 100 Hz; $V_{\rm r}$ = 0.5 Vpp
Temperature output voltage drift	$\mathrm{d}V_{\mathrm{Q}}/\mathrm{d}T$	_	0.5	_	mV/K	_

¹⁾ Measured when the output voltage $V_{\rm Q}$ has dropped 100 mV from the nominal value obtained at $V_{\rm I}$ = 6 V.

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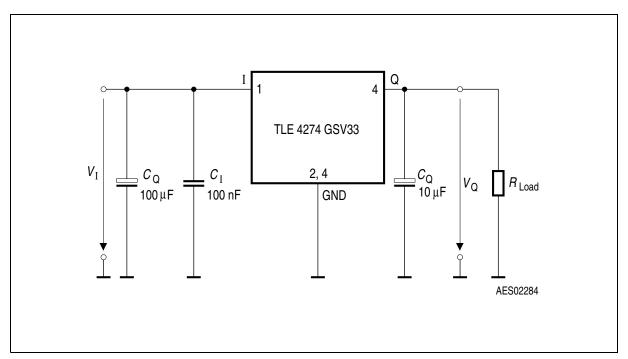


Figure 3 Measuring Circuit

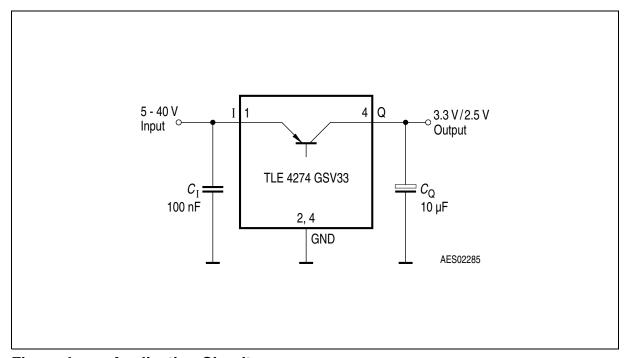


Figure 4 Application Circuit

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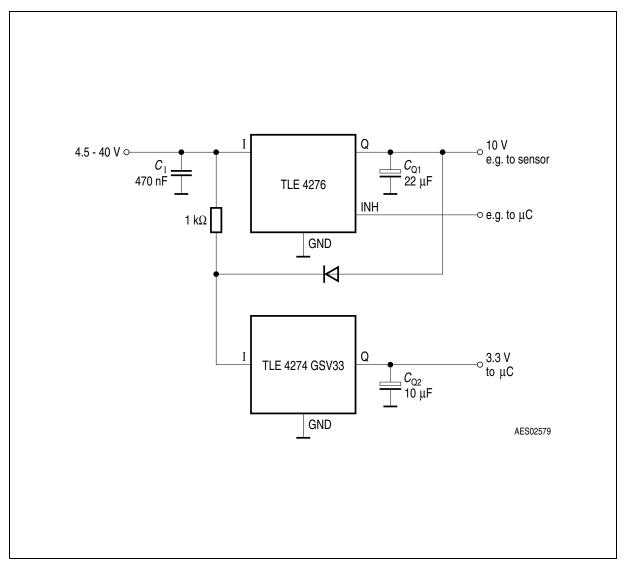
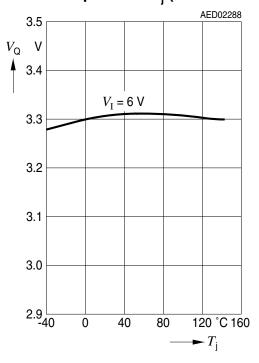


Figure 5 Application Example

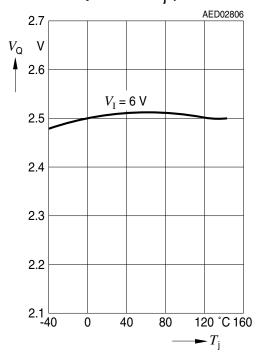


Typical Performance Characteristics

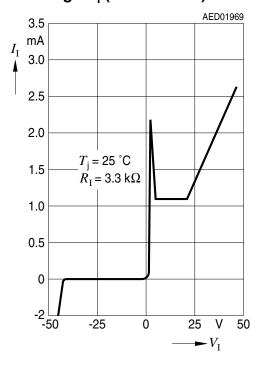
Output Voltage $V_{\rm Q}$ versus Junction Temperature $T_{\rm i}$ (V33-Version)



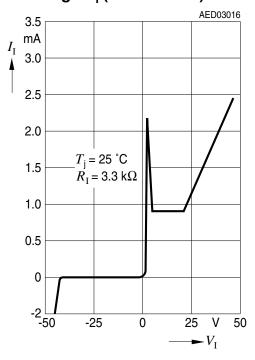
Output Voltage $V_{\rm Q}$ versus Junction Temperature $T_{\rm i}$ (V25-Version)



Input Current I_q versus Input Voltage V_1 (V33-Version)



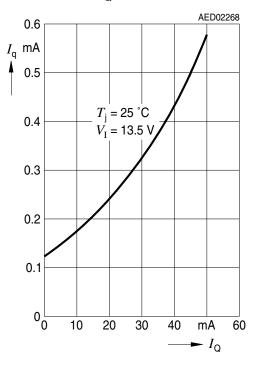
Input Current I_q versus Input Voltage V_1 (V25-Version)



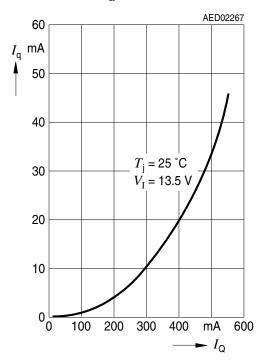
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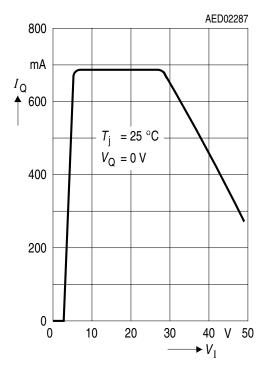
Current Consumption $I_{\rm q}$ versus Output Current $I_{\rm Q}$ (low load)



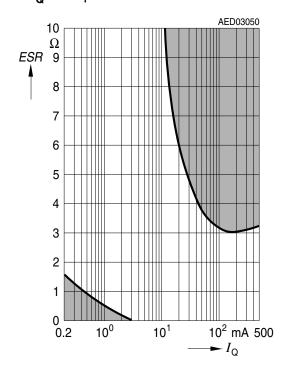
Current Consumption $I_{\rm q}$ versus Output Current $I_{\rm Q}$ (high load)



Output Current I_{Q} versus Input Voltage V_{I}



Region of Stability for $C_{\rm Q}$ = 10 $\mu{\rm F}$



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Package Outlines

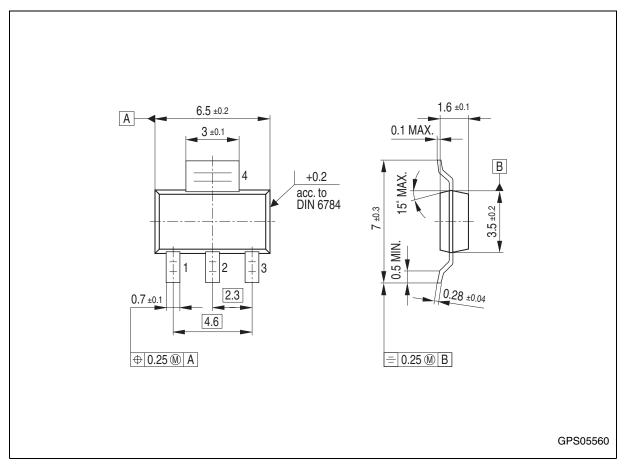


Figure 6 PG-SOT223-4 (Plastic Small Outline Transistor)

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

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SMD = Surface Mounted Device

Dimensions in mm

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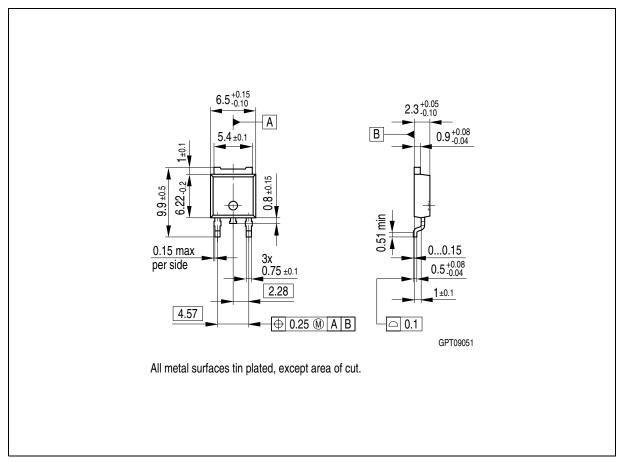


Figure 7 PG-TO252-3-11 (Plastic Transistor Single Outline)

Green Product (RoHS compliant)

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SMD = Surface Mounted Device

Dimensions in mm

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Revision History

Version	Date	Changes
Rev. 2.3	2008-03-10	Simplified package name to PG-SOT223-4. No modification of released product.
Rev. 2.2	2007-03-20	Initial version of RoHS-compliant derivate of TLE 4274 / 3.3V;2.5V Page 1: AEC certified statement added Page 1 and Page 10: RoHS compliance statement and Green product feature added Page 1 and Page 10: Package changed to RoHS compliant version Legal Disclaimer updated

Edition 2008-03-10

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Infineon Technologies AG
81726 Munich, Germany
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