INTEGRATED CIRCUITS

DATA SHEET

TZA3010B

30 Mbits/s up to 3.2 Gbits/s
A-rate™ laser driver

Product specification
Supersedes data of 2001 Nov 16

2003 Apr 07

Philips
Semiconductors

PHILIPS

CONTENTS

1 FEATURES

1.1 General

- A-rate™[1] from 30 Mbits/s to 3.2 Gbits/s
- Bias current up to 100 mA
- Modulation current up to 100 mA
- Rise and fall times typical 80 ps
- Jitter below 20 ps (peak-to-peak value)
- Modulation output voltage up to 2 V dynamic range
- 1.2 V minimum voltage on the modulation output pin and 0.4 V minimum voltage on pin BIAS
- Retiming function via external clock with disable option
- Pulse width adjustment function with disable option
- Positive Emitter Coupled Logic (PECL) and LVPECL and Current-Mode Logic (CML) compatible data and clock inputs
- Internal common mode voltage available for AC-coupled data and clock inputs and for single-ended applications
- 3.3 V supply voltage
- DC-coupled laser for 3.3 V and 5 V laser supply.

1.2 Control features

- Average power loop control
- Direct setting of modulation current
- Optional direct setting of bias current.

1.3 Protection features

- Alarm function on operating current
- Alarm function on monitor current
- Enable function on bias and modulation currents
- Soft start on bias and modulation currents.

2 APPLICATIONS

- SDH/SONET optical transmission systems
- High current drivers for converters
- High current drivers for high frequencies.

3 GENERAL DESCRIPTION

The TZA3010B is a fully integrated laser driver for optical transmission systems with data rates up to 3.2 Gbits/s. The TZA3010B incorporates all the necessary control and protection functions for a laser driver application with very few external components required and low power dissipation.

The design is made in the Philips BiCMOS RF process and is available in a HBCC32 package. The TZA3010B is intended for use in an application with a DC-coupled laser diode for both 3.3 and 5 V laser supply voltages.

4 ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
TZA3010BVH	HBCC32	plastic, heatsink bottom chip carrier; 32 terminals; body 5 × 5 × 0.65 mm	SOT560-1

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(1) A-rate - is a trademark of Koninklijke Philips Electronics N.V.

5 BLOCK DIAGRAM

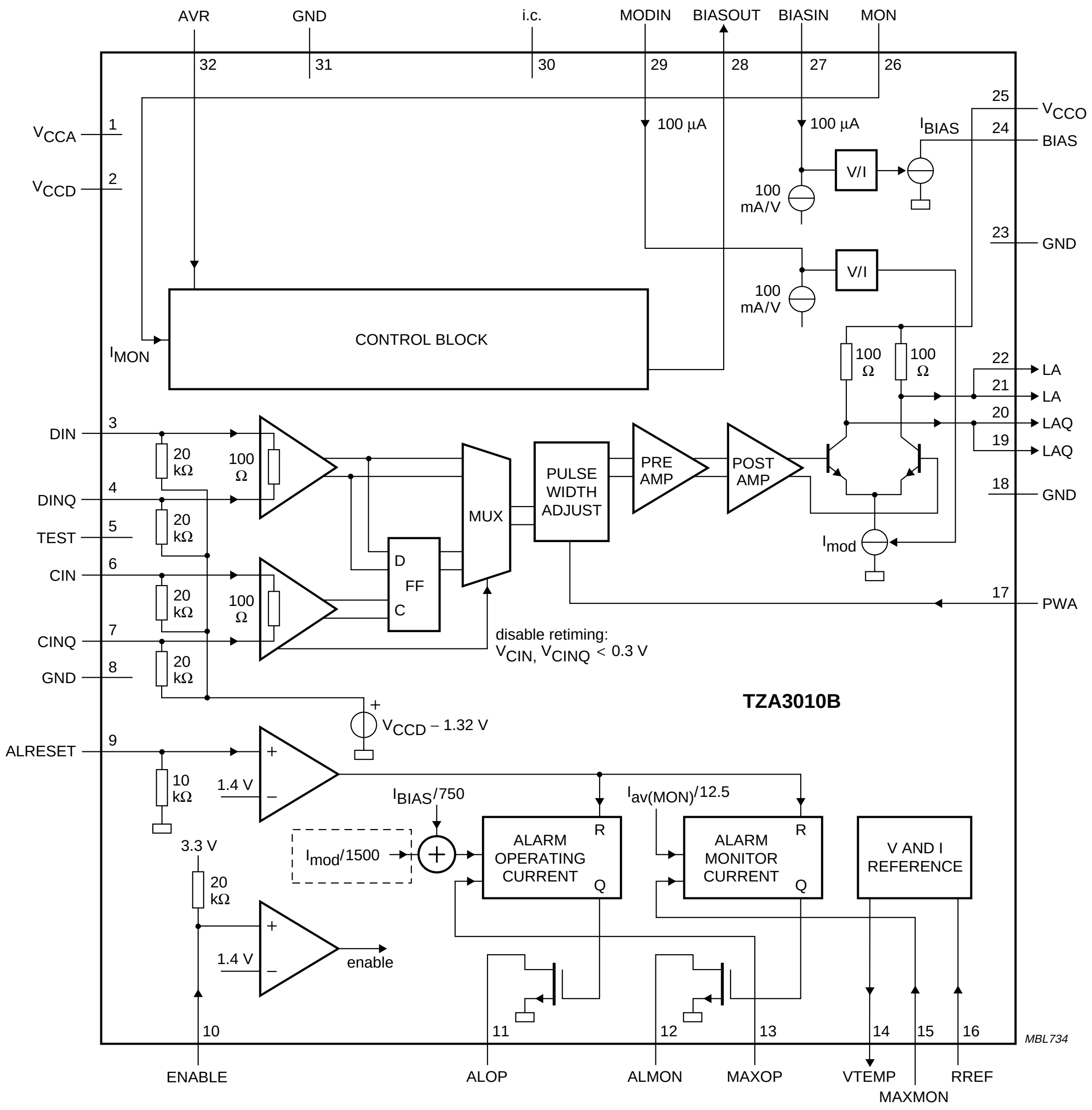


Fig.1 Block diagram.

6 PINNING

SYMBOL	PIN	DESCRIPTION
GND	die pad	common ground plane for V_{CCA}, V_{CCD}, V_{CCO}, RF and I/O; must be connected to GND
V_{CCA}	1	analog supply voltage
V_{CCD}	2	digital supply voltage
DIN	3	non-inverted data input (RF input)
DINQ	4	inverted data input (RF input)
TEST	5	test pin; must be connected to ground
CIN	6	non-inverted clock input (RF input)
CINQ	7	inverted clock input (RF input)
GND	8	ground
ALRESET	9	voltage input to reset the alarm outputs ALMON and ALOP
ENABLE	10	enable voltage input for modulation and bias current
ALOP	11	alarm output on operating current (open-drain)
ALMON	12	alarm output on monitor diode current (open-drain)
MAXOP	13	threshold level input for alarm on operating current
VTEMP	14	temperature dependent voltage output source
MAXMON	15	threshold level input for alarm on monitor diode current
RREF	16	reference current input; must be connected to ground with an accurate (1%) 10 kΩ resistor
PWA	17	pulse width adjustment input
GND	18	ground
LAQ	19	inverted laser modulation output (RF output); output for dummy load
LAQ	20	inverted laser modulation output (RF output); output for dummy load
LA	21	non-inverted laser modulation output (RF output); output for laser
LA	22	non-inverted laser modulation output (RF output); output for laser
GND	23	ground
BIAS	24	current source output for the laser bias current
V_{CCO}	25	supply voltage for the output stage and the laser diode
MON	26	current input for the monitor photodiode (RF input)
BIASIN	27	input voltage for the bias current setting
BIASOUT	28	output voltage of the control block for the bias current
MODIN	29	input voltage for the modulation current setting
i.c.	30	internally connected
GND	31	ground
AVR	32	input current for the optical average power level setting

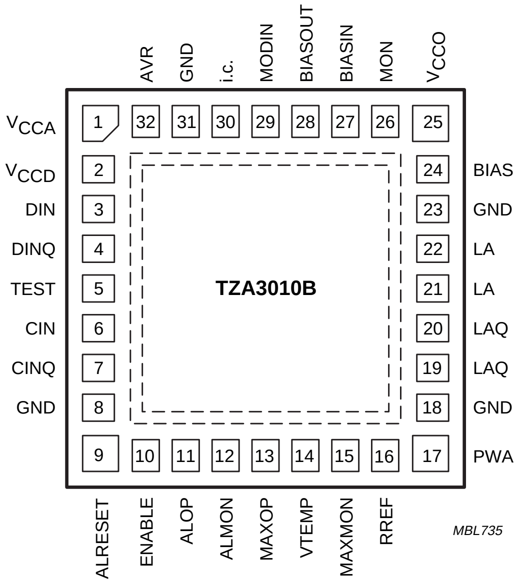


Fig.2 Pin configuration.

7 FUNCTIONAL DESCRIPTION

7.1 Data and clock input

The TZA3010B operates with differential Positive Emitter Coupled Logic (PECL) and Current-Mode Logic (CML) data and clock inputs with a voltage swing from 100 mV to 1 V (p-p). It is assumed that both the data and clock inputs carry a complementary signal with the specified peak-to-peak value (true differential excitation).

The circuit generates an internal common mode voltage for AC-coupled data and clock inputs and for single-ended applications.

If $V_{DIN} > V_{DINQ}$, the modulation current is sunk by pins LA and corresponds to an optical ‘one’ level of the laser.

7.2 Retiming

The retiming function synchronizes the data with the clock to improve the jitter performance. The data latch switches on the rising edge of the clock input. The retiming function is disabled when both clock inputs are below 0.3 V.

At start-up the initial polarity of the laser is unknown before the first rising edge of the clock input.

7.3 Pulse width adjustment

The on-duration of the laser current can be adjusted from −100 to +100 ps. The adjustment time is set by resistor R_{PWA}. The maximum allowable capacitive load on pin PWA is 100 pF. Pulse width adjustment is disabled when pin PWA is short-circuited to ground.

7.4 Modulator output stage

The output stage is a high-speed bipolar differential pair with typical rise and fall times of 80 ps and with a modulation current source of up to 100 mA when the pins LA are connected to V_{CCO}.

The modulation current switches between the LA and LAQ outputs. For a good RF performance the inactive branch carries a small amount of the modulation current.

Output LA is optimized for the laser allowing a 2 V dynamic range and a 1.2 V minimum voltage. Output LAQ is optimized for the dummy load.

The output stage is optimized for DC-coupled lasers.

Output BIAS is optimized for low voltage requirements (0.4 V minimum for a 3.3 V laser supply; 0.8 V minimum for a 5 V laser supply).

7.5 Average loop control

The average power control loop maintains a constant average power level of the monitor current over temperature and lifetime of the laser.

The average monitor current is programmable over a wide current range from 150 to 1100 μA by tuning the R_{AVR} setting resistor.

The maximum allowable capacitive load on pin AVR and pin BIASOUT is 100 pF.

The minimum capacitance on pin MON is 10 pF.

7.6 Direct current setting

The TZA3010B can also operate in open-loop mode with direct setting of the bias and modulation currents. The bias and modulation current sources are transconductance amplifiers and the output currents are determined by the BIASIN and MODIN voltages respectively. The bias current source has a bipolar output stage with minimum output capacitance for optimum RF performance.

7.7 Soft start

At power-up the bias and modulation current sources are released once the V_{CCA} voltage exceeds the 2.7 V level and the reference voltage has reached the correct value of 1.2 V.

The control loop starts with minimum bias and modulation current at power-up and when the device is enabled. The current levels increase until the MON input current matches the programmed average level.

7.8 Alarm functions

The TZA3010B features two alarm functions for the detection of excessive laser operating current and monitor diode current due to laser ageing, laser malfunctioning or a too high laser temperature. The alarm threshold levels are programmed by a resistor or a current source. For DC-coupled application, the operating current equals the bias current plus half of the modulation current.

7.9 Enable

A LOW level on the enable input disables the bias and modulation current sources: the laser is off. A HIGH level on the enable input or an open enable input switches both current sources on: the laser is operational.

7.10 Reference block

The reference voltage is derived from a band gap circuit and is available at pin RREF. An accurate (1%) 10 kΩ resistor has to be connected to pin RREF to provide the internal reference current. The maximum capacitive load on pin RREF is 100 pF.

The reference voltage on the setting pins (MAXOP, MAXMON, PWA and AVR) is buffered and derived from the band gap voltage.

The output voltage on pin VTEMP reflects the junction temperature of the TZA3010B, the temperature coefficient of pin VTEMP equals –2.2 mV/K.

8 LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134); all voltages are referenced to ground; positive currents flow into the IC.

SYMBOL	PARAMETER	CONDITION	MIN.	MAX.	UNIT
V_{CCD}	digital supply voltage		–0.5	+3.5	V
V_{CCA}	analog supply voltage		–0.5	+3.5	V
V_{CCO}	output stage supply voltage	3.3 V laser supply	–0.5	+3.5	V
		5 V laser supply	–0.5	+5.3	V
$V_{o(LA)}$	output voltage at pin LA	V_{CCO} = 3.3 V	0.8	4.1	V
		V_{CCO} = 5 V	1.2	4.5	V
$V_{o(LAQ)}$	output voltage at pin LAQ	V_{CCO} = 3.3 V	1.6	4.5	V
		V_{CCO} = 5 V	2.0	5.2	V
V_{BIAS}	bias voltage	V_{CCO} = 3.3 V	0.4	3.6	V
		V_{CCO} = 5 V	0.8	4.1	V
V_n	voltage on other input and output pins				V
	analog inputs and outputs		–0.5	V_{CCA} + 0.5	V
	digital inputs and outputs		–0.5	V_{CCD} + 0.5	V
I_n	input current on pins				
	MAXOP, MAXMON, RREF, PWA and AVR		–1.0	0	mA
	VTEMP and BIASOUT		–1.0	+1.0	mA
	ALOP, ALMON and MON		0	5.0	mA
T_{amb}	ambient temperature		–40	+85	°C
T_j	junction temperature		–40	+125	°C
T_{stg}	storage temperature		–65	+150	°C

9 THERMAL CHARACTERISTICS

In compliance with JEDEC standards JESD51-5 and JESD51-7.

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	4-layer Printed Circuit Board (PCB) in still air with 9 plated vias connected with the heatsink and the first ground plane in the PCB	35	K/W
		HBCC32 die pad soldered to PCB	60	K/W

10 DC CHARACTERISTICS

T_{amb} = –40 to +85 °C; $R_{th(j-a)}$ = 35 K/W; P_{tot} = 400 mW; V_{CCA} = 3.14 to 3.47 V; V_{CCD} = 3.14 to 3.47 V; V_{CCO} = 3.14 to 3.47 V; R_{AVR} = 7.5 kΩ; R_{MODIN} = 6.2 kΩ; R_{BIASIN} = 6.8 kΩ; R_{PWA} = 10 kΩ; R_{RREF} = 10 kΩ; R_{MAXMON} = 13 kΩ; R_{MAXOP} = 20 kΩ; positive currents flow into the IC; all voltages are referenced to ground; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supplies: pins V_{CCA}, V_{CCD} and V_{CCO}						
V_{CCA}	analog supply voltage		3.14	3.3	3.47	V
V_{CCD}	digital supply voltage		3.14	3.3	3.47	V
V_{CCO}	RF output supply voltage	3.3 V laser supply	3.14	3.3	3.47	V
		5 V laser supply	4.75	5	5.25	V
I_{CCA}	analog supply current		30	40	50	mA
I_{CCD}	digital supply current		35	45	55	mA
I_{CCO}	output stage supply current	pins LA and LAQ open-circuit				
		3.3 V laser supply	8	15	25	mA
		5 V laser supply	–	20	–	mA
P_{core}	core power dissipation	core excluding output currents $I_{o(LA)}$, $I_{o(LAQ)}$ and I_{BIAS}; PWA and retiming off	–	264	–	mW
P_{tot}	total power dissipation	V_{BIAS} = 3.3 V; I_{BIAS} = 20 mA; I_{mod} = 16 mA; note 1	330	400	500	mW
Data and clock inputs: pins DIN and CIN						
$V_{i(p-p)}$	input voltage swing (peak-to-peak value)	$V_{i(DIN)}$ = (V_{CCD} – 2 V) to $V_{CCD;}$ $V_{i(CIN)}$ = (V_{CCD} – 2 V) to V_{CCD}	100	–	1000	mV
$V_{int(cm)}$	internal common mode voltage	AC-coupled inputs	–	V_{CCD} – 1.32	–	V
V_{IO}	input offset voltage	note 2	–10	0	+10	mV
$Z_{i(dif)}$	differential input impedance		80	100	125	Ω
$Z_{i(cm)}$	common mode input impedance		8	10	13	kΩ
$V_{i(CIN)(dis)}$	input voltage for disabled retiming	V_{CIN} = V_{CINQ}	–	–	0.3	V
Average setting for average loop control						
PIN MON						
V_i	input voltage	I_{MON} = 50 to 2500 μA	0.9	1.1	1.3	V
$I_{av(low)}$	low average monitor current setting	I_{AVR} > –280 μA			150	μA
$I_{av(max)}$	maximum average monitor current setting	I_{AVR} = –15.0 μA	1200	1300	–	μA
ΔI_{av}	relative accuracy of average current	temperature and V_{CCA} variations; AVR = 550 μA	–10	–	+10	%
Z_i	input impedance	I_{MON} = 50 to 2500 μA	–	27	–	Ω

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
PIN AVR						
V_{ref}	reference voltage	I_{AVR} = –250 to –15 μA; C_{AVR} < 100 pF	1.15	1.20	1.25	V
I_{sink}	sink current		–280	–	–15	μA
Control loop bias output: pin BIASOUT						
I_{source}	source current	$V_{BIASOUT}$ = 0.5 to 1.5 V; $C_{BIASOUT}$ < 100 pF	–	–	–200	μA
I_{sink}	sink current	$V_{BIASOUT}$ = 0.5 to 1.5 V; $C_{BIASOUT}$ < 100 pF	200	–	–	μA
Bias current source: pins BIASIN and BIAS						
$g_{m(bias)}$	bias transconductance	V_{BIASIN} = 0.5 to 1.5 V				
		V_{BIAS} = V_{CCO} = 3.3 V	90	110	125	mA/V
		V_{BIAS} = 4.1 V; V_{CCO} = 5.0 V	95	110	130	mA/V
$I_{source(BIASIN)}$	source current at pin BIASIN	V_{BIASIN} = 0.5 to 1.5 V	–110	–100	–95	μA
$I_{BIAS(max)}$	maximum bias current	V_{BIASIN} = 1.8 V	100	–	–	mA
$I_{BIAS(min)}$	minimum bias current	V_{BIASIN} = 0 to 0.4 V	–	0.2	0.4	mA
$I_{BIAS(dis)}$	bias current at disable	V_{ENABLE} < 0.8 V	–	–	30	μA
V_{BIAS}	output voltage on pin BIAS	normal operation				
		V_{CCO} = 3.3 V	0.4	–	3.6	V
		V_{CCO} = 5 V	0.8	–	4.1	V
Modulation current source: pin MODIN						
$g_{m(mod)}$	modulation transconductance	V_{MODIN} = 0.5 to 1.5 V				
		V_{LA} = V_{LAQ} = V_{CCO} = 3.3 V	78	90	105	mA/V
		V_{LA} = V_{LAQ} = V_{CCO} = 4.5 V	80	95	110	mA/V
I_{source}	source current	V_{MODIN} = 0.5 to 1.5 V	–110	–100	–95	μA
Modulation current outputs: pins LA and LAQ						
$I_{o(max)(on)}$	maximum laser modulation output current at LA on	V_{MODIN} = 1.8 V; V_{LA} = V_{CCO} = 3.3 V; note 3	100	–	–	mA
$I_{o(min)(on)}$	minimum laser modulation output current at LA on	V_{MODIN} = 0 to 0.4 V; V_{LA} = V_{CCO} = 3.3 V; note 3	–	5	6	mA
$I_{o(min)(off)}$	minimum laser modulation output current at LA off	V_{LA} = V_{CCO} = 3.3 V; note 3				
		V_{MODIN} = 0.5 V	–	–	0.8	mA
		V_{MODIN} = 1.5 V	–	–	2	mA
Z_o	output impedance		80	100	125	Ω
$I_{o(dis)}$	non-inverted and inverted laser modulation output current at disable	V_{ENABLE} < 0.8 V	–	–	200	μA
$V_{o(min)}$	minimum output voltage	V_{CCO} = 3.3 V	1.2	–	–	V
		V_{CCO} = 5 V	1.6	–	–	V

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Enable function: pin ENABLE						
V_{IL}	LOW-level input voltage	bias and modulation currents disabled	–	–	0.8	V
V_{IH}	HIGH-level input voltage	bias and modulation currents enabled	2.0	–	–	V
$R_{pu(int)}$	internal pull-up resistance		16	20	30	kΩ
Alarm reset: pin ALRESET						
V_{IL}	LOW-level input voltage	no reset	–	–	0.8	V
V_{IH}	HIGH-level input voltage	reset	2.0	–	–	V
$R_{pd(int)}$	internal pull-down resistance		7	10	15	kΩ
Alarm operating current: pins MAXOP and ALOP						
$V_{ref(MAXOP)}$	reference voltage on pin MAXOP	I_{MAXOP} = 10 to 200 μA	1.15	1.2	1.25	V
N_{MAXOP}	ratio of $I_{oper(alarm)}$ and I_{MAXOP}	$I_{oper(alarm)}$ = 7.5 to 150 mA				
		V_{CCO} = 3.3 V	700	800	900	
		V_{CCO} = 5.0 V	750	850	950	
$V_{ALOP(L)}$	drain voltage at active alarm	I_{ALOP} = 500 μA	0	–	0.4	V
Alarm monitor current: pins MAXMON and ALMON						
$V_{ref(MAXMON)}$	reference voltage on pin MAXMON	I_{MAXMON} = 10 to 200 μA	1.15	1.2	1.25	V
N_{MAXMON}	ratio of $I_{MON(alarm)}$ and I_{MAXMON}	$I_{MON(alarm)}$ = 150 to 3000 μA	10	15	20	
$V_{ALMON(L)}$	drain voltage at active alarm	I_{ALMON} = 500 μA	0	–	0.4	V
Reference block: pins RREF and VTEMP						
V_{RREF}	reference voltage	R_{RREF} = 10 kΩ (1%); C_{RREF} < 100 pF	1.15	1.20	1.25	V
V_{VTEMP}	temperature dependent voltage	T_j = 25 °C; C_{VTEMP} < 2 nF; note 4	1.15	1.20	1.25	V
TC_{VTEMP}	temperature coefficient of V_{VTEMP}	T_j = –25 to +125 °C; note 4	–	–2.2	–	mV/K
$I_{source(VTEMP)}$	source current of pin VTEMP		–	–	–1	mA
$I_{sink(VTEMP)}$	sink current of pin VTEMP		1	–	–	mA

Notes

1. The total power dissipation P_{tot} is calculated with $V_{BIAS} = V_{CCO}$ = 3.3 V and I_{BIAS} = 20 mA. In the application V_{BIAS} will be V_{CCO} minus the laser diode voltage which results in a lower total power dissipation.
2. The specification of the offset voltage is guaranteed by design.

3. The relation between the sink current $I_{o(LA)}$ and the modulation current I_{mod} is: $I_{o(LA)} = I_{mod} \times \frac{100}{100 + Z_{L(LA)}}$ where $Z_{L(LA)}$ is the external load on pin LA. The voltage on pin MODIN programmes the modulation current I_{mod}. This current is divided between $Z_{L(LA)}$ and the 100 Ω internal resistor connected to pins LA. When the modulation current is programmed to 100 mA, a typical $Z_{L(LA)}$ of 25 Ω will result in an $I_{o(LA)}$ current of 80 mA, while 20 mA flows via the internal resistor. This corresponds to a voltage swing of 2 V on the real application load.
4. $V_{VTEMP} = 1.31\ V + TC_{VTEMP}$ and $T_j = T_{amb} + P_{tot} \times R_{th(j\text{-}a)}$.

11 AC CHARACTERISTICS

T_{amb} = –40 to +85 °C; $R_{th(j\text{-}a)}$ = 35 K/W; P_{tot} = 400 mW; V_{CCA} = 3.14 to 3.47 V; V_{CCD} = 3.14 to 3.47 V; V_{CCO} = 3.14 to 3.47 V; R_{AVR} = 7.5 kΩ; R_{MODIN} = 6.2 kΩ; R_{BIASIN} = 6.8 kΩ; R_{PWA} = 10 kΩ; R_{RREF} = 10 kΩ; R_{MAXMON} = 13 kΩ; R_{MAXOP} = 20 kΩ; positive currents flow into the IC; all voltages are referenced to ground; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
RF path						
BR	bit rate		0.03	–	3.2	Gbits/s
$J_{LA(p\text{-}p)}$	output jitter of pin LA (peak-to-peak value)	R_L = 25 Ω; note 1	–	–	20	ps
t_r	rise time of voltage on pin LA	20% to 80%; R_L = 25 Ω; I_{mod} = 17 mA; notes 2 and 3	70	85	110	ps
t_f	fall time of voltage on pin LA	80% to 20%; R_L = 25 Ω; I_{mod} = 17 mA; notes 2 and 3	50	70	100	ps
$t_{su(D)}$	data input set-up time		60	–	–	ps
$t_{h(D)}$	data input hold time		60	–	–	ps
$t_{en(start)}$	start-up time at enable	direct current setting	–	–	1	µs
Current control						
tc_{int}	internal time constant	C_{MON} > 10 pF	30	–	–	ms
Pulse width adjustment: pins LA						
$t_{PWA(min)}$	minimum pulse width adjustment	R_{PWA} = 6.7 kΩ; C_{PWA} < 100 pF	–	–	–100	ps
t_{PWA}	pulse width adjustment	R_{PWA} = 10 kΩ; C_{PWA} < 100 pF	–	0	–	ps
$t_{PWA(max)}$	maximum pulse width adjustment	R_{PWA} = 20 kΩ; C_{PWA} < 100 pF	80	100	–	ps

Notes

1. The output jitter specification is guaranteed by design.
2. With a 25 Ω load on pins LA: I_{LA} = 14 mA when I_{mod} = 17 mA.
3. For high modulation current, t_r and t_f are impacted by total inductance between the LA pins and the laser connection.

12 APPLICATION INFORMATION

12.1 Design equations

12.1.1 BIAS AND MODULATION CURRENTS

The bias and modulation currents are determined by the voltages on pins BIASIN and MODIN. For average loop control the BIASIN voltage is applied by pin BIASOUT and the MODIN voltage is applied by an external voltage source or an external resistor R_{MODIN}.

For direct setting of bias and the modulation current, the BIASIN and MODIN voltages have to be applied by external voltage sources or by R_{BIASIN} and R_{MODIN} external resistors connected on pins BIASIN and MODIN:

$$I_{BIAS} = (R_{BIASIN} \times 100\ \mu A - 0.5\ V) \times g_{m(bias)}$$

$$I_{mod} = (R_{MODIN} \times 100\ \mu A - 0.5\ V) \times g_{m(mod)} + 5\ mA$$

The bias and modulation current sources operate with an input voltage range from 0.5 to 1.5 V. The output current is at its minimum level for an input voltage below 0.4 V; see Figs 3 and 4.

The bias and modulation current sources are temperature compensated and the adjusted current level remains stable over the temperature range.

The bias and modulation currents increase with increasing resistor values for R_{BIASIN} and R_{MODIN} respectively, this allows resistor tuning to start at a minimum current level.

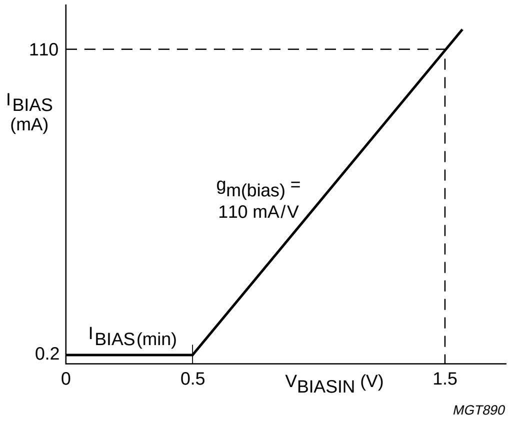
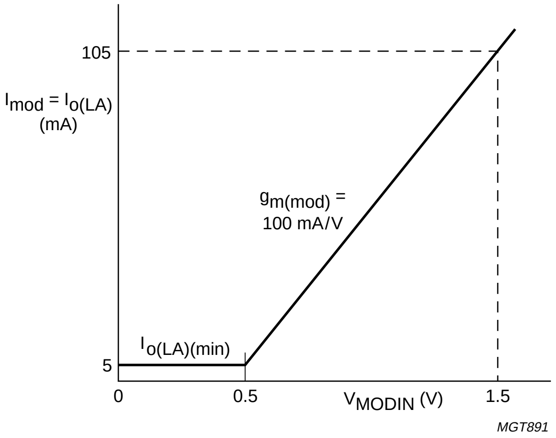


Fig.3 Bias current as a function of BIASIN voltage.



LA current when LA output is on.
$V_{0(LA)}$ = VCCO

Fig.4 Modulation current as a function of MODIN voltage.

12.1.2 AVERAGE MONITOR CURRENT

The average monitor current $I_{av(MON)}$ is determined by the source current (I_{AVR}) of pin AVR. The current can be sunk by an external current source or by an external resistor (R_{AVR}) connected to ground:

$$I_{av(MON)} = 1580\ \mu A - 5.26 \times \frac{V_{AVR}}{R_{AVR}}$$

The average monitor current increases with a decreasing I_{AVR} or increasing R_{AVR}, this allows resistor tuning of R_{AVR} to start at minimum I_{AVR} current level (see Fig.5).

The formula used to program AVR is valid for typical conditions; tuning is necessary to achieve good absolute accuracy of AVR value.

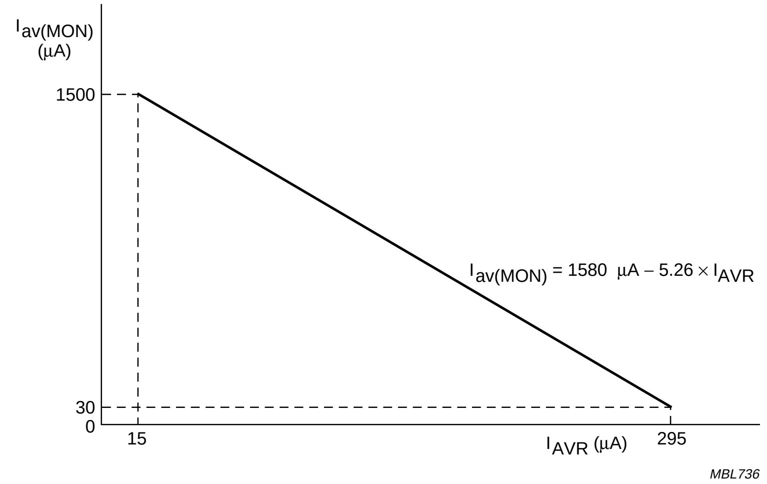


Fig.5 Average monitor current as a function of I_{AVR}.

12.1.3 ALARM OPERATING CURRENT

The alarm threshold $I_{oper(alarm)}$ on the operating current is determined by the source current I_{MAXOP} of pin MAXOP. The current range for I_{MAXOP} is from 10 to 200 μA which corresponds with an $I_{oper(alarm)}$ from 7.5 to 150 mA. The I_{MAXOP} current can be sunk by an external current source or by connecting R_{MAXOP} to ground:

$$I_{oper(alarm)} = N_{MAXOP} \times \frac{V_{MAXOP}}{R_{MAXOP}}$$

The operating current equals the bias current plus half of the modulation current:

$$I_{oper} = I_{BIAS} + \frac{I_{mod}}{2}$$

12.1.4 ALARM MONITOR CURRENT

The alarm threshold $I_{MON(alarm)}$ on the monitor current is determined by the source current I_{MAXMON} of pin MAXMON. The current range for I_{MAXMON} is from 10 to 200 μA which corresponds with an $I_{MON(alarm)}$ from 150 to 3000 μA. The I_{MAXMON} current can be sunk by an external current source or by connecting R_{MAXMON} to ground:

$$I_{MON(alarm)} = N_{MAXMON} \times \frac{V_{MAXMON}}{R_{MAXMON}}$$

12.1.5 PULSE WIDTH ADJUSTMENT

The pulse width adjustment time is determined by the value of resistor R_{PWA}, as illustrated in Fig.6.

$$t_{PWA} = \frac{R_{PWA} - 10\ k\Omega}{R_{PWA}} \times 200\,ps$$

The t_{PWA} range is from −100 to +100 ps which corresponds with a R_{PWA} range between a minimum resistance of 6.7 kΩ and a maximum resistance of 20 kΩ. The PWA function is disabled when the PWA input is short-circuited to ground; t_{PWA} equals 0 ps for a disabled PWA function.

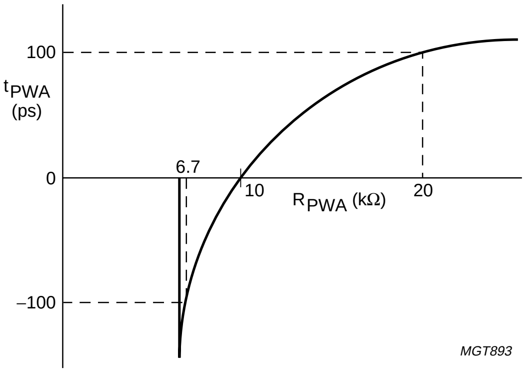


Fig.6 Pulse width adjustment.

12.2 Application with average loop control

A simplified application using the TZA3010B and a laser is illustrated in Fig.7. The average power level is determined by the resistor R_{AVR}. The average loop controls the bias current and pin BIASOUT is connected to pin BIASIN. The modulation current is determined by the MODIN input voltage which is generated by the resistor R_{MODIN} and the 100 µA source current of pin MODIN.

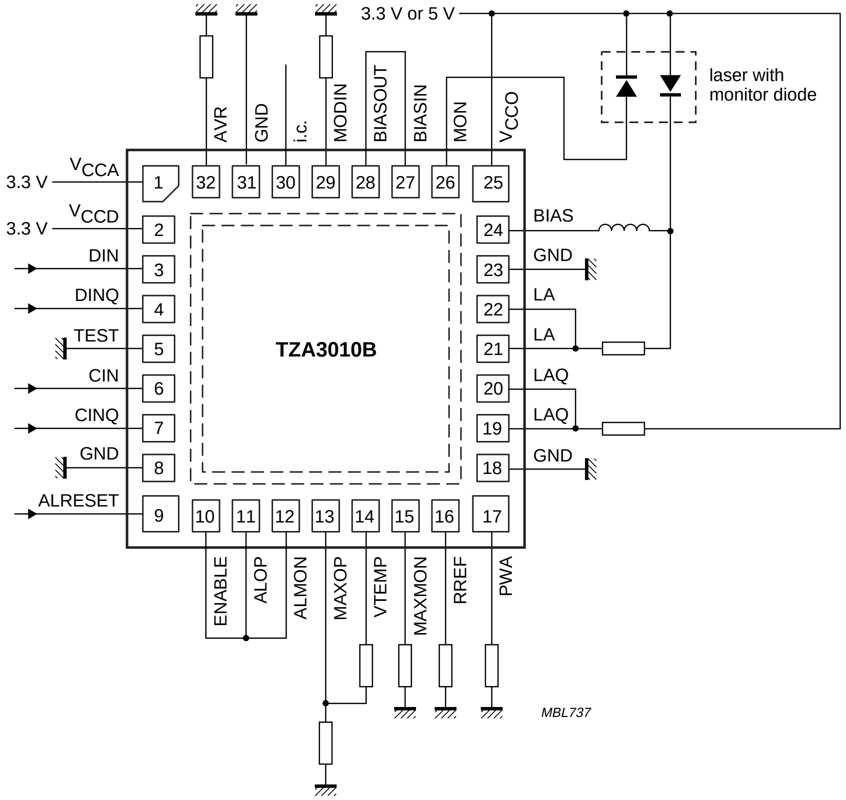
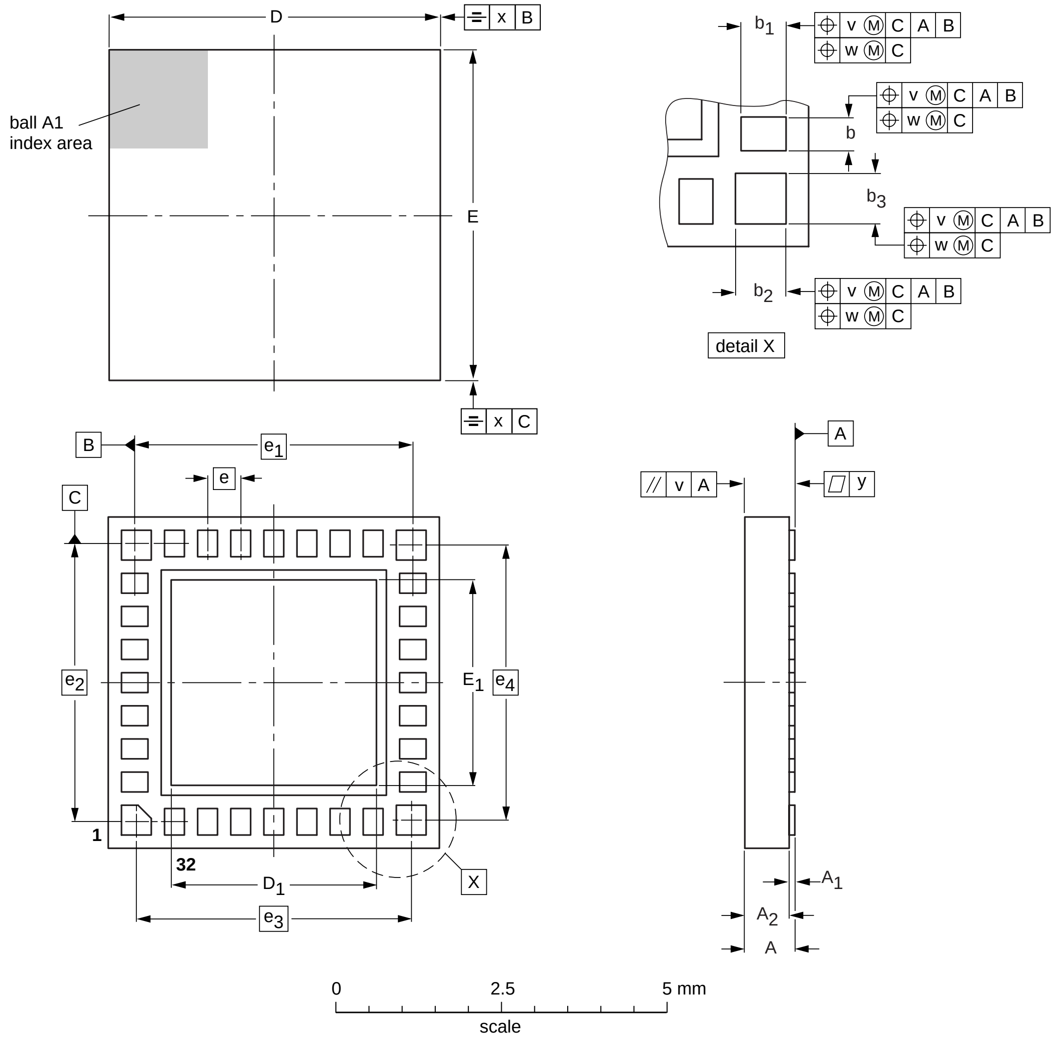


Fig.7 Application with laser and average loop control.

13 PACKAGE OUTLINE

HBCC32: plastic thermal enhanced bottom chip carrier; 32 terminals; body 5 x 5 x 0.65 mm SOT560-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A_1	A_2	b	b_1	b_2	b_3	D	D_1	E	E_1	e	e_1	e_2	e_3	e_4	v	w	x	y
mm	0.8	0.10 0.05	0.7 0.6	0.35 0.20	0.5 0.3	0.50 0.35	0.50 0.35	5.1 4.9	3.2 3.0	5.1 4.9	3.2 3.0	0.5	4.2	4.2	4.15	4.15	0.2	0.15	0.15	0.05

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT560-1		MO-217				~~00-02-01~~ 03-03-12

14 SOLDERING

14.1 Introduction to soldering surface mount packages

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our *“Data Handbook IC26; Integrated Circuit Packages”* (document order number 9398 652 90011).

There is no soldering method that is ideal for all surface mount IC packages. Wave soldering can still be used for certain surface mount ICs, but it is not suitable for fine pitch SMDs. In these situations reflow soldering is recommended.

14.2 Reflow soldering

Reflow soldering requires solder paste (a suspension of fine solder particles, flux and binding agent) to be applied to the printed-circuit board by screen printing, stencilling or pressure-syringe dispensing before package placement.

Several methods exist for reflowing; for example, convection or convection/infrared heating in a conveyor type oven. Throughput times (preheating, soldering and cooling) vary between 100 and 200 seconds depending on heating method.

Typical reflow peak temperatures range from 215 to 250 °C. The top-surface temperature of the packages should preferably be kept:

- below 220 °C for all the BGA packages and packages with a thickness ≥ 2.5mm and packages with a thickness <2.5 mm and a volume ≥350 mm^3 so called thick/large packages
- below 235 °C for packages with a thickness <2.5 mm and a volume <350 mm^3 so called small/thin packages.

14.3 Wave soldering

Conventional single wave soldering is not recommended for surface mount devices (SMDs) or printed-circuit boards with a high component density, as solder bridging and non-wetting can present major problems.

To overcome these problems the double-wave soldering method was specifically developed.

If wave soldering is used the following conditions must be observed for optimal results:

- Use a double-wave soldering method comprising a turbulent wave with high upward pressure followed by a smooth laminar wave.
- For packages with leads on two sides and a pitch (e):
  - larger than or equal to 1.27 mm, the footprint longitudinal axis is **preferred** to be parallel to the transport direction of the printed-circuit board;
  - smaller than 1.27 mm, the footprint longitudinal axis **must** be parallel to the transport direction of the printed-circuit board.

  The footprint must incorporate solder thieves at the downstream end.
- For packages with leads on four sides, the footprint must be placed at a 45° angle to the transport direction of the printed-circuit board. The footprint must incorporate solder thieves downstream and at the side corners.

During placement and before soldering, the package must be fixed with a droplet of adhesive. The adhesive can be applied by screen printing, pin transfer or syringe dispensing. The package can be soldered after the adhesive is cured.

Typical dwell time is 4 seconds at 250 °C.
A mildly-activated flux will eliminate the need for removal of corrosive residues in most applications.

14.4 Manual soldering

Fix the component by first soldering two diagonally-opposite end leads. Use a low voltage (24 V or less) soldering iron applied to the flat part of the lead. Contact time must be limited to 10 seconds at up to 300 °C.

When using a dedicated tool, all other leads can be soldered in one operation within 2 to 5 seconds between 270 and 320 °C.

14.5 Suitability of surface mount IC packages for wave and reflow soldering methods

PACKAGE(1)	SOLDERING METHOD	
	WAVE	**REFLOW(2)**
BGA, LBGA, LFBGA, SQFP, TFBGA, VFBGA	not suitable	suitable
DHVQFN, HBCC, HBGA, HLQFP, HSQFP, HSOP, HTQFP, HTSSOP, HVQFN, HVSON, SMS	not suitable(3)	suitable
PLCC(4), SO, SOJ	suitable	suitable
LQFP, QFP, TQFP	not recommended(4)(5)	suitable
SSOP, TSSOP, VSO, VSSOP	not recommended(6)	suitable

Notes

1. For more detailed information on the BGA packages refer to the *"(LF)BGA Application Note*" (AN01026); order a copy from your Philips Semiconductors sales office.
2. All surface mount (SMD) packages are moisture sensitive. Depending upon the moisture content, the maximum temperature (with respect to time) and body size of the package, there is a risk that internal or external package cracks may occur due to vaporization of the moisture in them (the so called popcorn effect). For details, refer to the Drypack information in the *"Data Handbook IC26; Integrated Circuit Packages; Section: Packing Methods"*.
3. These packages are not suitable for wave soldering. On versions with the heatsink on the bottom side, the solder cannot penetrate between the printed-circuit board and the heatsink. On versions with the heatsink on the top side, the solder might be deposited on the heatsink surface.
4. If wave soldering is considered, then the package must be placed at a 45° angle to the solder wave direction. The package footprint must incorporate solder thieves downstream and at the side corners.
5. Wave soldering is suitable for LQFP, TQFP and QFP packages with a pitch (e) larger than 0.8 mm; it is definitely not suitable for packages with a pitch (e) equal to or smaller than 0.65 mm.
6. Wave soldering is suitable for SSOP, TSSOP, VSO and VSSOP packages with a pitch (e) equal to or larger than 0.65 mm; it is definitely not suitable for packages with a pitch (e) equal to or smaller than 0.5 mm.

15 DATA SHEET STATUS

LEVEL	DATA SHEET STATUS(1)	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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16 DEFINITIONS

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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