## TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic <br> TC83230-0015

## TC83230-0015: Single-Chip CMOS LSI for Calculators with Printers (applicable printer heads: M31/M31A manufactured by EPSON)

The TC83230-0015 LSI is a single-chip CMOS LSI for use in calculators with printers.

It integrates I/O logic circuits necessary to configure a calculator with 10 - or 12 -digit display, two-memory function, two-tax function, serial printer used to print calculation results, oscillator, and LCD drivers.

## Features

## Operational Features

- Print: 10 or 12 digits of data. (including decimal point.)


Weight: 1.52 g (typ.)

1 digit of floating minus sign, 1 digit of operational
symbol.
1-color printing (black).

- Display: 10 or 12 digits of data. (including punctuation in each digit.)

1 digit of floating minus sign, memory load, error symbol, grand total memory load, 3 digits of commas.

- Decimal output: Decimal set lock key controls output format. Fixed decimal setting ("0", "1", "2", "3", " 4 ", " 6 "), full floating decimal, ADD mode and ADD2 mode.
- Key-input buffer: 12 words
- Operation methods: Addition and subtraction: By ARITHMETIC operation

Multiplication and division: By algebraic operation

- Function: Four function, repeat multiplication and division, mixed calculation, percentage calculation, percent discount and add-on calculation, memory calculation, delta percent calculation, add-mode calculation, mark-up/down calculation, total calculation, constant calculation, tax calculation Two-key rollover.
- Leading zero suppression


## Protection

(1) In the overflow condition, all key except "C", "C/CE", "CE", "Feed", " $\rightarrow$ " key are inoperative.
(2) Key chatter protection.

## Auto-Clear at Power On

Auto-clear functions by connecting a capacitor to the RESET pin.

## Pin Assignment (top view)

QFP80

TOSHIBA
TC83230-0015

## Connection of LCD

## Segment



## Common



## Key Connection



Touch Key


Lock Key

Touch Key Select


ON Key

## Specification of Calculator

## Operation Specifications

(1) Operations depending on key types and modes

- Touch key

| Key Name | CAL Mode |  | Tax Set Mode (SETI/II key is on) |  |
| :---: | :---: | :---: | :---: | :---: |
| Mode Switch | Touch Key Mode | Lock Key Mode | Touch Key Mode | Lock Key Mode |
| C | Operates as clear key | Operates as clear key | Clears input data | Clears input data |
| CE | Operates as clear entry key | Operates as clear entry key | Clears input data | Clears input data |
| C/CE | Operates as clear or clear entry key | Operates as clear or clear entry key | Clears input data | Clears input data |
| Numeral | Numeral Key-inputs numerals | Numeral Key-inputs numerals | Inputs numerals | Inputs numerals |
| OFF | Operates as off key | - | Unused | Unused |
| - | Key-inputs decimal points | Key-inputs decimal points | Key-inputs decimal points | Key-inputs decimal points |
| $\begin{aligned} & * \\ & * \end{aligned}$ | Operates as total or sub-total key | Operates as total or sub-total key | Unused | Unused |
| $\begin{aligned} & +,- \\ & \times, \end{aligned}$ | Operates as four-function key | Operates as four-function key | Unused | Unused |
| = | Operates as = key | Operates as = key | Unused | Unused |
| P/NP | Switches print or non-print | - | Unused | Unused |
| RND | Switches round-off and round-up | - | Unused | Unused |
| DEC | Switches decimal points | - | Unused | Unused |
| \% | Operates as \% key | Operates as \% key | Unused | Unused |
| $\Delta \%$ | Operates as delta percentage calculation key | Operates as delta percentage calculation key | Unused | Unused |
| MU/D | Operates as mark-up/down key | Operates as mark-up/down key | Unused | Unused |
| IC/AVE | Operates as item count key or average key | Operates as item count key or average key | Unused | Unused |
| \#/P | Operates as non-add-print key for left-justified printing | Operates as non-add-print key for left-justified printing | Unused | Unused |
| $\rightarrow$ | Operates as right-shift key | Operates as right-shift key | Operates as right-shift key | Operates as right-shift key |
| +/- | Operates as sign change key | Operates as sign change key | Unused | Unused |
| MI*, MII*, <br> Mİ, MIIऽ, <br> MI-, MII-, <br> MI+, MII+ | Operates as memory function key | Operates as memory function key | Unused | Unused |
| -TAXI/II | Operates as -TAXI/II key | Operates as -TAXI/II key | Unused | Unused |
| +TAXI/II | Operates as +TAXI/II key | Operates as +TAXI/II key | Unused | Unused |
| $\Sigma$ | Operates as $\Sigma$ key | - | Unused | Unused |
| IC MOD | Operates as IC-mode key | - | Unused | Unused |


| Key Name | CAL Mode |  | Tax Set Mode (SETI/II key is on) |  |
| :---: | :---: | :---: | :--- | :--- |
| Mode Switch | Touch Key Mode | Lock Key Mode | Touch Key Mode | Lock Key Mode |
| GT MOD | Operates as GT-mode or <br> non-GT mode key | - | Unused | Unused |
| $\mathrm{GT}_{*}^{\diamond}$ | Operates as GT key | Operates as GT key | Unused | Unused |

- Lock key

| Key Name | CAL Mode |  | Tax Set Mode (SETI/II key is on) |  |
| :---: | :---: | :---: | :---: | :---: |
| Mode Switch | Touch Key Mode | Lock Key Mode | Touch Key Mode | Lock Key Mode |
| 0, 1, 2, 3, 4, 6, A, AII | - | Switches decimal points | Unused | Unused |
| CUT, UP | - | Switches round-off and round-up | Unused | Unused |
| IC $\pm$, IC+ | - | Operates as IC $\pm$ IIC+ key | Unused | Unused |
| $\Sigma$ | - | Operates as $\Sigma$ key | Unused | Unused |
| GT | - | Switches GT-mode or non-GT mode | Unused | Unused |
| FEED | Operates as paper feed key | Operates as paper feed key | Operates as paper feed key | Operates as paper feed key |
| $\mathrm{P} / \overline{\mathrm{NP}}$ | - | Switches print or non-print | Unused | Unused |
| $\bar{T} / \mathrm{N}$ <br> (Note 1) | Selects lock key mode or touch key mode. |  |  |  |
| $2 \text { TAX }$ <br> (Note 1) | Selects single tax mode or double tax mode. |  |  |  |
| 2 M <br> (Note 1) | Selects single memory mode or double memory mode. |  |  |  |

Note 1: Can switch modes only with the reset key.
(2) Explanation of function
[0~9]..................... Keys in numbers from 0 to 9, 00, and 000. If the number of displays digits exceeds [00, 000] $\quad 10$ or 12 key entry is invalid.
$[\cdot]$.........................If this key is pressed after a key operation except data entry, the displays is cleared and entry of [•] is stored in memory. The decimal point is shifted for subsequent data entry. If the $[\cdot]$ key is pressed during data entry, displays does not change.
$[+,-] \ldots \ldots \ldots \ldots \ldots \ldots . . . . . . . . . .$. Add or subtract operation data and displays the result. The decimal point is floating except when A mode is specified. Addition or subtraction can be performed repeatedly.
If these key are pressed in multiplication/division mode or in constant calculation mode, add or subtract displays data to addition/subtraction registers, then displays the result. At this time, in the operation mode multiplicand or divisor do not change.
These keys increment or decrement the item counter. In the following operation mode, the operations are executed, and the results are printed and displayed. At that time, addition or subtraction using the addition/subtraction register is not executed.

1) Percent discount/add-on calculation

$$
\begin{array}{r}
\mathrm{a} \times \mathrm{b} \%+\ldots . . \mathrm{a}+(\mathrm{ab} / 100) \\
\mathrm{c} \%+\ldots . . \mathrm{a}+(\mathrm{ac} / 100) \\
\mathrm{a} \times \mathrm{b} \%-\ldots . \mathrm{a}-(\mathrm{ab} / 100) \\
\mathrm{c} \%-\ldots . . \mathrm{a}-(\mathrm{ac} / 100)
\end{array}
$$

Percent discount/add-on with constants are calculated as above.
[ $]$ ]......................... Prints and displays the intermediate result in addition/subtraction register. In item count mode, prints the contents of the item counter before the calculation result printing. Contents of data register or stored arithmetic instruction are not changed.
[*] .......................... Print and displays the result in addition/subtraction register. Automatically feeds paper one line. In item count mode, the contents of the item counter are printed before the calculation result printing.
After this key operation, the contents of the addition/subtraction register are cleared. The contents of the item counter are cleared at the first addition/subtraction in next step. The contents of the data register or stored arithmetic instruction are not changed. When GT mode is specified, the result of addition/subtraction is added to the GT memory.
$\lceil\mathrm{MI}+, \mathrm{MII}+\ldots \ldots . . . . .$. If the arithmetic instruction is not stored or if the mode is constantan
MI-, MIImode, first prints the displays contents after rounding to the specified number of decimal places, performs addition/subtraction using the data in memory, then stores the result in memory. If the multiplication/division instruction is stored, executes the arithmetic instruction, rounds the result to the specified number of decimal places, prints and displays the result, adds/subtracts with the data in memory, then stores the result to memory.
At that time, the multiplicand or divisor is stored together with the mode, constant calculation mode. When this key is pressed immediately after the $[\times]$ or $[\mathrm{MI}+$, MII+, MI-, MII-] key, operation is the same as that for the [=] key; that is, adds/subtracts using data in memory. This key operation increments or decrements the item counter for memory.
[MI॰, MII॰] $\qquad$ Prints or displays the intermediate result of memory calculation. In item count mode, prints the contents of the item counter for memory before the calculation result printing. Contents of the data register or stored arithmetic instruction are not changed.
[MI*, MII*]............ Prints and displays the result of memory calculation and automatically feeds paper one line. In item count mode, prints the contents of the item counter for memory before the calculation result printing. After the [MI*, MII*] key operation, the contents of memory and the contents of the item counter for memory are cleared. Contents of the data register or stored arithmetic instruction are not changed.
$[\times, \div]$ $\qquad$ If the multiplication or division instruction is stored in memory, prints the operators, performs the operations and displays the results while simultaneously storing a new arithmetic instruction in memory. The decimal point for the result is floating. If the $[x]$ or $[\div]$ key is pressed in constant calculation mode, prints the displayed numeric value without performing an operation and stores a new multiplication/division instruction in memory.
[=].......................... Executes a stored multiplication/division instruction, rounds the result to the specified number of decimal places, prints and displays the result, then automatically feeds the paper one line. Stores the multiplicand or divisor together with constant calculation mode in memory. If an instruction is not stored in memory, no operation is performed and the previous state is held. Pressing the [=] key immediately after the [x] or [ $\div \cdot]$ key performs the following operation.

$$
\begin{aligned}
& \mathrm{a} \times=\ldots \ldots . \mathrm{aa} \\
& \mathrm{a} \div=\ldots \ldots .1
\end{aligned}
$$

[\%].........................If an arithmetic instruction is stored in memory, performs percentage calculation, rounds the result to the specified number of decimal places, prints and displays the result. Stores the multiplicand/divisor together with constant calculation mode in memory. If a percentage calculation for multiplication is performed, percent discount/add-on calculation can be done by using the [+] or [-] key. At that time, addition/subtraction using the addition/subtraction register is not performed. If an arithmetic instruction is not stored in memory, no operation is performed and the previous states is held. Pressing the [\%] key immediately after the [x] or [ $\div$ ] key performs the following operation.
$a \times \%=\ldots a / 100$
$a \div \%=\ldots 100$
\% key operation example: percent discount/add-on calculation

$$
\mathrm{a} \times \mathrm{b} \% \ldots . . \mathrm{ab} / 100
$$

$+\ldots . . . . . . . . . a+(a b / 100)$
c\% ..........ac/100
$+\ldots . . . . . . . . . a+(a c / 100)$
$\mathrm{a} \times \mathrm{b} \% \ldots . \mathrm{ab} / 100$
-.............a - (ab/100)
c\% ..........ac/100
-.............a - (ac/100)
[MU/D] $\qquad$ If a multiplication/division instruction is stored in memory, cancels the data. The decimal point for the result is floating. MU/D key operation example:

| $\mathrm{aMU} / \mathrm{Db}=\ldots \ldots \ldots \ldots \ldots . . \mathrm{a} /(1-(\mathrm{b} / 100))-\mathrm{a}$ | (prints profit) |
| :---: | :--- |
| $\ldots \ldots \ldots \ldots \ldots . \mathrm{a} /(1-(\mathrm{b} / 100))$ | (mark-up) |
| $\mathrm{c}=\ldots \ldots \ldots \ldots \ldots . \mathrm{a} /(1-(\mathrm{c} / 100))-\mathrm{a}$ | (prints profit) |
| $\ldots \ldots \ldots \ldots \ldots . \mathrm{a} /(1-(\mathrm{c} / 100))$ | (mark-up) |
| $\mathrm{aMU} / \mathrm{Db}+/-=\ldots \ldots \ldots . \mathrm{a} /(1+(\mathrm{b} / 100))-\mathrm{a}$ | (prints profit) |
| $\ldots \ldots \ldots . \mathrm{a} /(1+(\mathrm{b} / 100))$ | (mark-down) |
| $\mathrm{c}+/-=\ldots \ldots \ldots . \mathrm{a} /(1+(\mathrm{c} / 100))-\mathrm{a}$ | (prints profit) |
| $\ldots \ldots \ldots . \mathrm{a} /(1+(\mathrm{c} / 100))$ | (mark-down) |

[ $\Delta \%$ ] $\qquad$ If a multiplication/division instruction is memorized, cancels the data. $\Delta \%$ key operation example:

| $\ldots . . . .(b-a) /\|a\|$ | (prints difference) |
| :---: | :---: |
| c - a | (change delta percent) |
| ( $(\mathrm{c}-\mathrm{a}) / \mathrm{a}$ \| | (prints difference) |
| $a \Delta \% \mathrm{~b}+/-=\ldots \ldots \ldots . . . . . . .-(b+a)$ | (change delta percent) |
| $-(\mathrm{b}+\mathrm{a}) / \mathrm{\|a\|}$ | (prints difference) |
| +/- = .............. -( $\mathrm{c}+\mathrm{a}$ ) | (change delta percent) |
| $-(c+a) /\|a\|$ | (prints difference) |

[+/-]....................... Inverts sign of the displayed number at key entry.
$[\rightarrow]$........................ Shifts the contents of the displays to the right by one digit at key entry. For an estimation calculation error, cancels the error.
$\left[\mathrm{GT}_{*}^{\diamond}\right] \ldots . . . . . . . . . . . . .$. Calls the contents of GT memory. If the key is pressed once, calls the contents of GT memory, but does not change current state. If the key is pressed twice, calls the contents of GT memory and clears them.
[C] $\qquad$ Cancels all arithmetic instructions and errors, clears the contents of all the registers except the memory register, and prints 0.C.
[CE] ..................... If pressed at key entry, clears only the contents of the displays; does not change the stored arithmetic instruction or the contents of the data register. Invalid if pressed after one of the following keys: [C] [x] [ $\div$ ] [+] [-] [=] [\%] [ $\Delta \%$ ] [MI+, MII +$]$ [MI-, MII-] [MI॰, MII॰] [MI*, MII*] [MU/D] [IC/AVE]. The result of pressing the [CE] key after the [\#/P] key depends on the state before the keys were pressed.
[IC+]...................... Selects item count mode.
$[\mathrm{IC} \pm] \quad \mathrm{IC}+\ldots . . . .$. Counts up by the [+] or [-] key.
IC $\pm . . . . . . .$. Counts up by the [+] key, down by the [-] key.
[ $\Sigma$ ] ........................ If an operation is performed by the [=] or [\%] key in auto accumulation calculation mode, adds the operation result to the addition/subtraction register and increments the item counter.
[C/E] .....................If pressed at key entry, operates same as the [CE] key. If pressed after one of the following keys, operates same as the [C] key: [C/CE] [x] $[\div][+][-][=][\%][\Delta \%][\mathrm{MI}+, \mathrm{MII}+][\mathrm{MI}-, \mathrm{MII}-][\mathrm{MI} \diamond, \mathrm{MII} \diamond][\mathrm{MI} *, \mathrm{MII} *][\mathrm{MU} / \mathrm{D}]$ [IC/AVE].
The result of pressing the [C/CE] key after the [+/-] or the [\#/P] key depends on the state before the keys were pressed.
[\#/P] $\qquad$ If pressed after the numerical key entry, prints the contents of the key entry data register together with the \# symbol, but does not change the current state. If the key is pressed after a key except the numerical keys or [+/-] key, does not change the contents of the displays or the current state. If the key is pressed in clock mode, automatically prints the displayed date and time.
$[+$ TAXI/II $] .. . . . . . . . .$. Calculate included tax operation or excluded tax operation. But, only prints and
[-TAXI/II does not express the tax. Prints or displays the result-value. (result-value adjusts decimal-point (TAB) setting.) Feeds the paper one line after prints.

$$
\left.\begin{array}{cl}
\text { TAXI key operation example: } & (\text { TAX }=3 \%) \\
\mathrm{a}[+\mathrm{TAXI}] & \ldots . . . . \mathrm{a}(3 / 100) \\
& \ldots . . . \mathrm{a}+(\mathrm{a}(3 / 100))
\end{array}\right) \text { (prints TAX) }
$$

If pressed at key entry after number key entry, calculate the tax as a result of calculation.
When multiplication/division instruction is stored in memory.
[P/NP] ................... Switches between PRINT and NON-PRINT mode. At reset, NON-PRINT mode is set. Switches mode in each time when the [ $\mathrm{P} / \mathrm{NP}$ ] key is pressed: $\mathrm{P} \rightarrow \mathrm{NP} \rightarrow \mathrm{P} \rightarrow$ NP. In PRINT mode, displays "print mode". Valid only when the [ $\overline{\mathrm{T}} / \mathrm{L}$ ] lock key is off.
[RND] $\qquad$ Switches between round-up, round-off and half-adjust. At reset, half-adjust is set. Switches the mode in each time when the [RND] key is pressed: $5 / 4 \rightarrow \downarrow \rightarrow \uparrow \rightarrow 5 / 4 \rightarrow \downarrow \rightarrow \uparrow$. Displays round-up/round-off. Valid only when the [ $\overline{\mathrm{T}} / \mathrm{L}$ ] lock key is off.
[GT MOD] $\qquad$ Exchange GT-mode. (initial setting isn't support GT-mode.) GT mode cycles not-support and support. And displays GT-mode flag. Only touch key mode is valid.
[IC MOD].............. Exchange IC-mode. (initial setting isn't support IC-mode.) IC mode cycles not-support, IC+ and IC $\pm$-mode. And displays IC-mode flag. Any touch key mode is valid.
[IC/AVE]............... Prints or displays the item counter, when IC/AVE key continuously pressed twice just after pressed [*] key and [ $\llcorner$ ] key,
After first, prints or displays the item counter.
The second, the calculation of the mean number are executed, prints or displays the operation result.
After calculation of the mean number, item counter are cleared.
Example

(*) $\quad \rightarrow$ Displays or prints addition/ subtraction register.
(IC/AVE) $\rightarrow$ Displays the item counter
(IC/AVE) $\rightarrow$ Displays or prints $(\mathrm{a}+\mathrm{b}+\mathrm{c}+\mathrm{d}+\mathrm{e}+\mathrm{f}+\mathrm{g}) / 7$

The even if IC-value is a negative, the calculation of the mean number.

## Example


[DEC] $\qquad$ Switches the decimal point. At reset, floating point (F) is set. Switches the mode in each time when the [DEC] key is pressed as follows:
$\mathrm{F} \rightarrow 0 \rightarrow 1 \rightarrow 2 \rightarrow 3 \rightarrow 4 \rightarrow 6 \rightarrow \mathrm{~A} \rightarrow \mathrm{AII} \rightarrow \mathrm{F} \rightarrow 0 \rightarrow 1$. Displays the specified decimal point or add mode.
Valid only when the [ $\overline{\mathrm{T}} / \mathrm{L}$ ] lock key is off.
(3) Explanation of lock keys
$[0,1,2,3] \ldots \ldots . . . . . .$. Sets the specified decimal point. If no specification, floating is set.
[4, 6, A, AII] When processing floating point data, the operation result is zero-shifted.
When A mode is specified, key-entered data are multiplied by $1 / 100$ only when the key-entered numerical value is used for addition/subtraction or memory addition/subtraction. If the [ $\cdot]$ key is pressed during data entry, A mode is invalid. The operation result is treated the same as the specified decimal point, 2. When AII mode is specified, key-entered data are multiplied by $1 / 100$ only when the key-entered numerical value is used for multiplication/division by [=] key. If the [•] key is pressed during data entry, AII mode is invalid. The operation result is treated the same as the specified decimal point, 2.
[CUT, UP] ............ Rounds-off in CUT mode; rounds-up in UP mode; when no specification is made, half-adjusts. When a decimal point is specified, the digit (s) in the subsequent decimal place is (are) half-adjusted, rounded-off, or rounded-up (??).
If floating point is specified, the value of the least significant digits which cannot be displayed is rounded off.
$[\mathrm{P} / \overline{\mathrm{NP}}]$................. Switches between print and non print mode. When [P/ $\overline{\mathrm{NP}}$ ] lock key is off, disables all printing except $[\mathrm{PF}]$ or $[\# / \mathrm{P}]$ key.
When mode changes from non-print to print, feeds the paper one line.
[IC+]...................... Selects item count mode.
$[\mathrm{IC} \pm] \quad \mathrm{IC}+\ldots . . . .$. Counts up by the $[+]$ or $[-]$ key.
IC $\pm \ldots . . . .$. Counts up by the [+] key, down by the [-] key.
[ $\Sigma$ ] ......................... I
If an operation is performed by the [=] or [\%] key in auto accumulation calculation mode, adds the operation result to the addition/subtraction register and increments the item counter.
[GT] $\qquad$ In grand total mode, adds the total register to the GT register by the [*] key.
[ $\overline{\mathrm{T}} / \mathrm{L}$ ] $\qquad$ When the [ $\overline{\mathrm{T}} / \mathrm{L}$ ] lock key is off, the [P/NP], [ $\Sigma$ ], [GT MOD], [IC MOD], [RND], and [DEC] keys are valid. When the [ $\bar{T} / L$ ] key is on, the [NP], [ $\Sigma$ ], [GT], [IC+], [IC $\pm$ ], [CUT], [UP], and [0, 1, 2, 3, 4, 6, A, AII] lock keys are valid.
[SETI $]. . . . . . . . . . . . . .$. When the [SETI/SETII] lock key is on, prints and express the stored tax rate. When the [SETI/SETII] lock key is off, store the expression data to the new tax rate. The result of tax rate is only floating-point, and not consent the decimal-point at this function.
[FEED] ................. Feed paper.
[TMR] ................... When the [TMR] lock key is on, auto power-off functions. (after approx. 6 minutes.)
[2 TAX] ................. Switches between single tax and double tax mode. When the [2 TAX] lock key is on, one tax rate can be set. (SETII and TAXII will be disabled.)
When the [2 TAX] lock key is off, two tax rates can be set.
[2 M] $\qquad$ Selects single memory or double memory mode. When the [ 2 M ] lock key is on, one memory can be used. (MII will be disabled.) When the [2 M] lock key is off, two memories can be used.
(4) ON, OFF key [ON $\qquad$ If pressed in HOLD mode, cancels HOLD. At that time, cancels all arithmetic instructions and errors. The contents of the memory register and the TAX RATE before HOLD mode are retained; all other registers are cleared. While the [ON] key is pressed, the [OFF] key is invalid.
[OFF] $\qquad$ Forcibly enters HOLD mode (CPU sleep mode).

## Operation Example



Note 2: <PF>. $\qquad$ .Paper feed


Note 2: <PF> $\qquad$ Paper feed

| Key |  |  |  |  |  |  |  |  |  | Print |  | Display |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| F | $4 / 5$ | IC | $\Sigma$ | GT | MOD | 10/12 | 2 TAX | 2 M | Touch |  |  |  |  |
| F | 4/5 | OFF | $\Sigma$ | OFF | CAL | 12 | ON | ON | $3 \times$ | 3. | $\times$ |  | 3. |
|  |  |  |  |  |  |  |  |  | $4 \div$ |  | $\div$ |  | 12. |
|  |  |  |  |  |  |  |  |  | $=$ |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | 3. |
|  |  |  |  |  |  |  |  |  | $5 \times$ | 5. | $\times$ |  | 5. |
|  |  |  |  |  |  |  |  |  | $6 \%$ | 6. | \% |  |  |
|  |  |  |  |  |  |  |  |  |  | 0.3 | $+$ |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | 0.3 |
|  |  |  |  |  |  |  |  |  | + |  | $+$ |  |  |
|  |  |  |  |  |  |  |  |  |  | 5.3 | \% |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | 5.3 |
|  |  |  |  |  |  |  |  |  | $2 \div$ | 2. | $\div$ |  | 2. |
|  |  |  |  |  |  |  |  |  | $3 \%$ | 3. | \% |  |  |
|  |  |  |  |  |  |  |  |  |  | 66.6666666666 | + |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | 66.6666666666 |
|  |  |  |  |  |  |  |  |  | $2 \mathrm{MU} / \mathrm{D}$ |  | M |  | 2. |
|  |  |  |  |  |  |  |  |  | $3=$ | 3. | \% |  |  |
|  |  |  |  |  |  |  |  |  |  |  | $=$ |  |  |
|  |  |  |  |  |  |  |  |  |  | 0.0618556701 | * |  |  |
|  |  |  |  |  |  |  |  |  |  | 2.0618556701 | + |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | 2.0618556701 |
|  |  |  |  |  |  |  |  |  | $2 \Delta \%$ | 2. | - |  | 2. |
|  |  |  |  |  |  |  |  |  | $3=$ | 3. | \% |  |  |
|  |  |  |  |  |  |  |  |  |  |  | $=$ |  |  |
|  |  |  |  |  |  |  |  |  |  | 1. | * |  |  |
|  |  |  |  |  |  |  |  |  |  |  | + |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | 50. |
|  |  |  |  |  |  |  |  |  | * | 122.028522336 | * |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | 122.028522336 |
| F | 4/5 | OFF | $\Sigma$ | GT | CAL | 12 | ON | ON | $2+$ | 2. | + |  | 2. |
|  |  |  |  |  |  |  |  |  | $3+$ | 3. | + |  | 5. |
|  |  |  |  |  |  |  |  |  | * |  | T |  |  |
|  |  |  |  |  |  |  |  |  |  | 5. | $+$ |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  | GT | 5. |
|  |  |  |  |  |  |  |  |  | $3-$ | 3. | - | GT | -3. |
|  |  |  |  |  |  |  |  |  | 4 - | 4. | - | GT | -7. |
|  |  |  |  |  |  |  |  |  | 5 - | 5. | - | GT | -12. |
|  |  |  |  |  |  |  |  |  |  |  | T |  |  |
|  |  |  |  |  |  |  |  |  | * | -12. |  |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  | GT | -12. |
|  |  |  |  |  |  |  |  |  | GT |  | T |  |  |
|  |  |  |  |  |  |  |  |  |  | -7. |  | GT | -7. |
|  |  |  |  |  |  |  |  |  |  |  | T |  |  |
|  |  |  |  |  |  |  |  |  | GT | -7. |  |  |  |
|  |  |  |  |  |  |  |  |  |  | <PF> |  |  | -7. |

Note 2: <PF> .Paper feed


Note 2: <PF> ........Paper feed


Note 2: <PF> $\qquad$ .Paper feed


Note 2: <PF> ........Paper feed


Note 2: <PF> $\qquad$ .Paper feed


Note 2: <PF> Paper feed


Note 2: <PF> ........Paper feed

## Maximum Ratings ( $\mathrm{V}_{\mathrm{SS}}=\mathbf{0} \mathrm{V}$ )

| Characteristics | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Supply voltage 1 | $\mathrm{~V}_{\mathrm{DD}}$ | $-0.3 \sim 6$ | V |
| Supply voltage (LCD drive) | $\mathrm{V}_{\mathrm{LC}}$ | $-0.3 \sim \mathrm{~V}_{\mathrm{DD}}+0.3$ | V |
| Input voltage | $\mathrm{V}_{\text {IN }}$ | $-0.3 \sim \mathrm{~V}_{\mathrm{DD}}+0.3$ | V |
| Output voltage | $\mathrm{V}_{\mathrm{OUT}}$ | $-0.3 \sim \mathrm{~V}_{\mathrm{DD}}+0.3$ | 3.2 |
| Output current | $\mathrm{I}_{\mathrm{OUT}}$ | 600 | V |
| Power dissipation | $\mathrm{P}_{\mathrm{D}}$ | $260(10 \mathrm{~s})$ | mW |
| Soldering temperature | $\mathrm{T}_{\text {sld }}$ | $-55 \sim 125$ | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | $\mathrm{T}_{\text {stg }}$ | $0 \sim 40$ | ${ }^{\circ} \mathrm{C}$ |
| Operating temperature | $\mathrm{T}_{\mathrm{Opr}}$ |  | ${ }^{\circ} \mathrm{C}$ |

## Electrical Characteristics

Recommended Operating Conditions (VSs $=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{opr}}=0 \sim 40^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Test Circuit | Test Condition | Min | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating temperature | $\mathrm{T}_{\text {opr }}$ | - | - | 0 | 40 | ${ }^{\circ} \mathrm{C}$ |
| Supply voltage | $V_{D D}$ | - | NORMAL | 4.5 | 5.5 | V |
|  |  | - | SLOW |  |  |  |
|  |  | - | HOLD | 2.0 |  |  |
| High-level input voltage (non-schmitt circuit) | $\mathrm{V}_{\mathrm{IH} 1}$ | - | $\mathrm{V}_{\mathrm{DD}} \geqq 4.5 \mathrm{~V}$ | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.7 \end{gathered}$ | $V_{D D}$ | V |
| High-level input voltage (schmitt circuit) | $\mathrm{V}_{\mathrm{IH} 2}$ |  |  | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.75 \end{gathered}$ | $V_{D D}$ | V |
| High-level input voltage | $\mathrm{V}_{\text {IH3 }}$ | - | $\mathrm{V}_{\mathrm{DD}}<4.5 \mathrm{~V}$ | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.9 \end{gathered}$ | $V_{D D}$ | V |
| Low-level input voltage (non-schmitt circuit) | VIL1 | - | $\mathrm{V}_{\mathrm{DD}} \geqq 4.5 \mathrm{~V}$ | 0 | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.3 \end{gathered}$ | V |
| Low-level input voltage (schmitt circuit) | $\mathrm{V}_{\text {IL2 }}$ |  |  | 0 | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.25 \end{gathered}$ | V |
| Low-level input voltage | VIL3 | - | $\mathrm{V}_{\mathrm{DD}}<4.5 \mathrm{~V}$ | 0 | $\begin{gathered} \mathrm{V}_{\mathrm{DD}} \times \\ 0.1 \end{gathered}$ | V |

DC Characteristics ( $\mathrm{V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{~T}_{\text {opr }}=0 \sim 40^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Test Circuit | Terminal | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Hysteresis voltage (schmitt circuit) | $\mathrm{V}_{\mathrm{HS}}$ | - | Hysteresis Input | - | - | 0.7 | - | V |
| Input current | $\mathrm{l}_{1 \times 1}$ | - | KO port, TEST, $\overline{\text { RESET }}, \overline{\text { HOLD }}$ | $\begin{aligned} & V_{D D}=5.5 \mathrm{~V} \\ & V_{I N}=5.5 / 0 \mathrm{~V} \end{aligned}$ | - | - | $\pm 2$ | $\mu \mathrm{A}$ |
|  | IIN2 | - | Open Drain R port, P port |  |  |  |  |  |
| Input resistance | RIN1 | - | KO port TEST with Input Resistor | $\begin{aligned} & V_{D D}=5.5 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{IN}}=5.5 / 0 \mathrm{~V} \end{aligned}$ | 30 | 70 | 150 | k $\Omega$ |
|  | RIN2 | - | $\overline{\mathrm{RESET}}$, $\overline{\mathrm{HOLD}}$ |  | 100 | 220 | 450 |  |
| Output leakage current | ILO1 | - | Sink Open Drain R port | $\begin{aligned} & V_{\mathrm{DD}}=5.5 \mathrm{~V} \\ & \mathrm{~V}_{\text {OUT }}=5.5 \mathrm{~V} \end{aligned}$ | - | - | 2 | $\mu \mathrm{A}$ |
|  | ILO2 | - | Source Open Drain R port, P port | $\begin{aligned} & V_{D D}=5.5 \mathrm{~V} \\ & \mathrm{~V}_{\text {OUT }}=-1.5 \mathrm{~V} \end{aligned}$ | - | - | -2 |  |
| High-level output voltage | $\mathrm{V}_{\mathrm{OH}}$ | - | Source Open Drain R port, P port | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V} \\ & \mathrm{IOH}=-1.6 \mathrm{~mA} \end{aligned}$ | 2.4 | - | - | V |
| Low-level output voltage | Vol | - | Sink Open Drain R port | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V} \\ & \mathrm{l} \mathrm{OL}=1.6 \mathrm{~mA} \end{aligned}$ | - | - | 0.4 | V |
| Pull-down resistance | Rout | - | R port, P port | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{IN}}=5.5 \mathrm{~V} \end{aligned}$ | 30 | 70 | 150 | k $\Omega$ |
| Output resistance | Ros | - | SEG | $\begin{aligned} & V_{D D}=5 V \\ & V_{D D}-V_{L C}=3 V \end{aligned}$ | - | - | 35 | k $\Omega$ |
|  | Roc | - | COM |  |  |  |  |  |
| Output voltage | $\mathrm{V}_{\mathrm{O} 2 / 3}$ | - | SEG/COM |  | 3.8 | 4.0 | 4.2 | V |
|  | $\mathrm{V}_{01 / 2}$ |  |  |  | 3.3 | 3.5 | 3.7 |  |
|  | $\mathrm{V}_{01 / 3}$ |  |  |  | 2.8 | 3.0 | 3.2 |  |
| Supply current (normal) | IDD | - | - | $\begin{aligned} & \hline \mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V}, \\ & \mathrm{~V}_{\mathrm{LC}}=\mathrm{V}_{\mathrm{SS}} \\ & \mathrm{f}_{\mathrm{C}}=4 \mathrm{MHz} \\ & \hline \end{aligned}$ | - | 3 | 6 | mA |
| Supply current (hold) | IDDH | - | - | $\mathrm{V}_{\mathrm{DD}}=5.5 \mathrm{~V}$ | - | 0.5 | 10 | $\mu \mathrm{A}$ |

Note 3: Typ. values are guaranteed at $\mathrm{T}_{\mathrm{opr}}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}$.
Note 4: $I_{\mathrm{IN} 1}$ : Excepts a current through a internal pull up/down resistor.
Note 5: ROS, Roc: Shows on-resistor at level switching.
Note 6: $V_{O 2 / 3}$ : Shows $2 / 3$ level output voltage at which $1 / 4$ or $1 / 3$ duty LCD drive.
Note 7: $V_{01 / 2}$ : Shows $1 / 2$ level output voltage at which $1 / 2$ duty or static LCD drive.
Note 8: $V_{\mathrm{O} 1 / 3}$ : Shows $1 / 3$ level output voltage at which $1 / 4$ or $1 / 3$ duty LCD drive.
Note 9: IDD, IDDH: Current consumption at $\mathrm{V}_{\mathrm{IN}}=5.3 \mathrm{~V} / 0.2 \mathrm{~V}$
Should be under that KO port is open and $R$ port voltage level is valid.

Oscillation Circuit ( $\mathrm{V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{DD}}=4.5 \sim 5.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{opr}}=0 \sim 40^{\circ} \mathrm{C}$ )

| Recommended Circuit | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{V} D \mathrm{DD}=5.0 \mathrm{~V} \\ & \mathrm{C}=100 \mathrm{pF} \\ & \mathrm{R}=1 \mathrm{k} \Omega \pm 2 \% \end{aligned}$ | 2.4 | 4.0 | 5.6 | MHz |

AC Characteristics ( $\mathrm{V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{DD}}=4.5 \sim 6.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{opr}}=0 \sim 40^{\circ} \mathrm{C}$ )

| Characteristics | Symbol | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Instruction cycle time | $\mathrm{t}_{\mathrm{CY}}$ | - | NORMAL | 1.9 | - | 20 |  |
|  |  | - | SLOW | 235 | - | 267 |  |
| High-level clock pulse width | twCH | - | External Clock Operation | 80 | - | - | ns |
| Low-level clock pulse width | $t_{\text {WCL }}$ | - |  |  |  |  | ns |
| Shift data hold time | $\mathrm{t}_{\text {SDH }}$ | - | - | $\begin{aligned} & 0.5 \text { tcy } \\ & -300 \end{aligned}$ | - | - | ns |
| High speed timer/counter input frequency | $\mathrm{f}_{\mathrm{HT}}$ | - | - | - | - | $\mathrm{f}_{\mathrm{C}}$ | MHz |

## Waveforms for Display



## The Proposal of Outer Circuit for Tax Rate Holding with Back-Up Battery.



Note 10: $\mathrm{V}_{1}=+3 \mathrm{~V}$ : Battery supply
$\mathrm{V}_{2}=+5 \mathrm{~V}$ : DC supply
( $\overline{\mathrm{HOLD}}$ pin is pulled down in the LSI, but normally pulled up to VDD.)
RESET pin is pulled up to VDD.
(1) Setting POWER SW to ON, $\mathrm{V}_{2}$ is supplied to VDD pin, and also to $\overline{\mathrm{HOLD}}$ pin. Then calculator operates normally.
(2) Setting POWER SW from ON to OFF, V1 is supplied to VDD pin and VSS is supplied to HOLD pin. Under this connection, TAX RATE is held.
(3) Setting POWER SW to ON, V2 is supplied to VDD pin, and also to $\overline{\text { HOLD }}$ pin. Then calculator operates normally with TAX RATE to be held.

Note 11: $\mathrm{V}_{1}$ (battery) should be supplied to the circuit after $\mathrm{V}_{2}(\mathrm{DC})$ supply, because of prevention from exhaustion of battery and abnormal operation.

## Package Dimensions

QFP80-P-1420-0.80A Unit : mm



Weight: 1.52 g (typ.)

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