DATA SHEET

Part No.	AN80L20RMS
Package Code No.	MINI-5DA (Exclusive use for AN80LxxRMS)

Panasonic

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Positive output, low dropout voltage regulator (150 mA type)

Overview

AN80LxxRMS series is a 0.15 A, low dropout, positive voltage regulator with reset function. 20 classifications of output voltages, 1.8 V, 1.9 V, 2.0 V, 2.1 V, 2.2 V, 2.5 V, 2.8 V, 2.9 V, 3.0 V, 3.1 V, 3.2 V, 3.3 V, 3.4 V, 3.5 V, 3.6 V, 4.8 V, 4.9 V, 5.0 V, 5.1 V and 5.2 V are available.

In addition, it is adopting the surface mounting type package, so that it is most suited for miniaturization and weight reduction of set equipment.

Features

- Minimum input and output voltage difference: 0.4 V max.
- High accuracy output voltage: (allowance: ±3%)
- Built-in reset function terminal (high: active)
- Built-in overcurrent limit circuit
- Built-in rush current prevention circuit at input voltage rise.
- Output voltage: 2.0 V

Applications

• Cellular phone, PHS, analog cordless phone, other small sized portable equipment.

Package

• 5-pin Plastic Surface Mount Small Package (MINI type)

Type

• Silicon monolithic bipolar IC.

Block Diagram



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Pin Descriptions

Pin No.	Pin name	Туре	Description
1	IN	Input	Input voltage
2	OUT	Output	Output voltage
3	N.C.		Non connected
4	GND	Ground	Ground
5	CONT	Input	Control (H \rightarrow start, L \rightarrow stop)

■ Absolute Maximum Ratings

No.	Parameter	Symbol	Rating	Unit	Note
1	Supply voltage	V _{CC}	14.6	V	*1
2	Supply current	I _{CC}	300	mA	
3	Power dissipation	P _D	78	mW	*2
4	Operating ambient temperature	T _{opr}	-30 to +85	°C	*3
5	Storage temperature	T _{stg}	-55 to +150	°C	*3

Note) *1: There may be a case of the device destruction when the output (V_{OUT}) and the grounding (GND), or the output (V_{OUT}) and input (V_{IN}) are short-circuited.

*2: The power dissipation shown is the value at $T_a = 85^{\circ}C$ for the independent (unmounted) IC package without a heat sink. When using this IC, refer to the • P_D - T_a diagram in the \blacksquare Technical Data and use under the condition not exceeding the allowable value.

*3: Except for the power dissipation, operating ambient temperature, and storage temperature, all ratings are for $T_a = 25^{\circ}C$.

Operating supply voltage range

Parameter	Symbol	Range	Unit	Note
Supply voltage range	V _{CC}	2.4 to 14.5	V	*

Note) *: The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

Electrical Characteristics

Note) Unless otherwise specified, T_a = 25°C±2°C, C_{IN} = 0.1 μF and C_{OUT} = 10 μF

Unless otherwise specially provided, shorten each test time (within 10 ms) so that the test is conducted under the condition that the drift due to the temperature increase in the chip junction part can be neglected.

Na	Parameter	Symbol	Conditiono	Limits			Linit	Noto
INO.	Palameter	Symbol	Conditions	Min	Тур	Max	Unit	note
1	Output voltage	V _{OUT}	$V_{IN} = 3.0 V$ $I_{OUT} = 50 mA$	1.940	2.0	2.060	V	
2	Line regulation 1	REG _{IN1}	$V_{IN} = 3.0 \text{ V} \rightarrow 14.5 \text{ V}$ $I_{OUT} = 50 \text{ mA}$		10	50	mV	
3	Line regulation 2	REG _{IN2}	$V_{IN} = 3.0 V \rightarrow 9 V$ $I_{OUT} = 50 mA$		5	20	mV	
4	Load regulation	REG _{LOA}	$V_{IN} = 3.0 V$ $I_{OUT} = 0 \text{ mA} \rightarrow 150 \text{ mA}$		50	150	mV	
5	Peak output current	I _{peak}	$V_{IN} = 3.0$ V, The output current value when V_{OUT} decreases by 5% from its value at $I_{OUT} = 50$ mA.	180	240		mA	*1
6	Bias current at no load	I _{Bias}	$V_{IN} = 3.0 V$ $I_{OUT} = 0 mA$	_	_	350	μΑ	
7	Bias current change with load	ΔI_{Bias}	$V_{IN} = 3.0 V$ $I_{OUT} = 0 mA \rightarrow 150 mA$	_	_	5	mA	
8	Current consumption in standby mode	I _{STB}	$V_{IN} = 10 V$ $V_{CONT} = 0 V$	_	_	0.1	μΑ	
9	Bias current before regulation	I _{rush}	$V_{IN} = 1.7 V$ $I_{OUT} = 0 mA$		1.5	5	mA	
10	Ripple rejection ratio	R.R.	1 V[rms], $f = 120 \text{ Hz}$ I _{OUT} = 10 mA	62	70		dB	
11	Minimum difference between input and output voltage 1	V _{DIF(min)1}	$V_{IN} = 1.7 V$ $I_{OUT} = 0 mA$		0.1	0.2	V	
12	Minimum difference between input and output voltage 2	V _{DIF(min)2}	$V_{IN} = 2.3 V$ $I_{OUT} = 150 mA$		_	0.4	V	
13	Control pin threshold voltage high level	V _{CONTH}	$V_{IN} = 3.0 V$ $I_{OUT} = 50 mA$	_	_	1.50	V	_
14	Control pin threshold voltage low level	V _{CONTL}	$V_{IN} = 3.0 V$ $I_{OUT} = 50 mA$	0.30	_	_	V	
15	Control pin current	V _{CONT}	$V_{IN} = 3.0 V$ $I_{OUT} = 50 mA, V_{CONT} = 1.8 V$	_		30	μA	

Note) *1: Peak output current : The output current when the output voltage has been decreased by 5% from the value at the time when the output current is 50 mA due to the overcurrent protection.

Electrical Characteristics (Reference values for design)

Note) Unless otherwise specified, $T_a = 25^{\circ}C \pm 2^{\circ}C$, $C_{IN} = 0.1 \ \mu\text{F}$ and $C_{OUT} = 10 \ \mu\text{F}$

Unless otherwise specially provided, shorten each test time (within 10 ms) so that the test is conducted under the condition that the drift due to the temperature increase in the chip junction part can be neglected.

The characteristics listed below are reference values for design of the IC and are not guaranteed by inspection.

If a problem does occur related to these characteristics, Matsushita will respond in good faith to user concerns.

No.	Parameter	Symbol	Conditions	Reference values			Unit	Note
		-		Min	Тур	Max		
1	Output noise voltage	Vno	$\begin{array}{l} 10 \text{Hz} \leq f \leq 100 \text{ kHz} \\ I_{\text{OUT}} = 10 \text{ mA} \end{array}$	_	42	_	μV	_
2	Temperature coefficient of output voltage	$\frac{1}{V_{OUT}} \cdot \frac{dV_{OUT}}{dT}$	$V_{IN} = 3.0 V$ $I_{OUT} = 0 mA$ $-30^{\circ}C \le T_a \le +85^{\circ}C$		90		ppm/ °C	
3	Output rise time	t _{on}	$V_{IN} = 3.0 V$ $I_{OUT} = 50 mA$ $V_{CONT} = 0 V \rightarrow 1.8 V$		0.10		ms	

Technical Data

ESR characteristics



Note) 1. The value are not the once guaranteed through inspection.

2. Select the value of C_{OUT} capacitor in the range from 0.22 μF to100 $\mu F.$ Recommended value is 10 $\mu F.$

3. Select a capacitor so that ESR (equivalent series resistance of the capacitor) is 35 Ω or less at T_a = -30°C to +85°C.

Technical Data (Continued) • $P_D - T_a$ diagram



SFF00063AEB

- Package Dimensions (Unit: mm)
- MINI-5DA (Exclusive use for AN80LxxRMS)



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