

BCR8PM-12L

Triac

Medium Power Use

REJ03G0303-0200 Rev.2.00 Nov.08.2004

Features

I_{T (RMS)}: 8 A
 V_{DRM}: 600 V

• I_{FGTI} , I_{RGTI} , I_{RGTIII} : 30 mA (20 mA)^{Note5}

• Viso: 2000 V

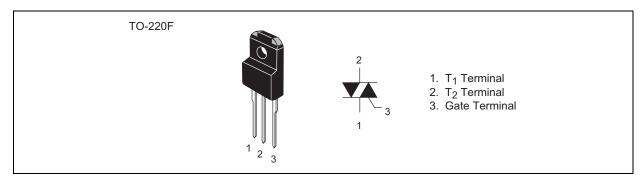
Insulated Type

Planar Passivation Type

• UL Recognized : Yellow Card No. E223904

File No. E80271

Outline



Applications

Switching mode power supply, light dimmer, electronic flasher unit, control of household equipment such as TV sets, stereo systems, refrigerator, washing machine, infrared kotatsu, and carpet, solenoid driver, small motor control, copying machine, electric tool, electric heater control, and other general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
Faranieter	Symbol	12	O I III	
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	600	V	
Non-repetitive peak off-state voltage ^{Note1}	V_{DSM}	720	V	

RENESAS

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	8	А	Commercial frequency, sine full wave 360° conduction, Tc = 88°C
Surge on-state current	I _{TSM}	80	А	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	26	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	5	W	
Average gate power dissipation	P _{G (AV)}	0.5	W	
Peak gate voltage	V_{GM}	10	V	
Peak gate current	I _{GM}	2	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	2.0	g	Typical value
Isolation voltage	Viso	2000	V	Ta = 25°C, AC 1 minute, $T_1 \cdot T_2 \cdot G$ terminal to case

Notes: 1. Gate open.

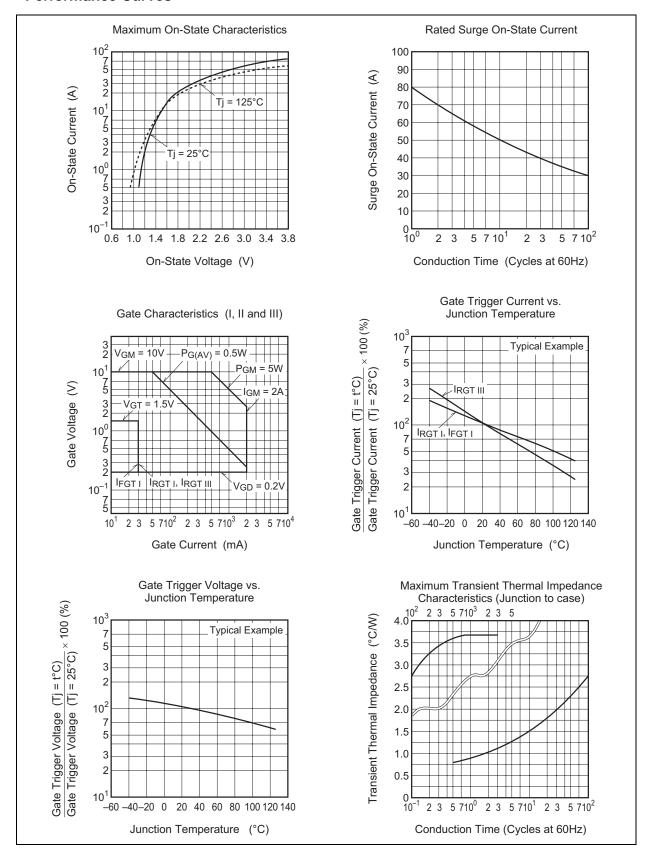
Electrical Characteristics

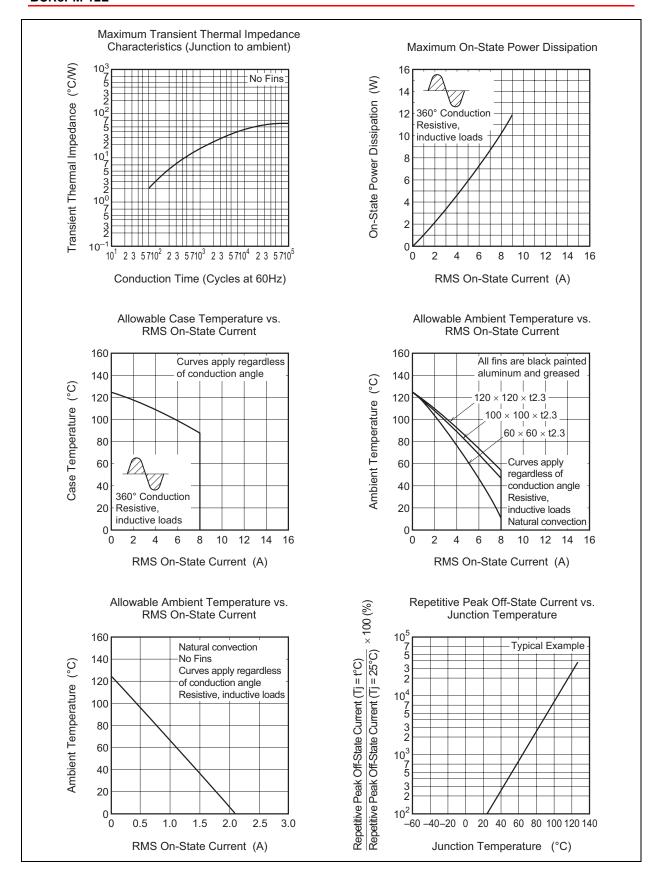
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state current		I _{DRM}	_	_	2.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V_{TM}	_	_	1.6	V	Tc = 25°C, I _{TM} = 12 A,
							Instantaneous measurement
Gate trigger voltage ^{Note2}	I	$V_{FGT_{I}}$		_	1.5	V	$Tj = 25^{\circ}C, V_D = 6 V, R_L = 6 \Omega,$
	II	V_{RGTI}			1.5	>	$R_G = 330 \Omega$
	III	V_{RGTIII}			1.5	V	
Gate trigger current ^{Note2}	I	I _{FGTI}		_	30 ^{Note5}	mA	$Tj = 25^{\circ}C, V_D = 6 V, R_L = 6 \Omega,$
	II	I_{RGTI}			30 ^{Note5}	mA	$R_G = 330 \Omega$
	III	I _{RGTIII}	_	_	30 ^{Note5}	mA	
Gate non-trigger voltage		V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$
Thermal resistance		R _{th (j-c)}	_	_	3.7	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off-stat commutating voltage ^{Note4}	е	(dv/dt)c	10	_	_	V/µs	Tj = 125°C

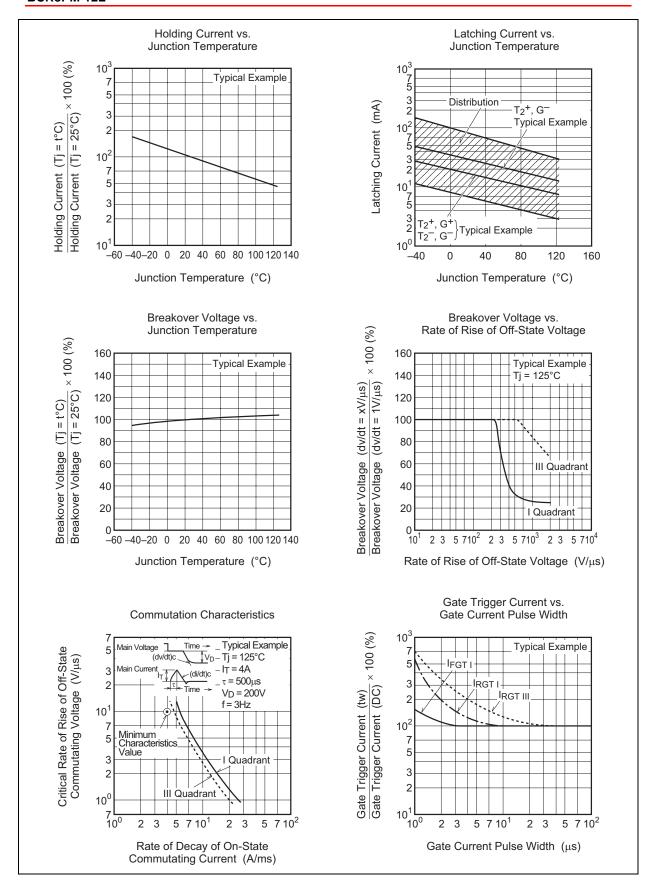
- Notes: 2. Measurement using the gate trigger characteristics measurement circuit.
 - 3. The contact thermal resistance $R_{th\;(c\text{-}f)}$ in case of greasing is 0.5°C/W.
 - 4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.
 - 5. High sensitivity ($I_{GT} \le 20 \text{ mA}$) is also available. (I_{GT} item: 1)

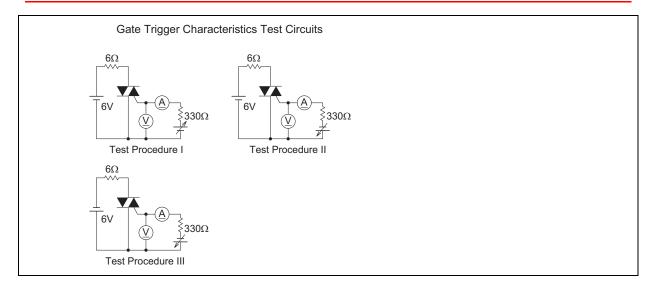
Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage → Time
2. Rate of decay of on-state commutating current (di/dt)c = - 4.0 A/ms	Main Current (di/dt)c → Time
3. Peak off-state voltage $V_D = 400 \text{ V}$	Main Voltage Time (dv/dt)c

Performance Curves

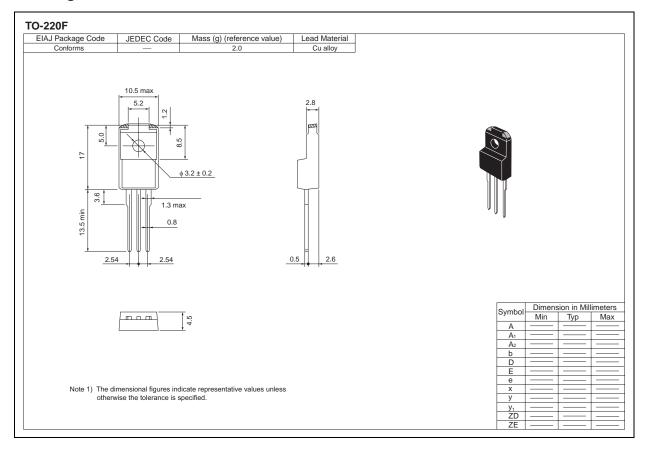








Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	100	Type name +A	BCR8PM-12LA
Lead form	Plastic Magazine (Tube)	50	Type name +A – Lead forming code	BCR8PM-12LA-A8

Note: Please confirm the specification about the shipping in detail.

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

- (ii) use of nonlitammable material or (iii) prevention against any malfunction or mishap.

 Notes regarding these materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.

 2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp, without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

 The information described here may contain technical inaccuracies or typographical errors.

 Renesas Technology Corp, assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information published by Renesas Technology Corp, by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).

 4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information as a total system before making a final decision on the applicability of the information as a total system before making a final decision on the applicability of the information as a total syste

- use.

 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.

 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.

 Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.

 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

RENESAS SALES OFFICES

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.

Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

© 2004. Renesas Technology Corp., All rights reserved. Printed in Japan. Colophon .2.0

http://www.renesas.com