

M51998P/FP Switching Regulator Control

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Description

M51998 is a primary side switching regulator control IC suitable for converting AC power supply to stabilized DC voltage.

Basic functions provided are from M51995A and limited to the indispensable. This device is housed in 10-pin SOP, 14-pin DIP.

Features

- Output current (Io peak): ±1 A
- Totempole output
- Small start-up current: 100 µA (typ.)
- Start-up threshold 16V. Stop voltage 10 V
- Output duty: 51% (internally fixed)
- Pulse-by-pulse current limit
- 10-pin SOP, 14-pin DIP package

Application

Fly back regulator

Block Diagram





Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C, unless otherwise noted.)$

ltem	Symbol	Ratings	Unit	Condition
Supply voltage	V _{CC}	36	V	
Collector terminal voltage	V _C	36	V	
Output current	lo	±1	А	Peak
		±0.15	А	Continuous
CLM terminal voltage	V _{CLM}	-0.3 to +4.0	V	
OVP terminal voltage	I _{OVP}	10	mA	
F/B terminal voltage	V _{FB}	0 to 10	V	
Power dissipation	Pd	440	mW	
Thermal derating ratio	Κθ	3.52	mW/°C	
Operating temperature	Topr	-20 to 85	°C	
Storage temperature	Tstg	-40 to 125	°C	



Electrical Characteristics

 $(Ta = 25^{\circ}C, V_{CC} = 5 V, unless otherwise noted.)$

		Limits					
Block	Item	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Supply	Operating voltage range	V _{cc}	V _{CC(STOP)}	—	35	V	
voltage / Circuit current	Operation start voltage	V _{cc} (START)	15.2	16.2	17.2	V	
	Operation stop voltage	V _{CC} (STOP)	9.0	9.9	10.9	V	
	Start/stop differential	ΔV _{CC}	5.0	6.3	7.6	V	$\Delta V_{CC} = V_{CC(START)} - V_{CC(STOP)}$
	Start-up current	I _{CCL}	50	100	150	μA	$V_{cc} = 14.5 V,$ Ta = 25°C
			40	100	200		V _{CC} = 14.5 V, -20 < Ta < 85°C
	Circuit current	I _{cco}	8	12	21	mA	$V_{CC} = 30 V$
	Circuit current in OVP state	I _{CC} OVP	1.3	2.0	3.0	mA	$V_{CC} = 25 V$
			125	210	320	μA	$V_{CC} = 9.5 V$
F/B	Current at 0% duty	I _{FBMIND}	-2.10	-1.54	-1.00	mA	
	Current at maximum duty	I _{FBMAXD}	-0.90	-0.55	-0.40	mA	
	Max./0% differential	ΔI_{FB}	-1.35	-0.99	-0.70	mA	$\Delta I_{FB} = I_{FBMIND} - I_{FBMAXD}$
	F/B terminal voltage	V _{FB}	4.9	5.9	7.1	V	
	F/B terminal resistance	RFB	420	600	780	Ω	
OVP	OVP term. H threshold volt.	V _{THOVPH}	540	750	960	mV	
	OVP term. hysteresis volt.	ΔV_{THOVP}	_	30	_	mV	$\Delta V_{THOVP} = V_{THOVPH} - V_{THOVPL}$
	OVP term. threshold current	I _{THOVP}	80	150	250	μA	
	OVP term. input current	I _{INOVP}	80	150	250	μA	
	OVP reset supply voltage	V _{CCOVPC}	7.5	9.0	10.0	V	
	Operation stop volt. -OVP reset supply voltage	V _{CC(STOP)} -V _{CCOVPC}	0.55	1.20	—	V	
	Current from OVP terminal	I _{THOVPC}	-480	-320	-213	μA	V _{CC} = 30 V
	for OVP reset		-210	-140	-93		$V_{cc} = 18 V$
CLM	CLM terminal threshold volt	V _{THCLM}	180	200	220	mV	
	CLM terminal current	IINCLM	-280	-200	-140	μA	
	Delay time from CLM to Vout	T _{PDCLM}	_	90	—	ns	
Oscillator	Oscillation frequency	f _{osc}	68	75	82	kHz	R _T = 27 kΩ,
	Maximum ON duty	T _{DUTY}	48	51	54	%	$C_T = 470 \text{ pF}$
	Upper limit volt. of OSC waveform	V _{OSCH}	3.97	4.37	4.77	V	
	Lower limit volt. of OSC waveform	V _{OSCL}	1.76	1.96	2.16	V	
	Upper/lower limit volt. difference	ΔV _{osc}	2.11	2.41	2.71	V	
	RT terminal voltage	V _{RT}	3.80	4.50	5.40	V	$R_T = 27 \text{ k}\Omega$



			Limits				
Block	Item	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Output	Output low voltage	V _{OL1}	—	0.04	0.4	V	V _{CC} = 18 V,
							I _o = 10 mA
		V _{OL2}	—	0.7	1.4	V	V _{CC} = 18 V,
							I _o = 100 mA
		V _{OL3}	—	0.85	1.0	V	$V_{CC} = 5 V, I_0 = 1 mA$
		V _{OL4}	—	1.3	2.0	V	$V_{CC} = 5 V$,
							I _o = 100 mA
	Output high voltage	V _{OH1}	16.0	16.7	_	V	V _{CC} = 18 V,
							$I_0 = -10 \text{ mA}$
		V _{OH2}	15.5	16.5	_	V	V _{CC} = 18 V,
							$I_0 = -100 \text{ mA}$
	Output voltage rise time	T _{RISE}	_	50	_	ns	
	Output voltage fall time	T _{FALL}		35	_	ns	

(Ta = 25° C, V_{CC} = 5 V, unless otherwise noted.)

M51998 Example Application Circuit





Package Dimensions





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