# Headphone driver for digital audio BA3578FS / BA3579FS

The BA3578FS and BA3579FS are headphone drivers with internal an LPF and fixed bass boost circuit for multi-bit D / A converters.

# Applications

Portable CD players

# Features

- 1) Suitable for use in digital audio equipment (line-out output noise voltage:  $18\mu Vrms$ , S / N = 95dB Typ.).
- 2) Internal LPF for multi-bit D / A converters (fc = 34kHz, -12dB / oct. Typ.).
- 3) Headphone mute function.

- 4) Internal BB (bass boost) circuit.
- 5) Internal supply current for line-mute transistor.
- 6) No need for output oscillation preventive measures.
- 7) Internal standby switch.

# ●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	AVDD	5.5	V
rower supply voltage	PVcc	5.5	V
Power dissipation	Pd	600*1	mW
Operating temperature	Topr	<b>−</b> 20~ <b>+</b> 60	ొ
Storage temperature	Tstg	<b>−55∼</b> +125	င

<sup>\$1</sup> Reduced by 6mW for each increase in Ta of 1  $^{\circ}\!\!\mathrm{C}$  over 25  $^{\circ}\!\!\mathrm{C}.$ 

### Recommended operating conditions

Parameter	Symbol	Limits	Unit		
Power supply voltage	AVDD	3.6	V		
	PVcc	3.6	V		

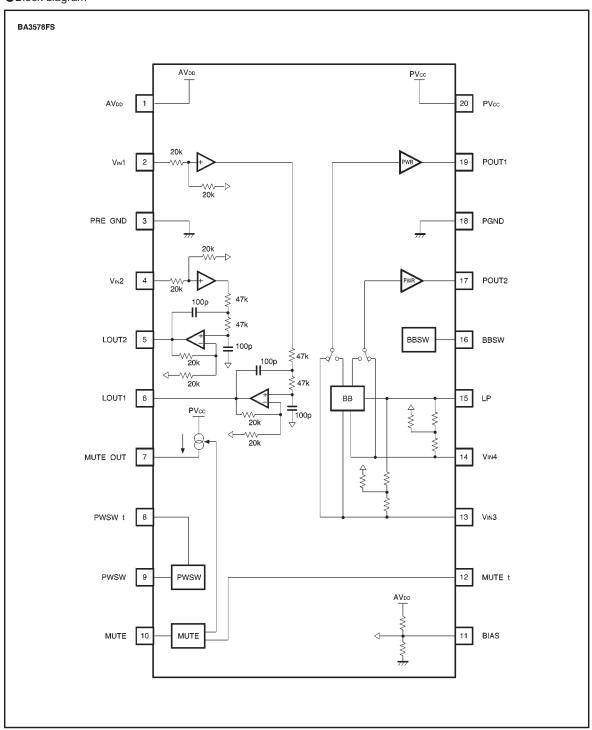
# Recommended operating range

Parameter	Symbol	Limits	Unit		
Power supply voltage	AVDD	2.8~5.0	V		
	PVcc	2.8~5.0*2	V		

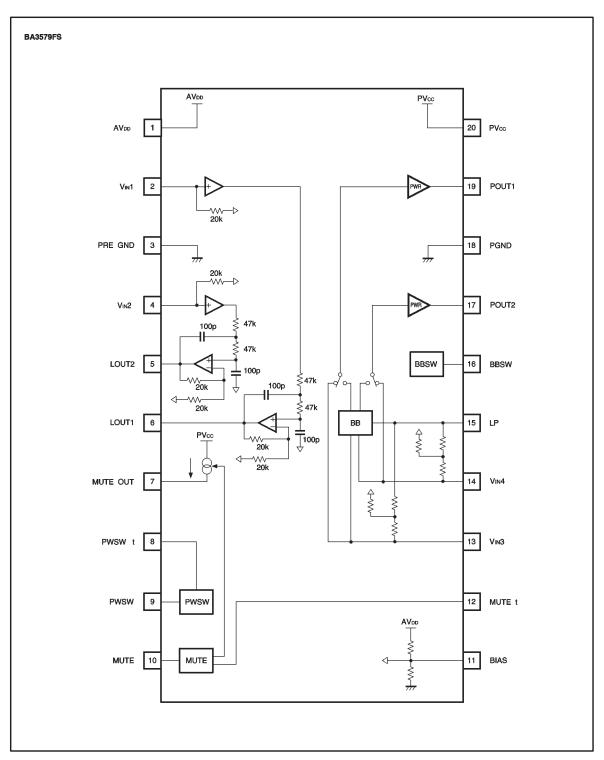
<sup>\*2</sup> In order to use the headphone output to its optimum performance, have the power supply voltage such that  $PVcc \ge AVDD - 0.3V$ 



# Block diagram



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•Electrical characteristics (Unless otherwise noted, Ta = 25°C, PVcc = AVdd = 3.6V, f = 1kHz, PwSw = ON, MUTE = OFF, BB = OFF, Line RL1 = 47kΩ, headphone RL2 = 32Ω, filter = DIN AUDIO, line-out measurements are with VIN3 and 4 = 0V)

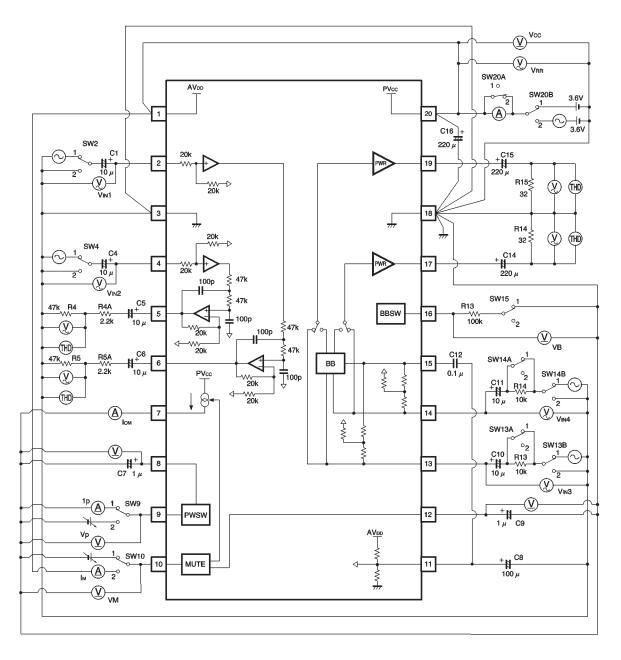
В .	0 1.1	1.47	-			0.189
Parameter	Symbol	Min.	Тур.	Max.	Unit	Coniditions
Circuit current	Icc	4.5	7.5	11.5	mA	V <sub>IN</sub> =0
Power ON voltage	VP	2.0	2.8	_	V	
Power ON pin current	lР	_	60	110	μΑ	V <sub>PWSW</sub> =0V
Mute ON voltage	Vм	_	0.8	1.4	V	
Mute pin current	Ім	_	110	190	μΑ	V <sub>MUTE</sub> =AV <sub>DD</sub>
Mute output current	Іом	1.0	2.0	_	mA	V <sub>MUTE</sub> =AV <sub>DD</sub>
Bass boost OFF voltage	V <sub>B</sub>	0.5	0.7	_	V	
〈Line-out〉 (BA3578FS)						
Voltage gain	Gv1	-2.4	-0.4	1.6	dB	V <sub>IN</sub> 1, 2=0.8Vrms
Voltage gain difference	ΔGv	-2.1	-0.3	1.2	dB	Gv1 (f=1kHz) -Gv1 (f=10kHz)
Total harmonic distortion 1	THD₁	_	0.05	0.2	%	V <sub>IN</sub> 1, 2=0.8Vrms
Maximum output voltage 1	V <sub>OM1</sub>	0.8	1.1	_	Vrms	THD=0.2%
Output noise voltage 1	V <sub>NO1</sub>	_	-96	-90	dBV	Rg=0
Channel separation 1	CS <sub>1</sub>	68	78	_	dB	V <sub>IN</sub> 1, 2=0.8Vrms, Rg=0
Ripple rejection 1	RR <sub>1</sub>	37	47	_	dB	V <sub>RR</sub> =-20dBV, f <sub>RR</sub> =1kHz, Rg=0
〈Line-out〉 (BA3579FS)						
Voltage gain	Gv1	3.6	5.6	7.6	dB	V <sub>IN</sub> 1, 2=0.4Vrms
Voltage gain difference	ΔGv	-2.1	-0.3	1.2	dB	Gv1 (f=1kHz) -Gv1 (f=10kHz)
Total harmonic distortion 1	THD₁	_	0.05	0.2	%	V <sub>IN</sub> 1, 2=0.4Vrms
Maximum output voltage 1	Vом1	0.8	1.1	_	Vrms	THD=0.2%
Output noise voltage 1	V <sub>NO1</sub>	_	-96	-90	dBV	Rg=0
Channel separation 1	CS <sub>1</sub>	68	78	_	dB	V <sub>IN</sub> 1, 2=0.4Vrms, Rg=0
Ripple rejection 1	RR <sub>1</sub>	37	47	_	dB	V <sub>RR</sub> =-20dBV, f <sub>RR</sub> =1kHz, Rg=0
〈Headphone out〉						
Voltage gain 2	Gv2	10.2	12.2	14.2	dB	V <sub>IN</sub> 3, 4=-20dBV
Total harmonic distortion 2	THD <sub>2</sub>	_	0.1	0.5	%	V <sub>IN</sub> 3, 4=-20dBV
Rated output	Po	20	32	_	mW	THD=10%
Output noise voltage 2	V <sub>NO2</sub>	_	-90	-84	dBV	Rg=0
Channel separation 2	CS <sub>2</sub>	59	69	_	dB	Vin3, 4=-20dBV, Rg=0
Channel separation 3	CS <sub>3</sub>	11	14	_	dB	V <sub>IN</sub> 3, 4=-20dBV, Rg=0, BB ON
Channel separation 4	CS <sub>4</sub>	25	35	_	dB	V <sub>IN</sub> 3, 4=-20dBV, Rg=10kΩ
Mute attenuation	АТТм	59	69	_	dB	V <sub>IN</sub> 3, 4=-20dBV, MUTE ON
Bass boost	ВВ	6.1	9.1	12.1	dB	V <sub>IN</sub> 3, 4=-30dBV, f=100Hz, BB ON
Ripple rejection 2	RR₂	26	36	_	dB	V <sub>RR</sub> =-20dBV, f <sub>RR</sub> =1kHz, Rg=0

ONot designed for radiation resistance.



# Measurement circuits

# **BA3578FS**



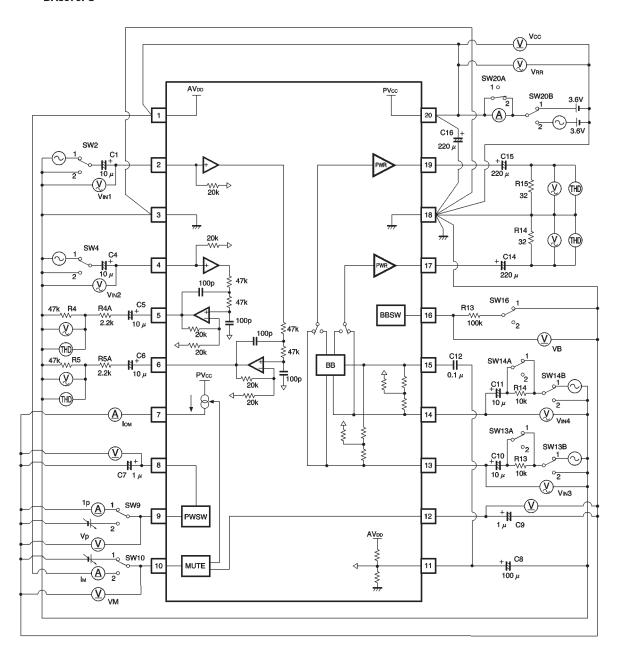
Units:

Resistance :  $\Omega$  ( $\pm 1\%$ )
Capacitance (film) : F ( $\pm 1\%$ )
Capacitance (electrolytic): F ( $\pm 5\%$ )

Fig.1



# **BA3579FS**

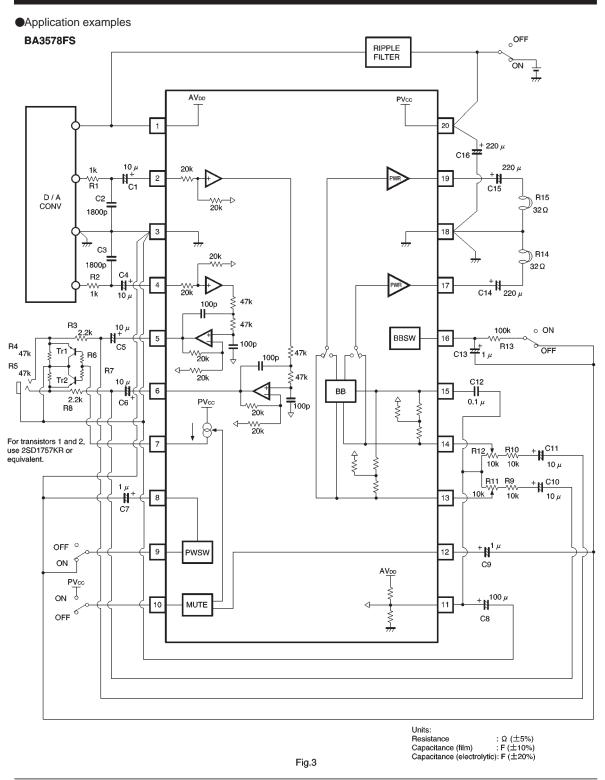


 $\begin{array}{lll} \text{Units:} \\ \text{Resistance} & : \Omega \ (\pm 1\%) \\ \text{Capacitance (film)} & : F \ (\pm 1\%) \\ \text{Capacitance (electrolytic):} & F \ (\pm 5\%) \\ \end{array}$ 

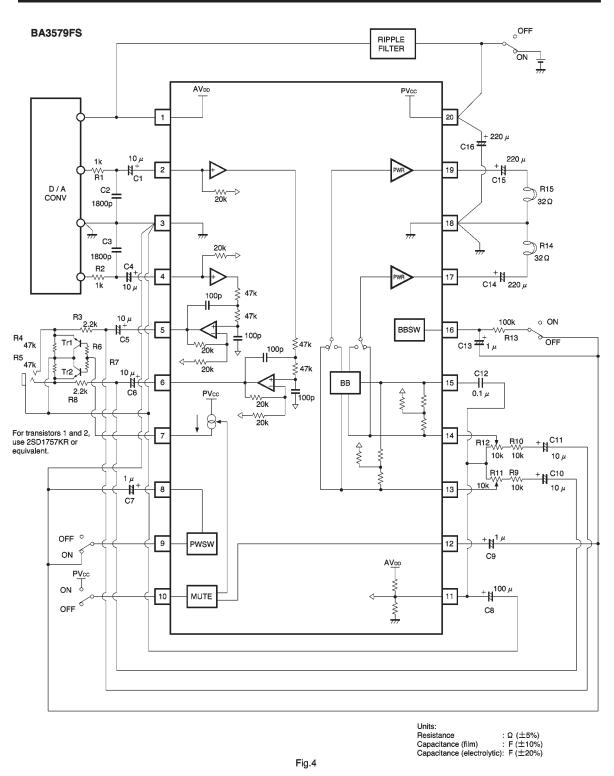
Fig.2

# Measurement conditions

Parameter	Symbol	SW2	SW4	SW9	SW10	SW13 A	SW13 B	SW14 A	SW14 B	SW16	SW20 A	SW20 B
Circuit current	lcc	2	2	1	1	1	2	1	2	1	1	1
Power ON voltage	VP	1	ţ	2	<b>↓</b>	ļ	1	1	ţ	ţ	2	<b>1</b>
Power ON pin current	lР	1	ţ	1	ţ	ļ	1	1	Ţ	ţ	ţ	1
Mute ON voltage	Vм	1	ţ	ţ	2	ļ	1	1	ļ	ţ	ţ	<u> </u>
Mute pin current	lм	ţ	ţ	Ţ	ţ	Ţ	1	1	ļ	ţ	ţ	1
Mute output current	Іом	ţ	ţ	ţ	1	1	1	1	Ţ	ţ	ţ	<b>1</b>
Bass boost OFF voltage	VB	1	ţ	ţ	ţ	1	Ţ	ţ	Ţ	ţ	ţ	1
⟨Line-out⟩												
Voltage gain 1	Gv <sub>1</sub>	1	1	ţ	ţ	Ţ	2	1	2	ţ	ţ	1
Voltage gain difference	ΔGv	ţ	ţ	ţ	ţ	1	1	1	Ţ	ţ	ţ	1
Total harmonic distortion 1	THD <sub>1</sub>	ţ	ţ	ţ	ţ	1	1	1	Ţ	ţ	ţ	1
Maximum output voltage 1	Vом1	ţ	ţ	ţ	ţ	Ţ	Ţ	1	Ţ	ţ	ţ	1
Output noise voltage 1	V <sub>NO1</sub>	2	2	ţ	ţ	Ţ	1	1	ţ	ţ	ţ	1
Channel separation 1	CS <sub>1</sub>	1/2	2/1	ţ	ţ	Ţ	1	1	Ţ	ţ	ţ	1
Ripple rejection 1	RR <sub>1</sub>	2	2	ţ	ţ	Ţ	Ţ	1	Ţ	ţ	ţ	2
〈Headphone out〉												
Voltage gain 2	Gv2	2	2	ţ	ţ	Ţ	1	1	Ţ	ţ	ţ	1
Total harmonic distortion 2	THD <sub>2</sub>	1	Ţ	1	1	Ţ	1	1	1	ţ	ţ	1
Rated output	Po	1	Ţ	1	1	Ţ	1	1	Ţ	1	ţ	1
Output noise voltage 2	V <sub>NO2</sub>	+	ţ	ţ	ţ	ţ	2	1	2	ţ	ţ	1
Channel separation 2	CS <sub>2</sub>	+	ţ	ţ	ţ	ţ	1/2	1	2/1	ţ	ţ	1
Channel separation 3	CS <sub>3</sub>	ţ	ţ	ţ	ţ	1	1	1	Ţ	2	ţ	1
Channel separation 4	CS₄	ţ	ţ	ţ	ţ	2/1	1	1/2	Ţ	1	ţ	1
Mute attenuation	АТТм	ţ	ţ	ţ	2	1	1	1	1	1	ţ	1
Bass boost	ВВ	1	ţ	ţ	ţ	1	1	ţ	Ţ	2	ţ	1
Ripple rejection 2	RR <sub>2</sub>	1	ţ	ţ	ţ	1	2	ţ	2	1	ţ	2



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# Circuit operation

(1) By operating the BA3578FS and BA3579FS according to the timing chart shown in Fig. 5, it is possible to suppress generation of "pop" noise in the headphone output.

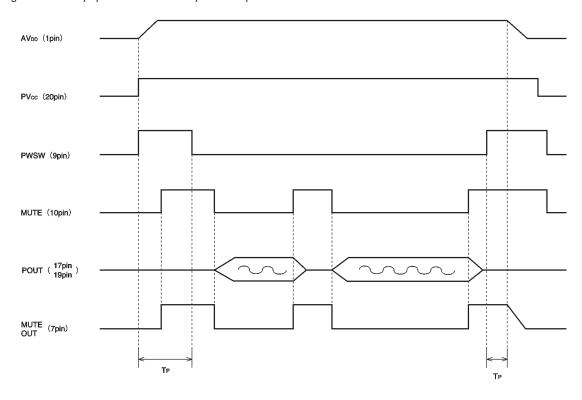
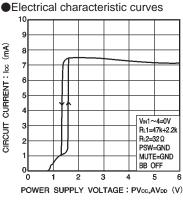


Fig.5 Timing chart

TP 100msec.

(2) The voltage of the BIAS pin (pin 11) for the BA3578FS and BA3579FS is the voltage divided from the AVDD pin (pin 1). There is no current carrying capacity, so do not use it as an operating point for external circuits.



20
BA3579FS(V<sub>N</sub>1,2=0.4V<sub>rms</sub>)

0
BA3578FS(V<sub>N</sub>1,2=0.8V<sub>rms</sub>)

0
BA3578FS(V<sub>N</sub>1,2=0.8V<sub>rms</sub>)

0
PVc=3.6V
AV<sub>0</sub>=3.6V
R<sub>1</sub>=2<sub>2</sub>k<sub>4</sub>47k<sub>Ω</sub>
PSW=SND
MUTE=GND
10
100
1k
10k
10k
10kk

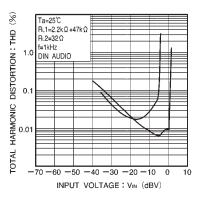
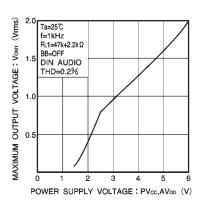
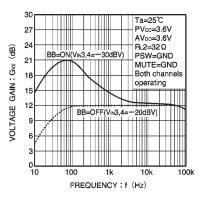


Fig.6 Circuit current vs. power supply voltage

Fig.7 Voltage gain vs.power supply voltage (line-out)

Fig.8 Total harmonic distortion vs. input voltage (line-out)





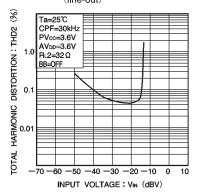
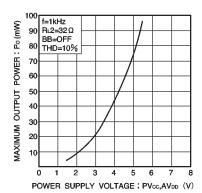


Fig.9 Maximum output voltage vs. power supply voltage (line-out)

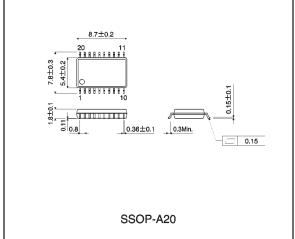
Fig.10 Voltage gain vs. frequency characteristics (headphone-out)

Fig.11 Total harmonic distortion vs. input voltage (headphone-out)



●External dimensions (Unit: mm)

Fig.12 Maximum output voltage vs. power supply voltage (headphone-out)



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