

PI3C3384 PI3C32384 (25 Ω)

2.5V/3.3V, High Bandwidth, Hot Insertion 10-Bit, 2-Port, Bus Switch

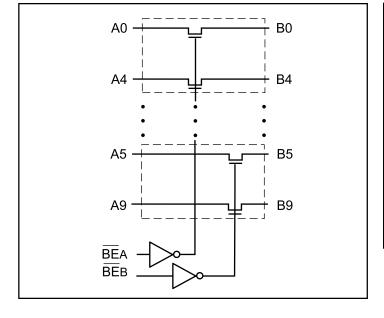
Product Features

- Near-zero propagation delay
- Low noise, 25Ω version (PI3C32384)
- 5Ω switches connect inputs to outputs (PI3C3384)
- High Bandwidth (>400 MHz)
- Permits Hot Insertion.
- Rail-to-Rail, 3.3V or 2.5V Switching
- 5V I/O Tolerant
- 2.5V Supply Voltage Operation
- · Packages available:
 - 24-pin 150-mil wide plastic QSOP (Q)
 - 24-pin 173-mil wide plastic TSSOP (L)

Applications

- · High Bandwidth Data switching
- · Hot Docking

Logic Block Diagram



Product Description

Pericom Semiconductor's PI3C series of logic circuits are produced using the Company's advanced submicron CMOS technology, achieving industry performance.

The PI3C3384, and PI3C32384 are 2.5V or 3.3Volt, high bandwidth 10-bit, 2-port bus switches designed with a low ON resistance allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable (BE) input signal. Two bus enable signals are provided, one for each of the upper and lower five bits of the two 10-bit buses. The PI3C32384 is designed with an internal 25 Ω resistor reducing noise reflection in high-speed applications.

Product Pin Configuration

BEA

Truth Table(1)

Function	BEA	BEB	B0-B4	B5-B9
Disconnect	Н	Н	Hi-Z	Hi-Z
Connect	L	Н	A0-A4	Hi-Z
Connect	Н	L	Hi-Z	A5-A9
Connect	L	L	A0-A4	A5-A9

Note:

- 1. H = High Voltage Level, X = Don't Care,
 - L = Low Voltage Level, Hi-Z = High Impedance

Product Pin Description

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Pin Name	Description
$\overline{BE}A$, $\overline{BE}B$	Bus Enable Inputs (Active LOW)
A0 - A9	Bus A
B0 - A9	Bus B
GND	Ground
V _{CC}	Power



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

<u> </u>
Storage Temperature
Ambient Temperature with Power Applied40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V_{CC} Only) $-0.5V$ to $+4.6V$
Supply Voltage to Ground Potential (Outputs & D/O Only) -0.5V to +4.6V
DC Input Voltage0.5V to +5.5V
DC Output Current
Power Dissipation

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over Operating Range, T_A = -40°C to +85°C, V_{CC} = 3.3V ± 10%)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Le	evel	2.0	_	_	V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Le	vel	-0.5	_	0.8	V
I _{IH}	Input HIGH Current	$V_{CC} = Max., VIN = V_{CC}$		_	_	±1	
I_{IL}	Input LOW Current	V _{CC} = Max., VIN = GND		_	±1	μА	
Iozh	High-Impedence Output Current	$0 \le A, B \le V_{CC}$			_	±1	
V _{IK}	Clamp diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$	$V_{CC} = Min., I_{IN} = -18mA$		-0.73	-1.2	V
		$V_{CC} = Min., V_{IN} = 0.0V$	PI3C3384	_	5	7	
R_{ON} Switch On Resistance ⁽⁴⁾ $V_{CC} = Min.$	$I_{ON} = 48$ mA or 64 mA	PI3C32384	18	28	40		
	Switch Off Resistance	$V_{CC} = Min., V_{IN} = 2.4V$	PI3C3384		8	15	Ω
		$I_{ON} = 15 \text{mA}$	PI3C32384	18	35	48	

Capacitance ($T_A = 25$ °C f = 1 MHz)

Parameters (5)	Description	Description Test Conditions		Units
C _{IN}	Input Capacitance		3.5	
C _{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0V$	5.0	pF
C _{ON}	A/B Capacitance, Switch On		10.0	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25$ °C ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

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5. This parameter is determined by device characterization but is not production tested.

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Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = Max.,$ $V_{IN} = GND \text{ or } V_{CC}$	PI3C3384 PI3C32384		260	500	
ΔI_{CC}	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.,$ $V_{IN} = 3.0V^{(3)}$			_	750	μА

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, $+25^{\circ}C$ ambient.
- 3. Per TTL driven input (control inputs only); A and B pins do not contribute to I_{CC}.
- 4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

PI3C3384/PI3C32384 Switching Characteristics over 3.3V Operating Range

			PI3C	C3384	PI3C	32384	
Parameters	Description	Conditions ⁽¹⁾	Conditions ⁽¹⁾ Com		Co	om.	Units
			Min.	Max.	Min.	Max.	
tPLH tPHL	Propogation Delay ^(2,3) Ax to Bx, Bx to Ax	$C_L = 50 pF$ $R_L = 500 \Omega$	_	0.25	_	1.25	
t _{PZL}	Bus Enable Time BEx to Ax or Bx	$C_L = 50 pF$	1.5	6.5	1.5	6.5	ns
t _{PHZ}	Bus Disable Time BEx to Ax or Bx	$R_{L} = 500\Omega$ $R = 500\Omega$	1.5	5.5	1.5	5.5	

PI3C3384/PI3C32384 Switching Characteristics over 2.5V Operating Range

			PI3C	C3384	PI3C.	32384	
Parameters	Description	$\mathbf{Conditions}^{(1)}$	Co	om.	Co	m.	Units
			Min.	Max.	Min.	Max.	
tplh tphl	Propogation Delay ^(2,3) Ax to Bx, Bx to Ax	$C_{L} = 50pF$ $R_{L} = 500\Omega$	_	0.25	_	1.25	
tPZH tPZL	$\frac{\text{Bus Enable Time}}{\text{BE}x \text{ to Ax or Bx}}$	$C_L = 50 pF$	1.5	9.8	1.5	9.8	ns
t _{PHZ}	Bus Disable Time BEx to Ax or Bx	$R_{L} = 500\Omega$ $R = 500\Omega$	1.5	8.3	1.5	8.3	

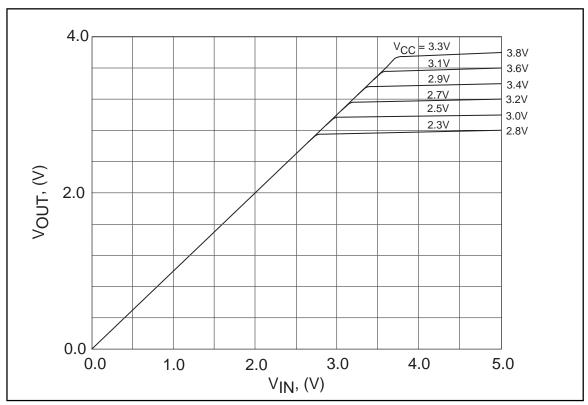
Notes:

- 1. See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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Output Voltage vs. Input Voltage over Various Supply Voltages

Ordering Information

Part	Pin - Package	Dimensions
PI3C3384L	24 - TSSOP (L)	−40°C to +85°C
PI3C3384Q	24 - QSOP (Q)	−40°C to +85°C

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