TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PA14FU

Dual Schmitt Inverter

Features

- Operating voltage range: V_{CC} = 1.8~3.6 V
- High-speed operation: t_{pd} = 4.0 ns (max) at V_{CC} = 3.0~3.6 V

 t_{pd} = 4.3 ns (max) at V_{CC} = 2.3~2.7 V

 t_{pd} = 8.6 ns (max) at V_{CC} = 1.8 V

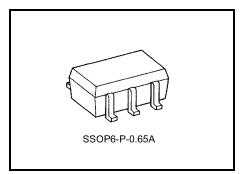
· High-level output current:

 I_{OH}/I_{OL} = ±24 mA (min) at V_{CC} = 3.0 V

 I_{OH}/I_{OL} = ±18 mA (min) at V_{CC} = 2.3 V

 I_{OH}/I_{OL} = ±6 mA (min) at V_{CC} = 1.8 V

- 3.6-V tolerant inputs
- 3.6-V power down protection outputs



Weight: 0.0068 g (typ.)

Maximum Ratings (Ta = 25°C)

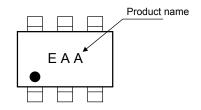
Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	VIN	-0.5~4.6	V
		-0.5~4.6 (Note 1)	
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5 (Note 2)	V
Input diode current	l _{IK}	-50	mA
Output diode current	lok	-50 (Note 3)	mA
DC output current	lout	±50	mA
Power dissipation	P _D	200	mW
DC V _{CC} /ground current	Icc	±100	mA
Storage temperature	T _{stg}	-65~150	ů

Note 1: $V_{CC} = 0 V$

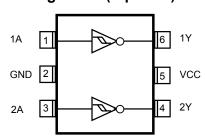
Note 2: High or Low state. $I_{\mbox{OUT}}$ absolute maximum rating must be observed.

Note 3: VOUT < GND

Marking



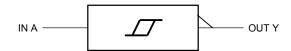
Pin Assignment (top view)



Truth Table

Α	Y
L	Н
Н	L

IEC Logic Symbol



Recommended Operating Conditions

Characteristics	Symbol	Value	Unit
Power supply voltage	Voc	1.8~3.6	V
Fower supply voltage	V _{CC}	1.2~3.6 (Note 4)	V
Input voltage	VIN	-0.3~3.6	V
Output voltage	V _{OUT}	0~3.6 (Note 5)	V
Output voltage		0~V _{CC} (Note 6)	V
		±24 (Note 7)	
Output Current	IOH/IOL	±18 (Note 8)	mA
		±6 (Note 9)	
Operating temperature	T _{opr}	−40~85	°C

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state

Note 7: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 9: $V_{CC} = 1.8 \text{ V}$



DC Electrical Characteristics (Ta = $-40\sim85^{\circ}$ C, 2.7 V < V_{CC} \leq 3.6 V)

Characteristics		Symbol Test Condition			Min	Max	Unit	
		Symbol	163	rest condition		IVIIII	IVIAX	Offic
	High level	VP			3.6	-	2.2	V
Threshold voltage	High level	VP		_	3.0	-	2.0	v
Threshold voltage	Low level	VN			3.6	0.8	1	V
	LOW level	٧N		_	3.0	0.7	ı	٧
Hysteresys Voltage		VH			3.6	0.3	1.2	V
Trysteresys voltage		٧H			3.0	0.3	1.2	٧
				$I_{OH} = -100 \mu A$	2.7~3.6	V _{CC} - 0.2		
High-Level Output Volt	age	V _{OH}	$V_{IN} = V_{IL} \\$	$I_{OH} = -12 \text{ mA}$	2.7	2.2		V
				$I_{OH} = -18 \text{ mA}$	3.0	2.4		
				$I_{OH} = -24 \text{ mA}$	3.0	2.2		
				$I_{OL} = 100 \ \mu A$	2.7~3.6		0.2	
Low-Level Output Voltage		Vol		$I_{OL} = 12 \text{ mA}$	2.7		0.4	
Low-Level Output Volta	age	VOL	$V_{IN} = V_{IH}$	$I_{OL} = 18 \text{ mA}$	3.0		0.4]
				$I_{OL} = 24 \text{ mA}$	3.0		0.55	
Input Leakage Current I _{IN}		I _{IN}	V _{IN} = 0~3.6 V	V _{IN} = 0~3.6 V			±5.0	μΑ
Power-off Leakage Current I _{OFF}		V _{IN} , V _{OUT} = 0~	-3.6 V	0		10.0	μΑ	
Quiescent Supply Current		loo	V _{IN} = V _{CC} or GND		2.7~3.6		20.0	
Quiescent Supply Cult	CIIL	Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		2.7~3.6		±20.0	μΑ
Increase in I _{CC} per Inp	ut	Δl _{CC}	$V_{IH} = V_{CC} - 0.0$	6 V	2.7~3.6	_	750	

DC Electrical Characteristics (Ta = $-40 \sim 85$ °C, 2.3 V \leq V_{CC} \leq 2.7 V)

Characteristics		Symbol	Symbol Test Condition			Min	Max	Unit
		Symbol	163	rest condition		IVIIII	IVIAX	Onit
Threshold voltage	High level	V _P		_	2.3	-	1.8	V
Threshold voltage	Low level	V _N		_	2.3	0.5	_	v
Hysteresys Voltage		V _H		_	2.3	0.3	1.0	٧
				$I_{OH} = -100 \mu A$	2.3~2.7	V _{CC} - 0.2	_	
High-Level Output Volt	age	V _{OH}	$V_{IN} = V_{II}$	$I_{OH} = -6 \text{ mA}$	2.3	2.0	_	
				$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	٧
				I _{OH} = -18 mA	2.3	1.7	_	
				I _{OL} = 100 μA	2.3~2.7	_	0.2	
Low-Level Output Volta	age	VoL	$V_{IN} = V_{IH}$	I _{OL} = 12 mA	2.3	_	0.4	
				I _{OL} = 18 mA	2.3	_	0.6	
Input Leakage Current		I _{IN}	V _{IN} = 0~3.6 V		2.3~2.7	_	±5.0	μА
Power-off Leakage Cu	rrent	I _{OFF}	V _{IN} , V _{OUT} = 0	~3.6 V	0	_	10.0	μА
Ouisesent Cumply Cum	- mt	1	$V_{IN} = V_{CC}$ or C	V _{IN} = V _{CC} or GND		_	20.0	
Quiescent Supply Curr	eni	Icc	$V_{CC} \le (V_{IN}, V_{CC})$	_{DUT}) ≦ 3.6 V	2.3~2.7		±20.0	μΑ



DC Electrical Characteristics (Ta = $-40\sim85^{\circ}$ C, 1.8 V \leq V_{CC} < 2.3 V)

Characteristics		Symbol	Too	Test Condition		Min	Max	Unit
		Symbol	163			IVIIII		
Threshold voltage	High level	VP		_	1.8	_	1.4	V
Threshold voltage	Low level	V _N		_	1.8	0.25	_	V
Hysteresys Voltage		VH		_	1.8	0.2	0.95	V
High-Level Output Vo	Itage	V _{OH}	$V_{IN} = V_{IL}$	I _{OH} = -100 μA	1.8	V _{CC} - 0.2	_	
				$I_{OH} = -6 \text{ mA}$	1.8	1.4	_	V
		V	V _{IN} = V _{IH}	$I_{OL} = 100 \ \mu A$	1.8	_	0.2	
Low-Level Output Vol	laye	V _{OL}	VIN = VIH	$I_{OL} = 6 \text{ mA}$	1.8	_	0.3	
Input Leakage Currer	it	I _{IN}	V _{IN} = 0~3.6 V		1.8	_	±5.0	μА
Power-off Leakage C	urrent	l _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μА
Quiescent Supply Current		loo	V _{IN} = V _{CC} or GND		1.8	_	20.0	μА
Quiescent Supply Cul	IIGIIL	Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		1.8	_	±20.0	μΑ

AC Electrical Characteristics (Ta = $-40 \sim 85$ °C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	t _{pLH}	(Figure 1 and 2)	1.8	1.0	8.6	
Propagation delay time			2.5 ± 0.2	0.8	4.3	ns
	^t pHL		3.3 ± 0.3	0.6	4.0	

For $C_L = 50$ pF, add approximately 300 ps to the AC maximum specification.



Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition			Тур.	Unit
Characteristics	Symbol			V _{CC} (V)	τyp.	Offic
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	1.8	0.25	
Quiet output maximum dynamic V _{OL}	V_{OLP}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	2.5	0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	3.3	8.0	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	1.8	-0.25	
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	2.5	-0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	3.3	-0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	1.8	1.5	
Quiet output minimum dynamic V _{OH}	V_{OHV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	2.5	1.9	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (I	Note 10)	3.3	2.2	

Note 10: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

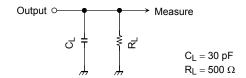
Characteristics	Symbol	Test Condition		V _{CC} (V)	TYP.	Unit
Input Capacitance	C _{IN}	_		1.8, 2.5, 3.3	4	pF
Power Dissipation Capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 11)	1.8, 2.5, 3.3	27	pF

Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

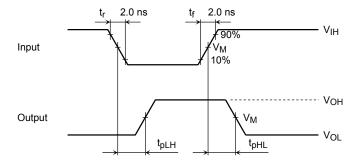
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Figure 1 Test Circuit



AC Waveforms

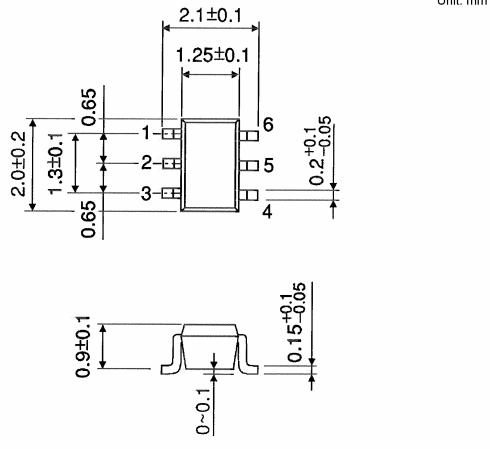
Figure 2 t_{pLH}, t_{pHL}



Symbol	Vcc						
Symbol	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 V				
V_{IH}	2.7 V	V _{CC}	V _{CC}				
V_{M}	1.5 V	V _{CC} /2	V _{CC} /2				

Package Dimensions

SSOP6-P-0.65A Unit: mm



Weight: 0.0068 g (typ.)

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