TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PA04FU

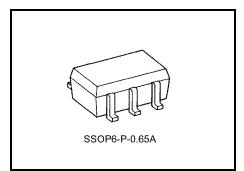
Dual Inverter

Features

- Operating voltage range: V_{CC} = 1.8~3.6 V
- High-speed operation: t_{pd} = 2.8 ns (max) at V_{CC} = 3.0~3.6 V
 - t_{pd} = 3.7 ns (max) at V_{CC} = 2.3~2.7 V t_{pd} = 7.4 ns (max) at V_{CC} = 1.8 V
- High-level output current:

 $I_{OH}/I_{OL} = \pm 24$ mA (min) at V_{CC} = 3.0 V $I_{OH}/I_{OL} = \pm 18$ mA (min) at V_{CC} = 2.3 V $I_{OH}/I_{OL} = \pm 6$ mA (min) at V_{CC} = 1.8 V

- 3.6-V tolerant inputs
- 3.6-V power down protection outputs



Weight: 0.0068 g (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	VIN	-0.5~4.6	V
		-0.5~4.6 (Note 1)	
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5 (Note 2)	V
Input diode current	Ι _{ΙΚ}	-50	mA
Output diode current	I _{OK}	-50 (Note 3)	mA
DC output current	I _{OUT}	±50	mA
Power dissipation	PD	200	mW
DC V _{CC} /ground current	ICC	±100	mA
Storage temperature	T _{stg}	-65~150	°C

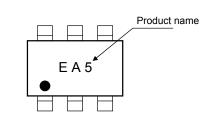
Note 1: $V_{CC} = 0 V$

Note 2: High or Low state.

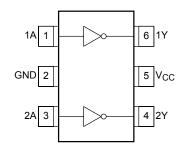
I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

Marking



Pin Assignment (top view)

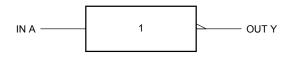


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Truth Table

IEC Logic Symbol

А	Y
L	Н
Н	L



Recommended Operating Conditions

Characteristics	Symbol	Value	Unit
Power supply voltage	Vcc	1.8~3.6	V
Power supply vollage	VCC	1.2~3.6 (Note 4)	v
Input voltage	VIN	-0.3~3.6	V
Output voltage	Vour	0~3.6 (Note 5)	V
Output voltage	Vout	0~V _{CC} (Note 6)	v
		±24 (Note 7)	
Output Current	I _{OH} /I _{OL}	±18 (Note 8)	mA
		±6 (Note 9)	
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	d _t /d _v	0~10 (Note 10)	ns/V

- Note 4: Data retention only
- Note 5: $V_{CC} = 0 V$
- Note 6: High or Low state
- Note 7: $V_{CC} = 3.0 \sim 3.6 \text{ V}$
- Note 8: $V_{CC} = 2.3 \sim 2.7 \text{ V}$
- Note 9: $V_{CC} = 1.8 V$
- Note 10: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

DC Electrical Characteristics (Ta = –40~85°C, 2.7 V < V_{CC} \leq 3.6 V)

Characteristics	Symbol	Tor	Test Condition		Min	Max	Unit	
Characteristics	Symbol	103	Condition	V _{CC} (V)	WIIII	IVIAA	Unit	
High-Level Input Voltage	VIH		—	2.7~3.6	2.0	_	V	
Low-Level Input Voltage	V _{IL}		_	2.7~3.6	_	0.8	v	
			I _{OH} = -100 μA	2.7~3.6	V _{CC} - 0.2	_		
High-Level Output Voltage	V _{OH}	$V_{IN} = V_{IL}$	I _{OH} = -12 mA	2.7	2.2	_		
			I _{OH} = -18 mA	3.0	2.4	_		
			I _{OH} = -24 mA	3.0	2.2	_	V	
			I _{OL} = 100 μA	2.7~3.6	_	0.2		
Law Lavel Output) (eltage				$I_{OL} = 12 \text{ mA}$	2.7	_	0.4	
Low-Level Output Voltage	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 18 mA	3.0	_	0.4		
			$I_{OL} = 24 \text{ mA}$	3.0	_	0.55		
Input Leakage Current	I _{IN}	V _{IN} = 0~3.6 V		2.7~3.6	_	±5.0	μA	
Power-off Leakage Current	IOFF	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μA	
Outine and Duranty Outine at	1	$V_{IN} = V_{CC}$ or C	$V_{IN} = V_{CC}$ or GND		_	20.0		
Quiescent Supply Current	Icc	$V_{CC} \leqq (V_{IN}, V_{OUT}) \leqq 3.6 \text{ V}$		2.7~3.6		±20.0	μA	
Increase in I _{CC} per Input	Δl _{CC}	$V_{IH} = V_{CC} - 0.$	6 V	2.7~3.6	_	750		

DC Electrical Characteristics (Ta = -40~85°C, 2.3 V \leq V_{CC} \leq 2.7 V)

Characteristics	Symbol	Те	Test Condition		Min	Max	Unit
High-Level Input Voltage	V _{IH}		_	2.3~2.7	1.6		v
Low-Level Input Voltage	V _{IL}		—	2.3~2.7	_	0.7	v
			I _{OH} = -100 μA	2.3~2.7	V _{CC} - 0.2	_	
High-Level Output Voltage	VOH	$V_{IN} = V_{IL}$	I _{OH} = -6 mA	2.3	2.0	_	-
			I _{OH} = -12 mA	2.3	1.8		
			I _{OH} = -18 mA	2.3	1.7	_	V
			I _{OL} = 100 μA	2.3~2.7	_	0.2	
Low-Level Output Voltage	V _{OL}	$V_{IN} = V_{IH} \\$	$I_{OL} = 12 \text{ mA}$	2.3	_	0.4	
			$I_{OL} = 18 \text{ mA}$	2.3	_	0.6	
Input Leakage Current	I _{IN}	V _{IN} = 0~3.6 V		2.3~2.7	_	±5.0	μA
Power-off Leakage Current	I _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μA
		$V_{IN} = V_{CC}$ or ($V_{IN} = V_{CC}$ or GND		_	20.0	
Quiescent Supply Current	Icc	$V_{CC} \leq (V_{IN}, V_{IN})$	OUT) ≦ 3.6 V	2.3~2.7	_	±20.0	μA

DC Electrical Characteristics (Ta = $-40 \sim 85^{\circ}$ C, 1.8 V $\leq V_{CC} < 2.3$ V)

Characteristics	Symbol	Test Condition			Min	Мах	Unit
Unaracteristics	Gymbol	reare	ondition	V _{CC} (V)	IVIIII	Max	Onit
High-Level Input Voltage	V _{IH}		—		$0.7 \times V_{CC}$	_	V
Low-Level Input Voltage	V _{IL}	_		1.8~2.3		$0.2 \times V_{CC}$	v
High-Level Output Voltage	V _{OH}	$V_{IN} = V_{IL}$	I _{OH} = -100 μA	1.8	V _{CC} - 0.2	_	
	-		I _{OH} = -6 mA	1.8	1.4	_	v
Low-Level Output Voltage	V _{OL}	<u> </u>	$I_{OL} = 100 \ \mu A$	1.8	_	0.2	
Low-Level Output Voltage	VOL	VIN = VIH	$V_{IN} = V_{IH}$ $I_{OL} = 6 \text{ mA}$		_	0.3	
Input Leakage Current	I _{IN}	V _{IN} = 0~3.6 V		1.8	_	±5.0	μA
Power-off Leakage Current	I _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μA
Quiescent Cumply Current		$V_{IN} = V_{CC}$ or GND		1.8	_	20.0	
Quiescent Supply Current	Icc	$V_{CC} \leq (V_{IN}, V_{OUT})$	-) ≦ 3.6 V	1.8	_	±20.0	μA

AC Electrical Characteristics (Ta = -40~85°C, input t_r = t_f = 2.0 ns, C_L = 30 pF, R_L = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	+		1.8	1.0	7.4	
Propagation delay time	^t pLH t _{pHL}	(Figure 1 and 2)	2.5 ± 0.2	0.8	3.7	ns
			$\textbf{3.3}\pm\textbf{0.3}$	0.6	2.8	

For $C_L = 50 \text{ pF}$, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25° C, input: t_r = t_f = 2.0 ns, C_L = 30 pF)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 11)	1.8	0.25	
Quiet output maximum dynamic VOL	V _{OLP}	$V_{IN} = 2.5 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 11)	2.5	0.6	ns
		$V_{IN} = 3.3 V, V_{IL} = 0 V$	(Note 11)	3.3	0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 11)	1.8	-0.25	
Quiet output minimum dynamic V_{OL}	VOLV	$V_{IN} = 2.5 V, V_{IL} = 0 V$	(Note 11)	2.5	-0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 11)	3.3	-0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 11)	1.8	1.5	
Quiet output minimum dynamic V_{OH}	V _{OHV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 11)	2.5	1.9	ns
		$V_{IN} = 3.3 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 11)	3.3	2.2	

Note 11: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition			TYP.	Unit
Characteristics	Symbol	Test Condition		V _{CC} (V)	ITE.	Unit
Input Capacitance	C _{IN}	_		1.8, 2.5, 3.3	5	pF
Power Dissipation Capacitance	CPD	f _{IN} = 10 MHz	(Note 12)	1.8, 2.5, 3.3	18	pF

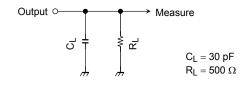
Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

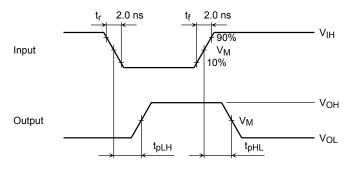
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AC Test Circuit

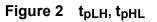




AC Waveforms



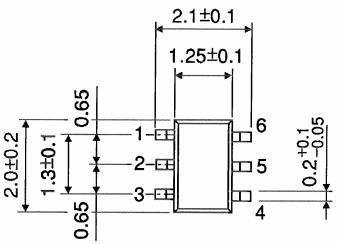
Symbol	V _{CC}						
Symbol	$3.3\pm0.3~\text{V}$	$2.5\pm0.2~\text{V}$	1.8 V				
VIH	2.7 V	V _{CC}	V _{CC}				
VM	1.5 V	V _{CC} /2	V _{CC} /2				

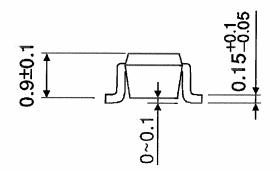


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Package Dimensions







Weight: 0.0068 g (typ.)

Unit: mm

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