

# NLU1GT50

## Non-Inverting Buffer / CMOS Logic Level Shifter

### TTL-Compatible Inputs

The NLU1GT50 is an advanced CMOS high-speed non-inverting buffer in ultra-small footprint.

The device input is compatible with TTL-type input thresholds and the output has a full 5.0 V CMOS level output swing.

The NLU1GT50 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

#### Features

- Designed for 1.65 to 5.5 V  $V_{CC}$  Operation
- High Speed:  $t_{PD} = 3.5$  ns (Typ) @  $V_{CC} = 5.0$  V
- Low Power Dissipation:  $I_{CC} = 1$   $\mu$ A (Max) at  $T_A = 25^\circ$ C
- TTL-Compatible Input:  $V_{IL} = 0.8$  V;  $V_{IH} = 2.0$  V,  $V_{CC} = 5.0$  V
- CMOS-Compatible Output:  
 $V_{OH} > 0.8 V_{CC}$ ;  $V_{OL} < 0.1 V_{CC}$  @ Load
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Ultra-Small Pb-Free Package
- This is a Pb-Free Device

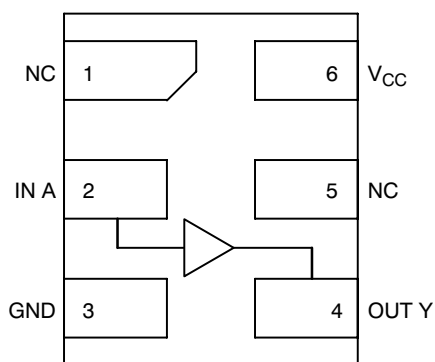


Figure 1. Pinout (Top View)

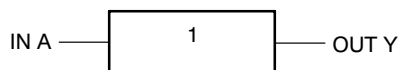


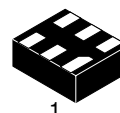
Figure 2. Logic Symbol



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#### MARKING DIAGRAM



UDFN6  
MU SUFFIX  
CASE 517AA



LE, U = Device Marking  
M = Date Code  
▪ = Pb-Free Package

#### PIN ASSIGNMENT

1	NC
2	IN A
3	GND
4	OUT Y
5	NC
6	$V_{CC}$

#### FUNCTION TABLE

A	Y
L	L
H	H

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

# NLU1GT50

## MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	DC Supply Voltage	-0.5 to +7.0	V
V <sub>IN</sub>	DC Input Voltage	-0.5 to +7.0	V
V <sub>OUT</sub>	DC Output Voltage	-0.5 to +7.0	V
I <sub>IK</sub>	DC Input Diode Current V <sub>IN</sub> < GND	-20	mA
I <sub>OK</sub>	DC Output Diode Current V <sub>OUT</sub> < GND	±20	mA
I <sub>O</sub>	DC Output Source/Sink Current	±12.5	mA
I <sub>CC</sub>	DC Supply Current Per Supply Pin	±25	mA
I <sub>GND</sub>	DC Ground Current per Ground Pin	±25	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
T <sub>L</sub>	Lead Temperature, 1 mm from Case for 10 Seconds	TBD	°C
T <sub>J</sub>	Junction Temperature Under Bias	TBD	°C
θ <sub>JA</sub>	Thermal Resistance (Note 1) UDFN6	TBD	°C/W
P <sub>D</sub>	Power Dissipation in Still Air at 85°C UDFN6	TBD	mW
MSL	Moisture Sensitivity	Level 1	
F <sub>R</sub>	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
V <sub>ESD</sub>	ESD Withstand Voltage Human Body Mode (Note 2) Machine Model (Note 3) Charged Device Model (Note 4)	> 2000 > 200 N/A	V
I <sub>LATCHUP1</sub>	Latchup Performance Above V <sub>CC</sub> and Below GND at 125 °C (Note 5)	±500	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.
2. Tested to EIA / JESD22-A114-A.
3. Tested to EIA / JESD22-A115-A.
4. Tested to JESD22-C101-A.
5. Tested to EIA / JESD78.

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage	1.65	5.5	V
V <sub>IN</sub>	Digital Input Voltage	0	5.5	V
V <sub>OUT</sub>	Output Voltage	0	5.5	V
T <sub>A</sub>	Operating Free-Air Temperature	-55	+125	°C
Δt/ΔV	Input Transition Rise or Fall Rate V <sub>CC</sub> = 3.3 V ± 0.3 V V <sub>CC</sub> = 5.0 V ± 0.5 V	0 0	100 20	ns/V

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## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	T <sub>A</sub> = 25 °C			T <sub>A</sub> = +85°C		T <sub>A</sub> = -55°C to +125°C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V <sub>IH</sub>	Low-Level Input Voltage		1.65 to 2.29	0.50 x V <sub>CC</sub>			0.50 x V <sub>CC</sub>				V
			2.3 to 2.99	0.45 x V <sub>CC</sub>			0.45 x V <sub>CC</sub>				
			3.0	1.4			1.4				
			4.5 to 5.5	2.0			2.0				
V <sub>IL</sub>	Low-Level Input Voltage		1.65 to 2.29				0.10 x V <sub>CC</sub>			0.10 x V <sub>CC</sub>	V
			2.3 to 2.99				0.15 x V <sub>CC</sub>			0.15 x V <sub>CC</sub>	
			3.0				0.53			0.53	
			4.5 to 5.5				0.8			0.8	
V <sub>OH</sub>	High-Level Output Voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	1.65 to 2.99	V <sub>CC</sub> - 0.1			V <sub>CC</sub> - 0.1			V <sub>CC</sub> - 0.1	V
		I <sub>OH</sub> = -50 μA	3.0	2.9	3.0	2.9			2.9		
			4.5	4.4	4.5	4.4			4.4		
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>OH</sub> = -4 mA I <sub>OH</sub> = -8 mA	3.0 4.5	2.58 3.94			2.48 3.80			2.34 3.66	
V <sub>OL</sub>	Low-Level Output Voltage	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>OL</sub> = 50 μA	1.65 to 2.99		0	0.1		0.1		0.1	V
			3.0		0	0.1		0.1		0.1	
			4.5		0	0.1		0.1		0.1	
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>OL</sub> = 4 mA I <sub>OL</sub> = 8 mA	3.0 4.5			0.36 0.36		0.44 0.44		0.52 0.52	
I <sub>IN</sub>	Input Leakage Current	0 = V <sub>IN</sub> = 5.5 V	0 to 5.5			±0.1		±1.0		±1.0	μA
I <sub>CC</sub>	Quiescent Supply Current	0 = V <sub>IN</sub> = V <sub>CC</sub>	5.5			1.0		20		40	μA
I <sub>CC</sub> T	Quiescent Supply Current	V <sub>IN</sub> = 3.4 V	5.5			1.35		1.50		1.65	mA
I <sub>OPD</sub>	Output Leakage Current	V <sub>OUT</sub> = 5.5 V	0.0			0.5		5.0		10	μA

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## AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0$ n)

Symbol	Parameter	$V_{CC}$ (V)	Test Condition	$T_A = 25^\circ\text{C}$			$T_A = +85^\circ\text{C}$		$T_A = -55^\circ\text{C}$ to $+125^\circ\text{C}$		Unit
				Min	Typ	Max	Min	Max	Min	Max	
$t_{PLH}$ , $t_{PHL}$	Propagation Delay, Input A to Output Y	1.65 to 1.95	$C_L = 15$ pF			16.6		18.0		22.0	ns
		2.3 to 2.7	$C_L = 15$ pF			13.3		14.5		17.5	
			$C_L = 50$ pF			19.5		22.0		25.5	
		3.0 to 3.6	$C_L = 15$ pF		4.5	10.0		11.0		13.0	
			$C_L = 50$ pF		6.3	13.5		15.0		17.5	
		4.5 to 5.5	$C_L = 15$ pF		3.5	6.7		7.5		8.5	
$C_L = 50$ pF			4.3	7.7		8.5		9.5			
$C_{IN}$	Input Capacitance				5	10		10		10.0	pF
$C_{PD}$	Power Dissipation Capacitance (Note 6)	5.0				12					pF

6.  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation  $I_{CC(OPR)} = C_{PD} \cdot V_{CC} \cdot f_{in} + I_{CC}$ .  $C_{PD}$  is used to determine the no-load dynamic power consumption:  $P_D = C_{PD} \cdot V_{CC}^2 \cdot f_{in} + I_{CC} \cdot V_{CC}$ .

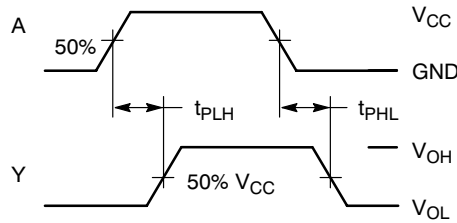
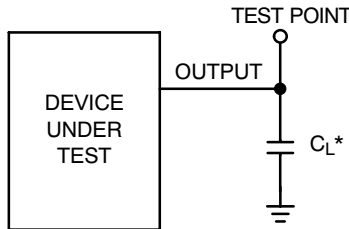


Figure 3. Switching Waveforms



\*Includes all probe and jig capacitance

Figure 4. Test Circuit

## ORDERING INFORMATION

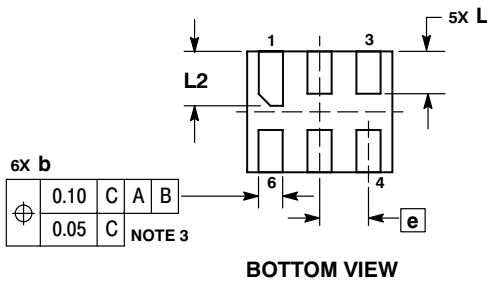
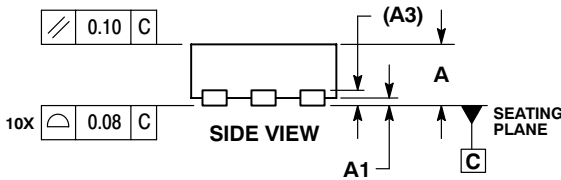
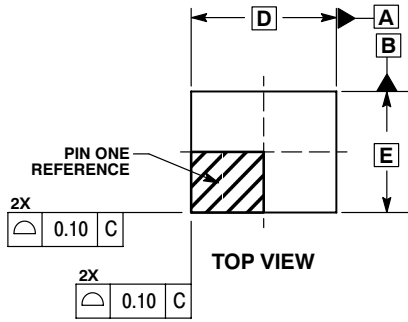
Device	Package	Shipping†
NLU1GT50MUTCG	UDFN6 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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## PACKAGE DIMENSIONS

UDFN6, 1.2x1.0, 0.4P  
CASE 517AA-01  
ISSUE B

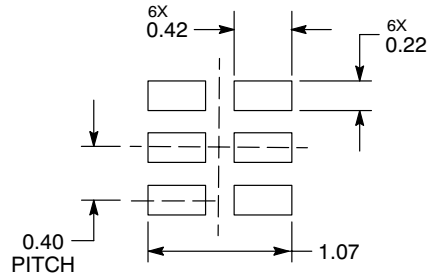


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 mm FROM TERMINAL.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.127	REF
b	0.15	0.25
D	1.20	BSC
E	1.00	BSC
e	0.40	BSC
L	0.30	0.40
L2	0.40	0.50

**MOUNTING FOOTPRINT\***



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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