TOSHIBA

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7SU04F, TC7SU04FU

# **INVERTER**

The TC7SU04 is a high speed C2MOS INVERTER fabricated with silicon gate C2MOS technology.

It achieves high speed operation similar to equivalent LSTTL while maintaining the C2MOS low power dissipation.

The internal circuit is composed of single stages inverter, it can be applied for crystal oscillation.

The input is equipped with protection circuits against static discharge or transient excess voltage.

Output currents are 1/2 compared to TC74HC series models.

#### **FEATURES**

•	High Speed		$t_{pd} = 7ns$ (Typ.) at
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$$V_{CC} = 5V$$

• Low Power Dissipation ............ 
$$I_{CC} = 1\mu A$$
 (Max.) at

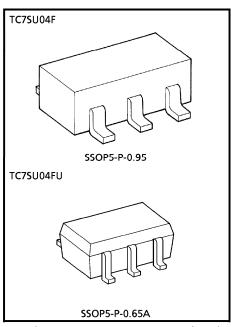
$$Ta = 25^{\circ}C$$

$$=28\%$$
 V<sub>CC</sub> (Min.

• Symmetrical Output Impedance ... 
$$|I_{OH}| = I_{OL}$$
  
= 2mA (Min.)

Balanced Propagation Delays ...... t<sub>pLH</sub>≒t<sub>pHL</sub>

Wide Operating Voltage Range ...  $V_{CC(opr)} = 2 \sim 6V$ 

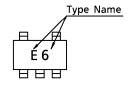


Weight SSOP5-P-0.95 : 0.016g (Typ.) SSOP5-P-0.65A: 0.006g (Typ.)

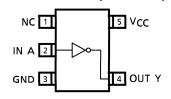
#### **MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	V <sub>C</sub> C	-0.5~7	V
DC Input Voltage	V <sub>IN</sub>	-0.5~V <sub>CC</sub> +0.5	<b>V</b>
DC Output Voltage	Vout	-0.5~V <sub>CC</sub> + 0.5	<b>V</b>
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	loк	± 20	mΑ
DC Output Current	lout	± 12.5	mA
DC V <sub>CC</sub> /Ground Current	lcc	± 25	mA
Power Dissipation	PD	200	mW
Storage Temperature	T <sub>stg</sub>	- 65~150	°C
Lead Temperature (10s)	TL	260	ů

#### **MARKING**



# PIN ASSIGNMENT (TOP VIEW)



# LOGIC DIAGRAM



#### **RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	VCC	2~6	V
Input Voltage	VIN	0~V <sub>CC</sub>	V
Output Voltage	Vout	0~V <sub>CC</sub>	V
Operating Temperature	T <sub>opr</sub>	<b>-40∼85</b>	°C

#### DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST CONDITION			Ta = 25°C			Ta = -40~85°C		UNIT
CHARACTERISTIC	JINBOL			Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	OIVII
Himb Lovel				2.0	1.7	_	_	1.7	_	
High-Level	∨ <sub>IH</sub>		_	4.5	3.6	_	—	3.6	—	V
Input Voltage				6.0	4.8	_	—	4.8	_	
Low Lovel				2.0	_	_	0.3	_	0.3	
Low-Level	V <sub>IL</sub>		_	4.5	<b>—</b>	_	0.9	_	0.9	V
Input Voltage				6.0	_	_	1.2	_	1.2	
		V <sub>IN</sub> = V <sub>IL</sub>		2.0	1.8	2.0	_	1.8	_	
llimb Lovel	Voн		$I_{OH} = -20\mu A$	4.5	4.0	4.5	—	4.0	_	
High-Level				6.0	5.5	5.9	_	5.5	_	V
Output Voltage			$I_{OH} = -2mA$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -2.6 mA$	6.0	5.68	5.80	_	5.63	_	
	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>		2.0	_	0.0	0.2	_	0.2	
l and land			$I_{OL} = 20 \mu A$	4.5	_	0.0	0.2	_	0.5	
Low-Level				6.0	_	0.0	0.5	_	0.5	V
Output Voltage			I <sub>OL</sub> = 2mA	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 2.6 mA$	6.0	_	0.18	0.26	_	0.33	
Input Leakage	lini	\/\r\ - \/\cc\d	or GND	6.0			± 0.1		± 1.0	
Current		$V_{IN} = V_{CC}$ or GND		0.0					- 1.0	
Quiescent Supply Current	lcc	V <sub>IN</sub> = V <sub>CC</sub> o	or GND	6.0	_	_	1.0	_	10.0	μΑ

Output currents are 1/2 compared to TC74HC series models.

AC ELECTRICAL CHARACTERISTICS ( $C_L = 15pF$ , Input $t_r = t_f = 6n$	1S, $V \subset C = 5V$ )
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CHARACTERISTIC	SYMBOL	TEST CONDITION	Т	UNIT		
CHARACTERISTIC	3 TIVIBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Transition Time	t <sub>TLH</sub> t <sub>THL</sub>	_	_	5	10	ns
Propagation Delay Time	t <sub>pLH</sub> t <sub>pHL</sub>	_	_	7	15	ns

# AC ELECTRICAL CHARACTERISTICS ( $C_L = 50pF$ , Input $t_f = t_f = 6ns$ )

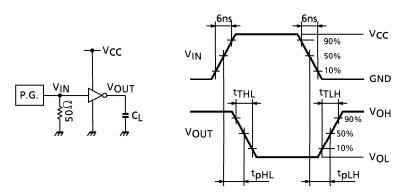
CHARACTERISTIC	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC	STIVIBOL			MIN.	TYP.	MAX.	MIN.	MAX.	OIVIII
Output Transition	t		2.0	_	50	125	_	155	
Time	<sup>†</sup> TLH	<u> </u>	4.5	_	14	25	_	31	ns
Tille	<sup>t</sup> THL		6.0	_	12	21	_	26	
Propagation Delay			2.0	_	48	100	_	125	
Time	t <sub>pLH</sub>	<u> </u>	4.5	<b>—</b>	12	20	<b>—</b>	25	ns
Time	t <sub>pHL</sub>		6.0	<b> </b>	9	17	<b> </b>	21	
Input Capacitance	CIN	_		_	5	10	_	10	
Power Dissipation Capacitance	C <sub>PD</sub>	(Note 1)		_	10	_	_	_	pF

Note 1: C<sub>PD</sub> defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit).

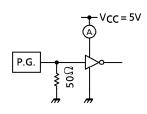
Average operating current can be obtained by the equation hereunder.

 $ICC (opr) = CPD \cdot VCC \cdot fIN + ICC$ 

# **SWITCHING CHARACTERISTICS TEST CIRCUIT**



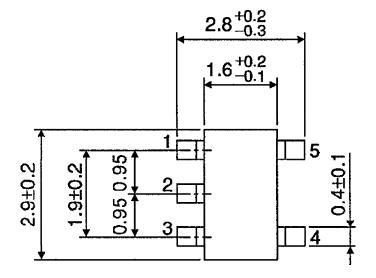
# ICC (opr) TEST CIRCUIT

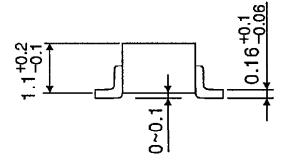


Input waveform is the same as that in case of switching characteristics test.

# PACKAGE DIMENSIONS SSOP5-P-0.95

Unit: mm

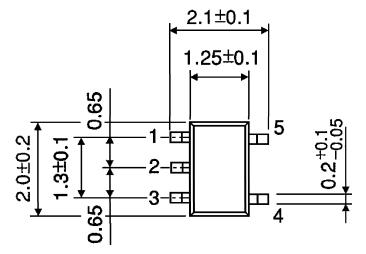


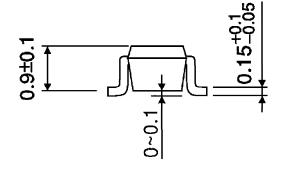


Weight: 0.016g (Typ.)

# PACKAGE DIMENSIONS SSOP5-P-0.65A

Unit: mm





Weight: 0.006g (Typ.)

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