TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SA08F,TC7SA08FU

2-Input AND Gate

Features

Low voltage operation: V_{CC} = 1.8~3.6 V

• High speed operation : t_{pd} = 2.8 ns (max) (V_{CC} = 3.0~3.6 V)

: t_{pd} = 3.7 ns (max) (V_{CC} = 2.3~2.7 V) : t_{pd} = 7.4 ns (max) (V_{CC} = 1.8 V)

• High Output current : I_{OH}/I_{OL} = ±24 mA (min) (V_{CC} = 3.0 V)

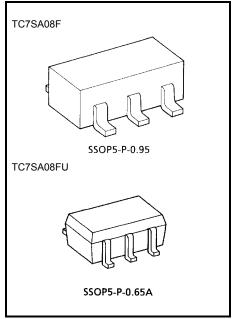
: I_{OH}/I_{OL} = ±18 mA (min) (V_{CC} = 2.3 V)

: $I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$

• 3.6-V tolerant input

• 3.6-V power down protection output

• TC74VCX08FT equivalent



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Maximum Ratings (Ta = 25°C)

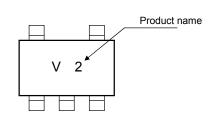
Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5~4.6	V	
DC input voltage	VIN	-0.5~4.6	٧	
DC output voltage	Va	-0.5~4.6 (Note 1)	V	
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5 (Note 2)		
Input diode current	I _{IK}	-50	mA	
Output diode current	Іок	-50 (Note 3)	mA	
DC output current	lout	±50	mA	
Power dissipation	PD	200	mW	
DC V _{CC} /ground current	Icc	±100	mA	
Storage temperature range	T _{stg}	-65~150	°C	

Note 1: V_{CC} = 0 V

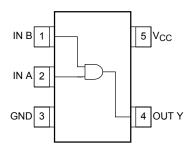
Note 2: High or low state. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

Marking



Pin Assignment (top view)



Logic Diagram



Truth Table

Inputs		Outputs
Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Recommended Operating Range

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vaa	1.8~3.6	V
Power supply voltage	V _{CC}	1.2~3.6 (Note 4)	V
Input voltage	V _{IN}	-0.3~3.6	V
Output voltage	Vout	0~3.6 (Note 5)	V
Output voltage	VOU1	0~V _{CC} (Note 6)	V
		±24 (Note 7)	
Output current	I _{OH} /I _{OL}	±18 (Note 8)	mA
		±6 (Note 9)	
Operating temperature range	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10 (Note 10)	ns/V

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or low state

Note 7: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 9: $V_{CC} = 1.8 \text{ V}$

Note 10: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$



Electrical Characteristics

DC Characteristics (Ta = -40~85°C, 2.7 V < V_{CC} \leq 3.6 V)

Charac	cteristics	Symbol	Toot C	Condition		Min Max		Unit
Charac	clensucs	Symbol	rest c	oridition	V _{CC} (V)	IVIIII	Max	Offic
Input voltage	High level	V _{IH}		_	2.7~3.6	2.0	_	V
input voltage	Low level	V _{IL}		_	2.7~3.6	_	0.8	V
				$I_{OH} = -100 \mu A$	2.7~3.6	V _{CC} - 0.2	_	
	High level	V _{OH}	$V_{IN} = V_{IH}$	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	
				$I_{OH} = -18 \text{ mA}$	3.0	2.4	_	
Output voltage				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	V
Low level	V	\\\\\\\\\	$I_{OL} = 100 \mu A$	2.7~3.6	_	0.2		
			I _{OL} = 12 mA	2.7	_	0.4		
	Low level	V _{OL}	VIN = VIH OI VIL	I _{OL} = 18 mA	3.0	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage curre	ent	I _{IN}	V _{IN} = 0~3.6 V	•	2.7~3.6	_	±5.0	μА
Power off leakage	current	I _{OFF}	V _{IN} , V _{OUT} = 0~3.	6 V	0	_	10.0	μА
Quiescent supply of	current	loo	V _{IN} = V _{CC} or GNI)	2.7~3.6	_	20.0	
Quiescent supply t	Juli Gill	Icc	$V_{IN} = V_{IH} \text{ or } V_{IL} \\ \hline \\ I_{OL} = 100 \mu\text{A} \\ \hline \\ I_{OL} = 12 \text{ mA} \\ \hline \\ I_{OL} = 24 \text{ mA} \\ \hline $	2.7~3.6	_	±20.0	μΑ	
Increase in I _{CC} per	r input	Δlcc	$V_{IH} = V_{CC} - 0.6 V$	′	2.7~3.6	_	750	

DC Characteristics (Ta = -40~85°C, 2.3 V \leq V_{CC} \leq 2.7 V)

Charac	cteristics	Symbol	Test C	Condition	V _{CC} (V)	Min	Max	Unit							
Innut valtage	High level	V _{IH}		_	2.3~2.7	1.6	_	V							
Input voltage	Low level	V _{IL}		_	2.3~2.7	_	0.7	V							
			V _{OH} V _{IN} = V _{IH}	I _{OH} = -100 μA	2.3~2.7	V _{CC} - 0.2	_								
	High level	V _{OH}		I _{OH} = -6 mA	2.3	2.0	_								
				I _{OH} = -12 mA	2.3	1.8	_								
Output voltage	utput voltage		I _{OH} = -18 mA	2.3	1.7	_	V								
			Ic	$I_{OL} = 100 \mu A$	2.3~2.7	_	0.2								
	Low level	V_{OL}		$V_{IN} = V_{IH} \text{ or } V_{IL}$	$V_{IN} = V_{IH}$ or V_{IL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 12 mA	2.3	_	0.4				
											I _{OL} = 18 mA	2.3	_	0.6	
Input leakage curre	ent	I _{IN}	V _{IN} = 0~3.6 V	•	2.3~2.7	_	±5.0	μА							
Power off leakage	current	loff	V _{IN} , V _{OUT} = 0~3.	6 V	0	_	10.0	μА							
	l	V _{IN} = V _{CC} or GND		2.3~2.7	_	20.0	^								
Quiescent supply of	surrent	Icc	$V_{CC} \leq (V_{IN}, V_{OUT})$	-) ≦ 3.6 V	2.3~2.7		±20.0	μΑ							



DC Characteristics (Ta = -40~85°C, 1.8 V \leq V_{CC} < 2.3 V)

Chara	cteristics	Symbol	Test C	ondition	Min M		Max	Unit
Cilara	Cleristics	Symbol	rest o	ondition	V _{CC} (V)	IVIIII	IVIAX	O I II
Input voltage	High level	V _{IH}	-	_	1.8~2.3	0.7 × V _{CC}		V
input voltage	Low level	V _{IL}	-	_	1.8~2.3	ı	0.2 × V _{CC}	٧
	High level	V _{OH}	$V_{IN} = V_{IH}$	$I_{OH} = -100 \mu A$	1.8	V _{CC} - 0.2	_	_
Output voltage				$I_{OH} = -6 \text{ mA}$	1.8	1.4	_	V
	Low level	Voi	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 100 \ \mu A$	1.8		0.2	
	Low level	V _{OL}	VIN = VIH OI VIL	I _{OL} = 6 mA	1.8	_	0.3	
Input leakage curr	ent	I _{IN}	V _{IN} = 0~3.6 V		1.8	_	±5.0	μА
Power off leakage	current	l _{OFF}	V _{IN} , V _{OUT} = 0~3.6	6 V	0	_	10.0	μА
	laa	V _{IN} = V _{CC} or GND		1.8	_	20.0		
Quiescent supply	Current	Icc	V _{CC} ≤ (V _{IN} , V _{OUT}	·) ≦ 3.6 V	1.8	_	±20.0	μА

AC Characteristics (Ta = $-40\sim85^{\circ}$ C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
			1.8	1.5	7.4	
Propagation delay time	t _{pLH} t _{pHL}	Figure 1, Figure 2	2.5 ± 0.2	1.0	3.7	ns
,	φпц		3.3 ± 0.3	0.8	2.8	

For $C_L = 50 \ pF$, add approximately 300 ps to the AC maximum specification.



Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition			Тур.	Unit
Characteristics	Symbol	rest Condition		V _{CC} (V)	τyp.	Gill
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	1.8	0.25	
Quiet output maximum dynamic V_{OL}	V_{OLP}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	2.5	0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	3.3	8.0	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	1.8	-0.25	
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	2.5	-0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	3.3	-0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	1.8	1.5	
Quiet output minimum dynamic VOH	V_{OHV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	2.5	1.9	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Not	e 11)	3.3	2.2	

Note 11: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_		1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 12)	1.8, 2.5, 3.3	20	pF

Note 12: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

AC Test Circuit

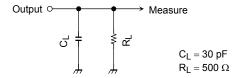


Figure 1

AC Waveforms

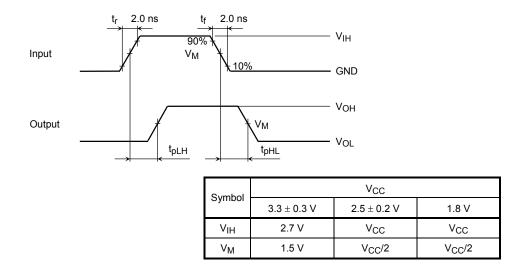
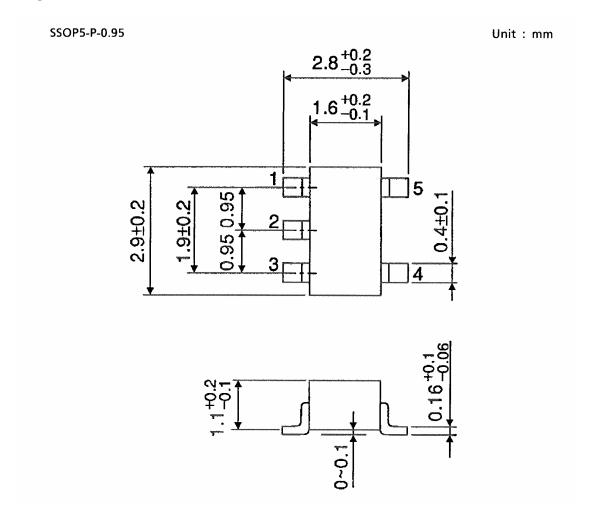


Figure 2 t_{pLH}, t_{pHL}

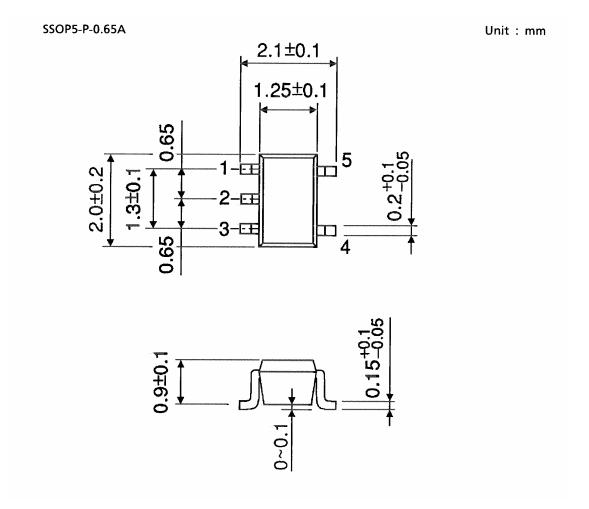
Package Dimensions



Weight: 0.016 g (typ.)

Package Dimensions

TOSHIBA



Weight: 0.006 g (typ.)

RESTRICTIONS ON PRODUCT USE

030619EBA

- The information contained herein is subject to change without notice.
- The information contained herein is presented only as a guide for the applications of our products. No
 responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which
 may result from its use. No license is granted by implication or otherwise under any patent or patent rights of
 TOSHIBA or others.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- TOSHIBA products should not be embedded to the downstream products which are prohibited to be produced and sold, under any law and regulations.

9 2006-03-15

Handbook" etc..