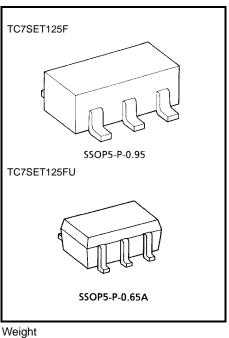
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SET125F,TC7SET125FU

### Bus Buffer

# Features

- High speed .....  $t_{pd} = 3.7 \text{ ns} (typ.)$
- at  $V_{CC} = 5 V$
- + Low power dissipation ...... ICC = 2  $\mu A$  (max) at Ta = 25°C
- Compatible with TTL outputs...VIL = 0.8 V (max.) VIH = 2.0 V (min.)
- 5.5V tolerant input.

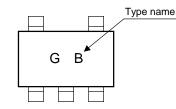


SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

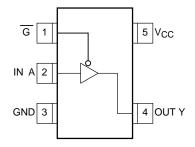
# Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5~7.0	V
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V
DC output voltage	V <sub>OUT</sub>	$-0.5 \sim V_{CC} + 0.5$	V
Input diode current	lıк	-20	mA
Output diode current	I <sub>OK</sub>	±20	mA
DC output current	Ιουτ	±25	mA
DC V <sub>CC</sub> /ground current	ICC	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C
Lead temperature (10 s)	TL	260	°C

#### Marking

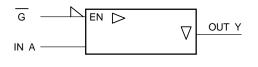


#### Pin Assignment (top view)



# TOSHIBA

# Logic Diagram



G	А	Y
Н	Х	Z
L	L	L
L	Н	Н

**Truth Table** 

# **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5~5.5	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Output voltage	V <sub>OUT</sub>	0~Vcc	V
Operating temperature	T <sub>opr</sub>	-40~85	°C
Input rise and fall time	dt/dv	0~20	ns/V

#### **DC Electrical Characteristics**

					Ta = 25°C		Ta = -40~85°C			
Characteristics	Symbol			Vcc (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage	VIH	—		4.5~ 5.5	2.0	_		2.0	_	V
Low-level input voltage	V <sub>IL</sub>	_		4.5~ 5.5		_	0.8	_	0.8	V
High lovel output veltage	Mari	$V_{IN} = V_{IH}$	$I_{OH} = -50 \ \mu A$	4.5	4.4	4.5	_	4.4		V
High-level output voltage V <sub>OH</sub>	or V <sub>IL</sub>	I <sub>OH</sub> = -8 mA	4.5	3.94	_	_	3.80	_	v	
		$V_{IN} = V_{IH}$	$I_{OL} = 50 \ \mu A$	4.5	_	0.0	0.10	_	0.10	V
Low-level output voltage V <sub>OL</sub>	or VIL	I <sub>OL</sub> = 8 mA	4.5	_	_	0.36		0.44	v	
3-state output off-state current	I <sub>OZ</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or } GND$		5.5	_	_	±0.25	_	±2.5	μΑ
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0~ 5.5		_	±0.1	_	±1.0	μΑ
	ICC	$V_{IN} = V_{CC}$ or GND		5.5			2.0		20.0	μA
Quiescent supply current	I <sub>CCT</sub>	Per Input Other Input	:V <sub>IN</sub> = 3.4 V :V <sub>CC</sub> or GND	5.5	_	_	1.35	_	1.50	mA

### AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Conditio		n	Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Symbol		V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	t <sub>pLH</sub>		5.0 ± 0.5	15		3.7	6.0	1.0	6.9	ns
Tropagation delay time	tpHL		<b>5.0</b> ± 0.5	50		6.0	10.4	1.0	11.9	115
3-state output enable time	t <sub>pZL</sub>		5.0 ± 0.5	15		3.6	5.6	1.0	6.5	ns
5-state output enable time	t <sub>pZH</sub>			50		6.0	10.3	1.0	11.9	113
3-state output disable time	t <sub>pLZ</sub> t <sub>pHZ</sub>		$5.0\pm0.5$	50	_	7.3	10.0	1.0	11.5	ns
Input capacitance	C <sub>IN</sub>				_	4	10	_	_	pF
Output capacitance	C <sub>OUT</sub>				_	6		_		pF
Power dissipation capacitance	C <sub>PD</sub>			(Note)	_	15	_	_		pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

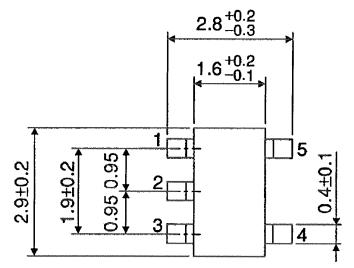
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

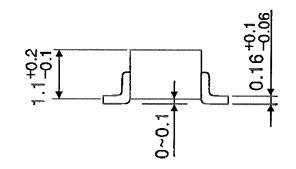
# TOSHIBA

# Package Dimensions



Unit : mm



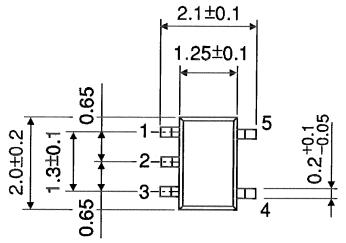


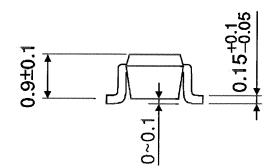
Weight: 0.016 g (typ.)

# Package Dimensions

SSOF	P5-P-0	.65A
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Unit : mm





Weight: 0.006 g (typ.)

2004-06-04

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