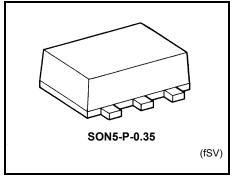
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG14AFS

#### Schmitt Inverter

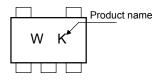
#### **Features**

- High-level output current: I<sub>OH</sub>/I<sub>OL</sub> = ±8 mA (min) at V<sub>CC</sub> = 3.0 V
- High-speed operation: t<sub>pd</sub> = 3.7 ns (typ.)
   at V<sub>CC</sub> = 3.3 V,15pF
- Operating voltage range: V<sub>CC</sub> = 0.9~3.6 V
- 5.5-V tolerant input.

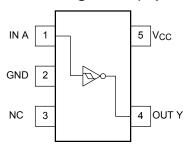


Weight: 0.001 g (typ.)

## Marking



### Pin Assignment (top view)



### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol Value		Unit
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V
DC output voltage	V <sub>OUT</sub>	-0.5~ V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	±20 (Note 1)	mA
DC output current	I <sub>OUT</sub>	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	P <sub>D</sub>	50	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

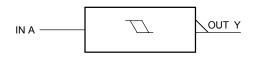
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V<sub>OUT</sub> < GND, V<sub>OUT</sub> > V<sub>CC</sub>



# **IEC Logic Symbol**

## **Truth Table**



Α	Υ
L	Н
Н	L

# **Operating Ranges**

Characteristics	Symbol	Value	Unit
Power supply voltage	Vcc	0.9~3.6	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub>	V
Output Current	I <sub>OH</sub> /I <sub>OL</sub>	±8.0 (Note 2)	
		±4.0 (Note 3)	
		±3.0 (Note 4)	mA
		±1.7 (Note 5)	IIIA
		±0.3 (Note 6)	
		±0.02 (Note 7)	
Operating temperature	T <sub>opr</sub>	-40~85	°C

Note 2:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 3:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 4:  $V_{CC} = 1.65 \sim 1.95 \text{ V}$ 

Note 5:  $V_{CC} = 1.4 \sim 1.6 \text{ V}$ 

Note 6:  $V_{CC} = 1.1 \sim 1.3 \text{ V}$ 

Note 7:  $V_{CC} = 0.9 \text{ V}$ 

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## **Electrical Characteristics**

## **DC Characteristics**

Characteristics S		Symbol	Test Condition			Ta = 25°C		)	Ta = -40~85°C		Unit
		Cymbol	10	rest Condition		Min	Тур.	Max	Min	Max	Offic
Positive threshold voltage	Vp				_		0.73		0.80	-	
					_		0.86		0.93		
					_		1.07	_	1.12		
		_		1.65			1.23	_	1.25		
				2.3			1.66	_	1.68	v	
				3.0			2.14	_	2.15		
input voitage						0.18	_		0.07	_	V
					1.1	0.26	_	_	0.18	_	
	Negative	\/			1.4	0.36	_		0.31	_	
	threshold voltage	VN		_	1.65	0.45	_	_	0.41	_	
					2.3	0.69	_		0.64	_	
					3.0	0.96	_	_	0.91	_	
					0.9	0.20	_	0.38	0.15	0.53	
						0.25	_	0.41	0.21	0.53	
Hyatarasia	voltago	\/			1.4	0.35	_	0.48	0.34	0.57	,,
Hysteresis v	ronage	VH	_		1.65	0.42	_	0.56	0.40	0.60	V
					2.3	0.60	_	0.74	0.61	0.76	
					3.0	0.79	_	0.93	0.80	0.94	
				$I_{OH} = -0.02 \text{ mA}$	0.9	0.75		_	0.75		
				$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V <sub>CC</sub> × 0.75	I	_	V <sub>CC</sub> × 0.75	I	. V
	High level	V <sub>OH</sub>	$V_{IN} = V_{IL}$	$I_{OH} = -1.7 \text{ mA}$	1.4~1.6	V <sub>CC</sub> × 0.75		_	V <sub>CC</sub> × 0.75		
				$I_{OH} = -3.0 \text{ mA}$	1.65~ 1.95	V <sub>CC</sub> -0.45	I	_	V <sub>CC</sub> -0.45	I	
				$I_{OH} = -4.0 \text{ mA}$	2.3~2.7	2.0		_	2.0		
Output voltage				$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48		_	2.48		
Output voltage		Low level V <sub>OL</sub>	V <sub>IN</sub> = I	$I_{OL} = 0.02 \text{ mA}$	0.9			0.1	_	0.1	
				$I_{OL} = 0.3 \text{ mA}$	1.1~1.3			V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low lev	Low level			I <sub>OL</sub> = 1.7 mA	1.4~1.6	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
				I <sub>OL</sub> = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	
				I <sub>OL</sub> = 4.0 mA	2.3~2.7	_	1	0.4	_	0.4	
				I <sub>OL</sub> = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage curre	Input leakage current		$V_{IN} =$	0~5.5V	0~3.6	_		±0.1	_	±1.0	μΑ
Quiescent supply of	current	Icc	$V_{IN} =$	V <sub>CC</sub> or GND	3.6	_	ĺ	1.0	_	10.0	μΑ



# AC Characteristics (input $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics	Syllibol		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
		$\begin{array}{c} C_L = 10 \ pF, \\ R_L = 1 \ M\Omega \end{array}$	0.9	_	27.3	_	_	_	
			1.1~1.3	_	13.0	22.6	1.0	35.9	
			1.4~1.6	_	7.5	10.5	1.0	11.3	ns
			1.65~ 1.95		6.0	7.8	1.0	8.2	
			2.3~2.7	_	4.3	5.4	1.0	5.8	
			3.0~3.6		3.5	4.4	1.0	4.6	
Propagation delay time			0.9		29.5	_	_	_	
	<sup>t</sup> pLH <sup>t</sup> pHL		1.1~1.3	_	14.3	25.1	1.0	41.8	
		$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	1.4~1.6	_	8.0	11.5	1.0	12.6	
			1.65~ 1.95		6.3	8.4	1.0	8.7	
			2.3~2.7		4.6	5.7	1.0	6.1	
			3.0~3.6		3.7	4.6	1.0	5.0	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	40.5	_	_	—	
			1.1~1.3		19.6	35.7	1.0	58.1	
			1.4~1.6	_	10.7	15.8	1.0	17.6	
			1.65~ 1.95		7.8	10.7	1.0	11.7	
			2.3~2.7	_	5.4	6.9	1.0	8.1	
			3.0~3.6	_	4.3	5.2	1.0	6.1	
Input capacitance	C <sub>IN</sub>		3.6	_	3	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 8)	0.9 ~ 3.6	_	7	_	_	_	pF

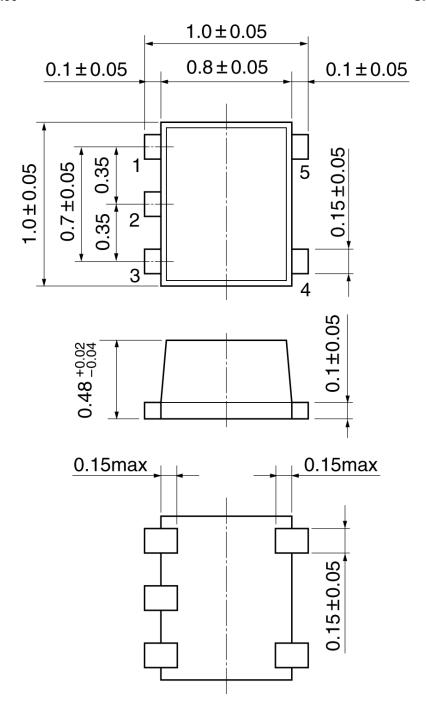
Note 8: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $ICC (opr.) = CPD \cdot VCC \cdot fIN + ICC$ 

# **Package Dimensions**

SON5-P-0.35 Unit:mm



Weight: 0.001 g (typ.)

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20070701-EN GENERAL

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