TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SHU04FE

#### INVERTER (Un-Buffer)

#### **Features**

• Super high speed operation :tpD = 3.5 ns (typ.)

$$@V_{CC} = 5 \text{ V}$$

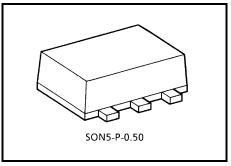
• Low power dissipation : ICC =  $2 \mu A$  (Max.)

$$@$$
 Ta = 25°C

• High noise immunity :  $V_{NIH} = V_{NIH}$ 

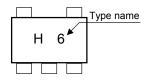
• 5.5V tolerant input.

• Wide operation voltage range :  $V_{CC}$  (opr) =  $2 \sim 5.5 \text{ V}$ 

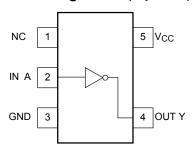


Weight: 0.003 g (typ.)

## Marking



## Pin Assignment (top view)



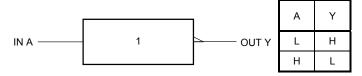
#### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	pol Rating		
Supply voltage range	Vcc	-0.5~7	٧	
DC input voltage	V <sub>IN</sub>	-0.5~7	V	
DC output voltage	Vout	-0.5~V <sub>CC</sub> + 0.5	V	
Input diode current	l <sub>IK</sub>	-20	mA	
Output diode current	lok	±20	mA	
DC output current	I <sub>OUT</sub>	±25	mA	
DC V <sub>CC</sub> /ground current	Icc	±50	mA	
Power dissipation	PD	150	mW	
Storage temperature	T <sub>stg</sub>	-65~150	°C	



## **Logic Diagram**

## **Truth Table**



## **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2~5.5	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Output voltage	V <sub>OUT</sub>	0~ V <sub>CC</sub>	٧
Operating temperature	T <sub>opr</sub>	-40~85	°C

### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol Test Circuit			Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
		rest Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic	
High-level input					2.0	1.7	_	_	1.7	_	
voltage	$V_{IH}$			_	3.0~5.5	V <sub>CC</sub> × 0.8	_	_	V <sub>CC</sub> × 0.8	_	V
Low-level input				2.0	_	_	0.3	_	0.3		
voltage	$V_{IL}$	_	_		3.0~5.5	_	_	V <sub>CC</sub> × 0.2	_	V <sub>CC</sub> × 0.2	V
		$V_{IN} = V_{IL}$	I <sub>OH</sub> = -50 μA	2.0	1.8	2.0	_	1.8	_	-	
				3.0	2.7	3.0	_	2.7	_		
High-level output voltage	High-level VOH	_			4.5	4.0	4.5	_	4.0	_	V
		V <sub>IN</sub> =GND	$I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48	_		
			VIN -GIVD	$I_{OH} = -8 \text{ mA}$	4.5	3.94		_	3.80	_	
Low-level output voltage		$V_{IN} = V_{IH}$	I <sub>OL</sub> = 50 μA	2.0		0	0.2	_	0.2	V	
				3.0	_	0	0.3	_	0.3		
	_			4.5		0	0.5	_	0.5		
		V <sub>IN</sub> =V <sub>CC</sub>	$I_{OL} = 4 \text{ mA}$	3.0			0.36	—	0.44		
			$I_{OL} = 8 \text{ mA}$	4.5			0.36	_	0.44		
Input leakage current	I <sub>IN</sub>	_	V <sub>IN</sub> = 5.5 V	or GND	0~5.5	_	_	±0.1		±1.0	μА
Quiescent supply current	Icc	_	$V_{IN} = V_{CC}$	or GND	5.5	_	_	2.0	_	20.0	μА



#### AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
			V <sub>CC</sub> (V)	C <sub>L (</sub> pF)	Min	Тур.	Max	Min	Max	
Propagation delay time	t <sub>PLH</sub>	3.3 ± 0.3 5.0 ± 0.5	33+03	15	_	5.0	8.9	1.0	10.5	
			50	_	7.5	11.4	1.0	13.0	ns	
			5.0 ± 0.5	15	_	3.5	5.5	1.0	6.5	- 115
				50	_	5.0	7.0	1.0	8.0	
Input capacitance	C <sub>IN</sub>				_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>	·	(Note)		_	6	_	_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

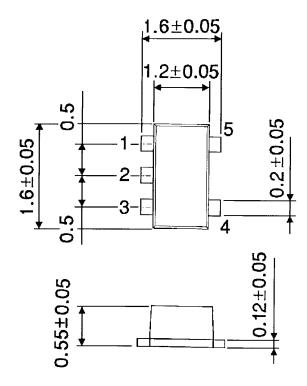
Average operating current can be obtained by the equation.

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

TC7SHU04FE

## **Package Dimensions**

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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