TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH34FS

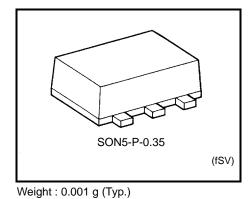
NON-INVERT BUFFER

Features

High speed: t_{pd} = 3.8 ns (typ.) at V_{CC} = 5 V Low power dissipation: I_{CC} = 2 μ A (max) at Ta = 25°C High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)

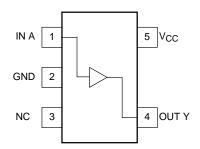
5.5V tolerant input.

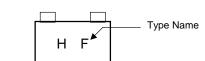
Wide operating voltage range: V_{CC} (opr) = 2~5.5 V



Marking (top view)

· Pin Assignment







Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65~150	°C

Logic Diagram



Truth Table

А	Υ
L	L
Н	Н

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	$0 \sim 100 \; (V_{CC} = 3.3 \pm 0.3 \; V)$	ns/V	
input rise and fail time	uvuv	$0 \sim 20 \; (V_{CC} = 5 \pm 0.5 \; V)$	113/ V	

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Circuit		Test				Ta = 25°C		Ta = -40~85°C			
		Test Condition		V _{CC}	Min	Тур.	Max	Min	Max	Unit	
High-level input					2.0	1.50	_	_	1.50	_	V
voltage	V _{IH}	_	_			V _{CC} × 0.7	_	_	V _{CC} × 0.7		
Low lovel input					2.0	_	_	0.50	_	0.50	
Low-level input voltage			_		_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V	
				I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	V
		_	V _{IN} = V _{IH}		3.0	2.9	3.0	_	2.9	_	
High-level output voltage	Voн				4.5	4.4	4.5	_	4.4	_	
				I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
				$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
					2.0	_	0.0	0.1	_	0.1	
Low-level output voltage VoL -		V _{IN} = V _{IL}	$I_{OL} = 50 \mu A$	3.0	_	0.0	0.1	_	0.1	V	
	_			4.5	_	0.0	0.1	_	0.1		
				I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44	
				I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	_	V _{IN} = 5.5 V or GND		0~ 5.5		-	±0.1	_	±1.0	μА
Quiescent supply current	Icc	_	V _{IN} = V _{CC} or GND			_	_	2.0	_	20.0	μА

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol	Cumbal	Test	Т	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
	Circuit	_	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Onit	
time	^t pLH		_	3.3 ± 0.3	15		5.0	7.1	1.0	8.5	ns ns
					50		7.5	10.6	1.0	12.0	
				5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	
			5.0 ± 0.5	50	_	5.3	7.5	1.0	8.5		
Input capacitance	C _{IN}	_		_			4	10	_	10	pF
Power dissipation capacitance	C _{PD}	_			(Note)	_	13	_	_		pF

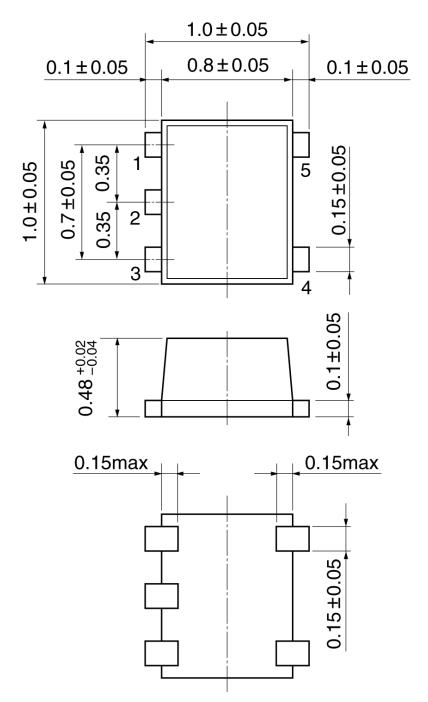
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SON5-P-0.35 Unit:mm



Weight: 0.001 g (typ.)

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