TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

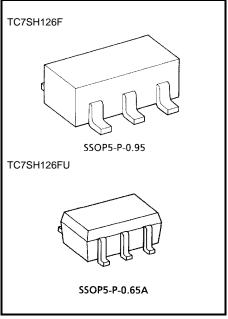
# TC7SH126F,TC7SH126FU

#### **Bus Buffer**

#### **Features**

High speed:  $t_{pd}$  = 3.8 ns (typ.) at  $V_{\rm CC}$  = 5 V Low power dissipation:  $I_{\rm CC}$  = 2  $\mu$ A (max) at Ta = 25°C High noise immunity:  $V_{\rm NIH}$  =  $V_{\rm NIL}$  = 28%  $V_{\rm CC}$  (min) 5.5V tolerant input.

Wide operating voltage range: VCC (opr) = 2~5.5 V



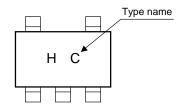
Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

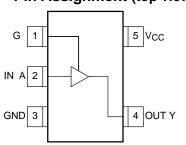
## **Maximum Ratings (Ta = 25°C)**

<b>Q</b> 1		D. d	Unit	
Characteristics	Symbol	Rating		
Supply voltage range	Vcc	-0.5~7.0	V	
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V	
DC output voltage	Vout	-0.5~V <sub>CC</sub> + 0.5	V	
Input diode current	I <sub>IK</sub>	-20	mA	
Output diode current	lok	±20	mA	
DC output current	lout	±25	mA	
DC V <sub>CC</sub> /ground current	Icc	±50	mA	
Power dissipation	P <sub>D</sub>	200	mW	
Storage temperature	T <sub>stg</sub>	-65~150	°C	
Lead temperature (10 s)	TL	260	°C	

#### Marking



## Pin Assignment (top view)





# **Logic Diagram**



## **Truth Table**

G	Α	Υ
L	Х	Z
Н	L	L
Н	Н	Н

# **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2~5.5	V	
Input voltage	V <sub>IN</sub>	0~5.5	V	
		0~ V <sub>CC</sub>		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Input rise and fall time	dt/dv	0~100 ( $V_{CC}$ = 3.3 V $\pm$ 0.3 V )	ns/V	
input rise and rail time	ui/uv	0~20 ( $V_{CC}$ = 5 $V \pm 0.5 V$ )		

## **DC Electrical Characteristics**

Characteristics Symbol Test Circuit		Test	Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
		Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic	
High-level input				2.0	1.5	_	_	1.5			
voltage	V <sub>IH</sub>	_	_		3.0~5.5	V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7	_	V
Low-level input					2.0	_	_	0.5	_	0.5	V
voltage	V <sub>IL</sub>	_		_		_	_	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	
			V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> = -50 μA	2.0	1.9	2.0	_	1.9		V
					3.0	2.9	3.0	_	2.9		
High-level output voltage	V <sub>OH</sub>	_			4.5	4.4	4.5	_	4.4		
			$I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48			
				$I_{OH} = -8 \text{ mA}$	4.5	3.94	_		3.80		
				I <sub>OL</sub> = 50 μA	2.0	_	0	0.1	_	0.1	-
					3.0		0	0.1	_	0.1	
Low-level output voltage VoL	_	$V_{IN} = V_{IH}$ or $V_{IL}$		4.5	_	0	0.1	_	0.1	V	
				$I_{OL} = 4 \text{ mA}$	3.0		_	0.36	_	0.44	
				$I_{OL} = 8 \text{ mA}$	4.5		_	0.36	_	0.44	
Input leakage current	I <sub>IN</sub>	_	V <sub>IN</sub> = 5.5 V or GND		0~5.5		_	±0.1	_	±1.0	μА
Quiescent supply current	Icc		V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2.0	_	20.0	μА

## AC Characteristics (input: $t_r = t_f = 3$ ns)

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Characteristics Symbol	Symbol	Test	Test Condition		n	Ta = 25°C			Ta = -40~85°C		Unit
	Circuit		V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	Offic	
				3.3 ± 0.3	15	_	5.6	8.0	1.0	9.5	
Propagation delay	t <sub>pLH</sub>				50	_	8.1	11.5	1.0	13.0	
time t <sub>pHL</sub>			5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	ns	
				50	_	5.3	7.5	1.0	8.5		
	t <sub>p</sub> zL t <sub>p</sub> zн	_	_	3.3 ± 0.3	15	_	5.4	8.0	1.0	9.5	ns
anable time					50		7.9	11.5	1.0	13.0	
				5.0 ± 0.5	15	_	3.6	5.1	1.0	6.0	
					50		5.1	7.1	1.0	8.0	
3-state output disable time	t <sub>pLZ</sub>	_	_	$3.3 \pm 0.3$	50	_	9.5	13.2	1.0	15.0	ns
	t <sub>pHZ</sub>			$5.0 \pm 0.5$	50	_	6.1	8.8	1.0	10.0	115
Input capacitance	C <sub>IN</sub>			_			4	10	_	10	pF
Output capacitance	C <sub>OUT</sub>	_		_		_	6	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	_			(Note)		14		_	_	pF

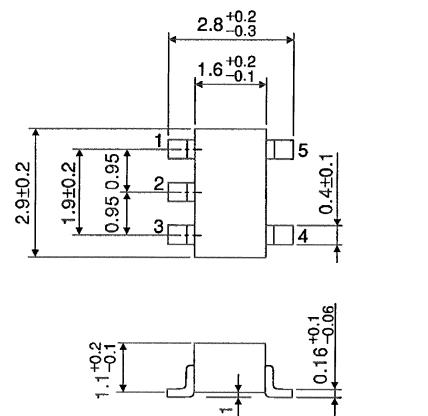
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

# **Package Dimensions**

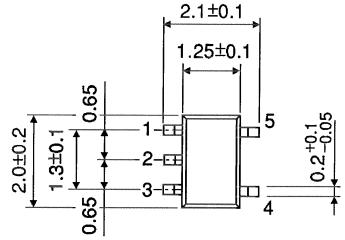
SSOP5-P-0.95 Unit: mm

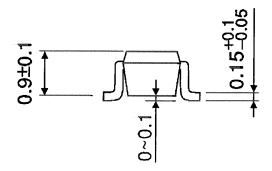


Weight: 0.016 g (typ.)

# **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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