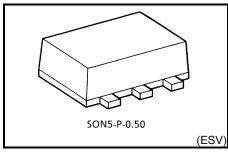
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SH07FE

NON-Inverter (Open Drain)

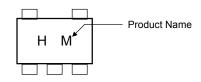
#### **Features**

- High speed:  $t_{pZL}$  = 3.7 ns (typ.) at  $V_{CC}$  = 5 V, 15 pF
- Low power dissipation: I<sub>CC</sub> = 2 μA (max) at Ta = 25°C
- Wide operating voltage range: V<sub>CC</sub> (opr.) = 2 to 5.5 V
- 5.5-V tolerant input
- 5.5-V power down protection output

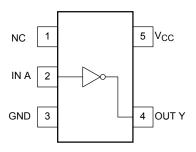


Weight: 0.003 g (Typ.)

#### Marking



### Pin Assignment (top view)



### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V <sub>CC</sub>	−0.5 to 7	V	
DC input voltage	V <sub>IN</sub>	-0.5 to 7	V	
	\/a	-0.5 to 7 (Note 1)	V	
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	·	
Input diode current	lık	-20	mA	
Output diode current	lok	-20 (Note 3)	mA	
DC output current	lout	25	mA	
DC V <sub>CC</sub> /ground current	Icc	±50	mA	
Power dissipation	P <sub>D</sub>	150	mW	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0V$  or high impedance condition

Note 2: Low state. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 3: VOUT<GND

# **IEC Logic Symbol**

## **Truth Table**



Α	Υ
L	L
Н	Z

Z: High impedance

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2 to 5.5	٧	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to 5.5	V	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to100 (V $_{CC} = 3.3 \pm 0.3 \text{ V}$ )	ns/V	
input rise and rail unle	ui/uv	0 to 20 (V <sub>CC</sub> = $5.0 \pm 0.5$ V)	115/ V	

### **Electrical Characteristics**

### **DC Characteristics**

Characteristics Symbol Test Condition			Candition		Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics Symbol		lest Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic	
High-level		_		2.0	1.5	_	_	1.5			
input voltage	V <sub>IH</sub>			3.0 to 5.5	V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7	_	V	
Low-level				2.0	_	_	0.5	_	0.5		
input voltage	V <sub>IL</sub>		_	3.0 to 5.5	_		V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	V	
		$V_{IN} = V_{IL}$	Ι <sub>ΟL</sub> = 50 μΑ	2.0	_	0	0.1	_	0.1	٧	
Low lovel				3.0	_	0	0.1	_	0.1		
Low-level VOL output voltage	$V_{OL}$			4.5	_	0	0.1	_	0.1		
			$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36	_	0.44		
			$I_{OL} = 8 \text{ mA}$	4.5	_		0.36	_	0.44		
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μА	
Output Z ICVCI		V <sub>IN</sub> = V <sub>IH</sub> V <sub>OUT</sub> = 0 to 5.5V		0 to 5.5	_		±0.25		±2.5	μА	
leakage current											
Power-off leakage current	loff	$V_{IN} = 5.5V$ or $V_{OUT} = 0$ to 5.5V		0.0	_	_	1.0	_	10.0	μА	
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2.0	_	20	μА	

# AC Characteristics (Input: $t_r = t_f = 3$ ns)

Characteristics	Cumahal	Took Condition			Ta = 25°C			Ta = -40 to 85°C		
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)		Min	Тур.	Max	Min	Max	Unit
	t <sub>pZL</sub>	$R_L = 1 k\Omega$	3.3 ± 0.3	15	_	5.5	7.9	1.0	9.5	
				50	-	8.0	11.4	1.0	13.0	- ns
			5.0 ± 0.5	15		3.7	5.5	1.0	6.5	
Propagation delay time				50	-	5.2	7.5	1.0	8.5	
i Topagation delay time	t <sub>pLZ</sub>	$R_L = 1k\Omega$	3.3 ± 0.3	15	_	5.5	7.9	1.0	9.5	
				50	ı	8.0	11.4	1.0	13.0	
			5.0 ± 0.5	15		3.7	5.5	1.0	6.5	
				50	-	5.2	7.5	1.0	8.5	
Input capacitance	C <sub>IN</sub>	_				4	10	_	10	pF
Output capacitance	Cout	_		-	6		_	_	pF	
Power dissipation capacitance	C <sub>PD</sub>	(Note4)				14		_		pF

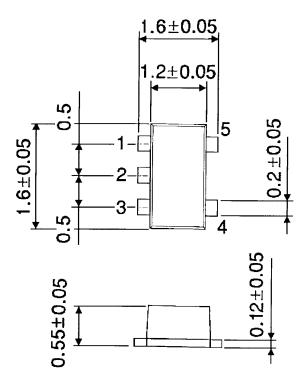
Note4: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

## **Package Dimensions**

SON5-P-0.50 Unit: mm



Weight: 0.003 g (Typ.)

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20070701-EN GENERAL

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