TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ17F,TC7SZ17FU

Schmitt Buffer

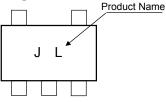
Features

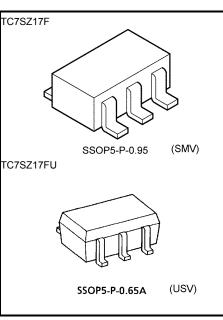
- High output current
- Super high speed operation : tpd = 3.7ns (typ.)
 - at $V_{CC} = 5V, 50pF$
- Operation voltage range
- 5.5-V tolerant input
- 5.5-V power down protection output
- Matches the performance of TC74LCX series when operated at 3.3- V V_{CC}

: ±24mA (min) at V_{CC} = 3V

: V_{CC (opr)} = 1.65 to 5.5V

Marking





Weight:

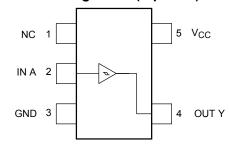
SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

550P5-P-0.65A : 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V _{CC}	–0.5 to 6	V	
DC input voltage	VIN	–0.5 to 6	V	
DC output voltage	Vour	-0.5 to 6 (Note 1)	V	
DC oulput voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 2)	v	
Input diode current	I _{IK}	-20	mA	
Output diode current	I _{OK}	-20 (Note 3)	mA	
DC output current	IOUT	±50	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	ΤL	260	°C	

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 2: High or Low state. Do not exceed IOUT of absolute maximum ratings.

Note 3: V_{OUT} < GND

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IEC Logic Symbol



А	Y
L	L
Н	Н

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vee	1.65 to 5.5	v	
Supply voltage	Vcc	1.5 to 5.5 (Note 4)	v	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	Vour	0 to 5.5 (Note 5)	V	
Output voltage	Vout	0 to V _{CC} (Note 6)	v	
Operating temperature	T _{opr}	-40 to 85	°C	

Note 4: Date retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low State

Electrical Characteristics

DC Characteristics

Charge	teristics	Cumbal	Test Condition			Ta = 25°C)	$Ta = -40$ to $85^{\circ}C$		Unit
Charac	tenstics	Symbol	Test Condition	$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Onit
				1.65	0.6	1.0	1.4	0.6	1.4	
				1.8	0.7	1.1	1.5	0.7	1.5	
	High level	VP		2.3	1.0	1.4	1.8	1.0	1.8	
	riigirievei	٩V	_	3.0	1.3	1.75	2.2	1.3	2.2	
				4.5	1.9	2.45	3.1	1.9	3.1	
Threshold				5.5	2.2	2.9	3.6	2.2	3.6	v
voltage	voltage		1.65	0.2	0.5	0.8	0.2	0.8	v	
				1.8	0.25	0.55	0.9	0.25	0.9	
	Low level	M.		2.3	0.40	0.75	1.15	0.40	1.15	
Low level	V _N	—	3.0	0.6	1.0	1.5	0.6	1.5		
				4.5	1.0	1.43	2.0	1.0	2.0	
				5.5	1.2	1.70	2.4	1.2	2.4	
				1.65	0.1	0.48	0.9	0.1	1.0	
Hysteresis voltage			1.8	0.15	0.54	1.0	0.15	1.0		
	N/		2.3	0.25	25 0.65 1.1	0.25	1.1	V		
	maye	∨н	V _H —	3.0	0.4	0.77	1.2	0.4	1.2	V
			4.5	0.6	1.01	1.5	0.6	1.5		
		5.5	0.7	1.18	1.7	0.7	1.7			

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Charao	teristics	Symbol	Test Condition		mbol lest Condition		Ta = 25°0	C	$Ta = -40$ to $85^{\circ}C$		Unit						
Cliarac		Symbol	Test		$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Offic						
					1.65	1.55	1.65	—	1.55	_							
					1.8	1.7	1.8		1.7	—							
				$I_{OH}=-100~\mu A$	2.3	2.2	2.3		2.2	—							
					3.0	2.9	3.0		2.9	—							
	High level	V _{OH}	$V_{IN} = V_P$		4.5	4.4	4.5	_	4.4	_							
	riigirievei	VОН	VIN – VP	I _{OH} = -4 mA	1.65	1.29	1.52		1.29	_							
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_							
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8		2.4	_							
							I _{OH} = -24 mA	3.0	2.3	2.68		2.3	_				
Output				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	-	v						
voltage				1.65		0	0.1	_	0.1	v							
						1.8	_	0	0.1	_	0.1						
							$I_{OL} = 100 \ \mu A$	2.3		0	0.1	_	0.1				
					N						3.0		0	0.1	_	0.1	
	Low level					$V_{IN} = V_N$		4.5	_	0	0.1	_	0.1				
	LOWIEVEI	V _{OL}	VIN = VN	$I_{OL} = 4 \text{ mA}$	1.65	_	0.08	0.24	—	0.24							
				I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3							
			l	l		I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4					
				I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55							
				I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55							
Input leakage	current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5			±1	_	±10	μA						
Power OFF le current	eakage	I _{OFF}	V_{IN} or $V_{OUT} = 5.5 V$		0.0		_	1	_	10	μA						
Quiescent su	pply current	Icc	V _{IN} = 5.5 V c	or GND	1.65 to 5.5		_	1	_	10	μA						

AC Characteristics (Unless otherwise specified Input: $t_r = t_f = 3 \text{ ns}$)

Ohanna stanistian	Oursels al	Test Oserdition		-	Ta = 25°0)	Ta = -40) to 85°C	Unit			
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit			
			1.8 ± 0.15	2.0	9.1	15.0	2.0	15.6				
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	2.5 ± 0.2	1.0	5.0	9.0	1.0	9.5				
Propagation delay time	^к рНн t _{рНL}					3.3 ± 0.3	1.0	3.7	6.3	1.0	6.5	ns
				5.0 ± 0.5	0.5	3.1	5.2	0.5	5.5			
				(C _L = 50 pF,	$\textbf{3.3}\pm\textbf{0.3}$	1.5	4.4	7.2	1.5	7.5	
		$R_L = 500 \Omega$	5.0 ± 0.5	0.5	3.7	5.9	0.8	6.2				
Input capacitance	C _{IN}		0 to 5.5		4	_	_	_	pF			
Dower dissinction conscitutes	C	(Note 7)	3.3		24	_		_	pF			
Power dissipation capacitance	C _{PD}		5.5	_	30	_	_	_	pF			

Note 7: CPD is defined as the value of the internal equivalent capacitance which is Calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

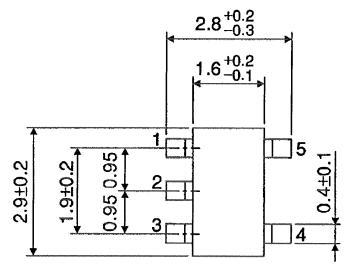
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

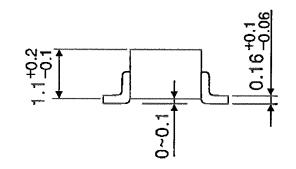
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Package Dimensions



Unit : mm





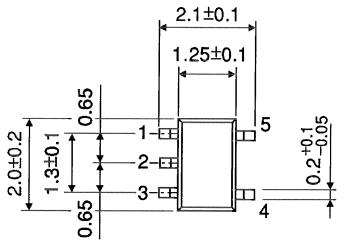
Weight: 0.016 g (typ.)

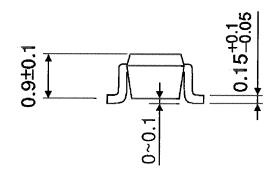
4

Package Dimensions

SSOP5-P	-0.65A
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Unit : mm





Weight: 0.006 g (typ.)

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