TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ17FE

Schmitt Buffer

Features

High output current : ±24mA (min) at V_{CC} = 3 V

• Super high speed operation : t_{pd} = 3.7 ns (typ.)

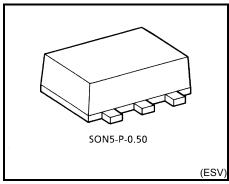
at V_{CC} = 5V, 50pF

• Operation voltage range : V_{CC (opr.)} = 1.65 to 5.5 V

• 5.5-V tolerant input

5.5-V power down protection output

 Matches the performance of TC74LCX series when operated at 3.3-V VCC



Weight: 0.003 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	−0.5 to 6	V
DC input voltage	V _{IN}	−0.5 to 6	V
DC output voltage	Vour	-0.5 to 6 (Note 1)	V
DC output voltage	Vout	-0.5 to V _{CC} +0.5 (Note 2)	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	P_{D}	150	mW
Storage temperature	T _{stg}	-65 to 150	°C

lote: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating

temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling

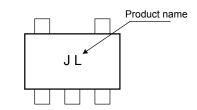
Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

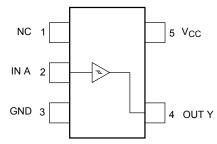
Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: Vout < GND

Marking



Pin Assignment (top view)





IEC Logic Symbol



Truth Table

Α	Υ
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.65 to 5.5	V	
Supply voltage	VCC	1.5 to 5.5 (Note 4)	-	
Input voltage	V _{IN}	0 to 5.5	٧	
Output voltage	Vour	0 to 5.5 (Note 5)	V	
	Vout	0 to V _{CC} (Note 6)		
Operating temperature	T _{opr}	-40 to 85	°C	

Note 4: Data retention only

Note 5: $V_{CC} = 0V$

Note 6: High or Low State

Electrical Characteristics

DC Characteristics

Characteristics		Symbol	Symbol Test Condition		Ta = 25°C			Ta = -40	Unit		
	Symbol rest condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic		
				1.65	0.6	1.0	1.4	0.6	1.4		
				1.8	0.7	1.1	1.5	0.7	1.5		
High-level	V _P	_	2.3	1.0	1.4	1.8	1.0	1.8			
	VP		3.0	1.3	1.75	2.2	1.3	2.2			
				4.5	1.9	2.45	3.1	1.9	3.1		
Threshold				5.5	2.2	2.9	3.6	2.2	3.6	V	
voltage		evel V _N	V _N	1.65	0.2	0.5	0.8	0.2	0.8	·	
				1.8	0.25	0.55	0.9	0.25	0.9	-	
	Low-level			2.3	0.40	0.75	1.15	0.40	1.15		
	Low-level			3.0	0.6	1.0	1.5	0.6	1.5		
				4.5	1.0	1.43	2.0	1.0	2.0		
				5.5	1.2	1.70	2.4	1.2	2.4		
		2.3 0.25			1.65	0.1	0.48	0.9	0.1	1.0	
				0.15	0.54	1.0	0.15	1.0			
Hysteresis voltage	V _H		voltage V.	zis voltage Vu	/	2.3	0.25	0.65	1.1	0.25	1.1
		3.0	0.4	0.77	1.2	0.4	1.2	v			
			4.5	0.6	1.01	1.5	0.6	1.5			
			5.5	0.7	1.18	1.7	0.7	1.7			



Characteristics		Symbol T		Test Condition		Ta = 25°C			Ta = -40) to 85°C	Unit
	Cymbol Test condition		Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
					1.65	1.55	1.65	_	1.55	_	
					1.8	1.7	1.8	_	1.7	_	
				$I_{OH} = -100~\mu A$	2.3	2.2	2.3	-	2.2	_	
					3.0	2.9	3.0		2.9	_	
	High-level	V _{OH}	$V_{IN} = V_P$		4.5	4.4	4.5	-	4.4	_	V
	i ligit-level	VOH	VIN – VP	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52	_	1.29	_	V
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9	_	
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
				I _{OH} = -24 mA	3.0	2.3	2.68	-	2.3	_	
Output	Output			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
voltage		Vol		I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	
					1.8	_	0	0.1	_	0.1	
					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1	_	0.1	
	Low-level output				4.5	_	0	0.1	_	0.1	
	voltage	VOL	$V_{IN}=V_{N}$	$I_{OL} = 4 \text{ mA}$	1.65	_	0.08	0.24	_	0.24	V
				$I_{OL} = 8 \text{ mA}$	2.3	_	0.1	0.3	_	0.3	
				$I_{OL} = 16 \text{ mA}$	3.0	_	0.15	0.4	_	0.4	
			$I_{OL} = 24 \text{ mA}$	3.0	_	0.22	0.55	_	0.55		
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage	current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±1	_	±10	μА
Power OFF le	eakage current	I _{OFF}	V _{IN} ορ V _{OUT} = 5.5 V		0.0	_	_	1	_	10	μА
Quiescent su	pply current	Icc	$V_{IN} = V_{CC}$ or GND		1.65 to 5.5	_	_	2	_	20	μА

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	Ta = 25°C			С	Ta = -40	Unit	
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		C_L = 15 pF, R_L = 1 M Ω C_L = 50 pF, R_L = 500 Ω	1.8±0.15	2.0	9.1	15.0	2.0	15.6	
	t _{pLH}		2.5 ± 0.2	1.0	5.0	9.0	1.0	9.5	
			3.3 ± 0.3	1.0	3.7	6.3	1.0	6.5	ns
			5.0 ± 0.5	0.5	3.1	5.2	0.5	5.5	113
			3.3 ± 0.3	1.5	4.4	7.2	1.5	7.5	
			5.0 ± 0.5	0.5	3.7	5.9	0.5	6.2	
Input capacitance	C _{IN}	_	0 to 5.5		4		_		pF
Power dissipation capacitance	0	C _{PD} (Note 7)	3.3		24		_		pF
	CPD		(Note 7)	5.5	_	30	_	_	_

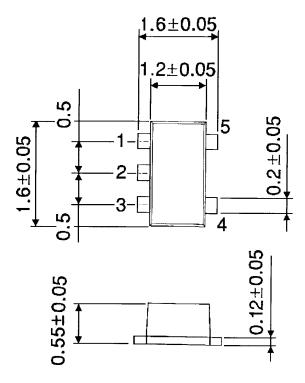
Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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