TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ126AFE

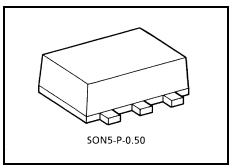
Bus Buffer with 3-STATE Output

Features

- High output drive: ±24 mA (min) at V_{CC} = 3 V
- Super high speed operation: t_{pd} = 2.6 ns (typ.)

at V_{CC} = 5 V, 50 pF

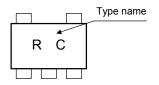
- Operation voltage range: V_{CC (opr)} = 1.8~5.5 V
- 5.5-V tolerant inputs
- Matches the performance of TC74LCX series when operated at 3.3-V V_{CC}

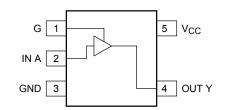


Weight: 0.003 g (typ.)

Pin Assignment (top view)

Marking





Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5~6	V	
DC input voltage	V _{IN}	V _{IN} -0.5~6		
DC output voltage	V _{OUT}	$-0.5 \sim V_{CC} + 0.5$	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	IOK	±20	mA	
DC output current	IOUT	±50	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	150	mW	
Storage temperature	T _{stg}	-65~150	°C	
Lead temperature (10s)	ΤL	260	°C	

TOSHIBA

Truth Table

А	G	Y
Х	L	Z
L	Н	L
Н	Н	Н

X : Don't Care

Z : High Impedance



Logic Diagram

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.8~5.5	V	
Supply voltage	v CC	1.5~5.5 (Note1)	v	
Input voltage	VIN	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
		0~20 (V_{CC} = 1.8 V, 2.5 V \pm 0.2 V)	ns/V	
Input rise and fall time	dt/dv	0~10 (V_{CC} = 3.3 V \pm 0.3 V)		
		0~5 (V_{CC} = 5.5 V \pm 0.5 V)		

Note1: Data retention only

Electrical Characteristics

DC Characteristics

Characteris		Symbol	nbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
Characteris	5005	Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
					1.8	$\begin{array}{c} 0.75 \times \\ V_{CC} \end{array}$		_	$_{V_{CC}}^{0.75\times}$	_	
Input voltage	High level	Vih		_	2.3~5.5	$0.7 \times V_{CC}$	_		$0.7 \times V_{CC}$		v
input voitage					1.8	_		$\begin{array}{c} 0.25 \times \\ V_{CC} \end{array}$	_	$\begin{array}{c} 0.25 \times \\ V_{CC} \end{array}$	
	Low level	VIL		_		_		$0.3 \times V_{CC}$	_	$0.3 \times V_{CC}$	
					1.8	1.7	1.8		1.7	_	
				lau - 100 u A	2.3	2.2	2.3		2.2	_	-
				I _{OH} = -100 μA	3.0	2.9	3.0	_	2.9	_	
	High level	V _{OH}	V V		4.5	4.4	4.5		4.4	_	
	rigitievei		V _{IN} = V _{IH}	I _{OH} = -8 mA	2.3	1.9	2.15	_	1.9	_	
				I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
				I _{OH} = -24 mA	3.0	2.3	2.68		2.3	_	
Output voltage				I _{OH} = -32 mA	4.5	3.8	4.2		3.8	_	V
output voltage					1.8	—	0	0.1	—	0.1	v
				I _{OL} = 100 μA	2.3	_	0	0.1		0.1	
				10L - 100 μ/τ	3.0	—	0	0.1	—	0.1	
	Low level	V _{OL}	$V_{IN} = V_{IH}$ or V_{IL}	н	4.5	—	0	0.1	—	0.1	
	VOL	or VIL	I _{OL} = 8 mA	2.3	—	0.1	0.3	—	0.3		
			I _{OL} = 16 mA	3.0	—	0.15	0.4	—	0.4		
			I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55		
				I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage curre	ent	I _{IN}	$V_{IN} = 5.5$ V	/ or GND	0~5.5	—		±1		±10	μA
3-state output off-s	tate current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL},$ $V_{OUT} = 0 \sim 5.5 \text{ V}$		1.8~5.5	_		±1	_	±10	μA
Quiescent supply o	urrent	ICC	$V_{IN} = 5.5$ V	/ or GND	5.5			2	—	20	μA

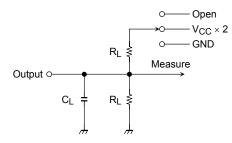
AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Symbol Test Condition		Ta = 25°C			Ta = -40~85°C		Unit	
Characteristics	Characteristics Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
			1.8	2.0	5.3	11.0	2.0	11.5		
		$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	2.5 ± 0.2	0.8	3.4	7.5	0.8	8.0		
Propagation delay time	t _{pLH}	$G_{L} = 15 \text{pr}, \text{KL} = 1 \text{Msz}$	$\textbf{3.3}\pm\textbf{0.3}$	0.5	2.5	5.2	0.5	5.5		
r topagation delay time	t _{pHL}		5.0 ± 0.5	0.5	2.1	4.5	0.5	4.8		
		$C_{1} = 50 \text{ pc}$ $P_{1} = 500 \text{ C}$	$\textbf{3.3}\pm\textbf{0.3}$	1.5	3.2	5.7	1.5	6.0		
		$C_L = 50 \text{ pF}, \text{ R}_L = 500 \Omega$	5.0 ± 0.5	0.8	2.6	5.0	0.8	5.3		
Output enable time t _{pZH}		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	1.8	2.0	6.5	11.5	2.0	12.0	ns	
	t _{pZH}		2.5 ± 0.2	1.5	3.8	8.0	1.5	8.5	113	
	t _{pZL}	$O_{L} = 30 \text{ pr}$, $N_{L} = 300 \text{ sz}$	$\textbf{3.3}\pm\textbf{0.3}$	1.5	3.2	5.7	1.5	6.0		
			5.0 ± 0.5	0.8	2.3	5.0	0.8	5.3		
			1.8	2.0	5.6	11.0	2.0	12.0		
Output disable time	t _{pLZ}	t _{pLZ}	C 50 pF D 500 C	2.5 ± 0.2	1.0	4.0	8.0	1.5	8.5	
t _{pHZ}	t _{pHZ}	$C_L = 50 \text{ pF}, \text{ R}_L = 500 \Omega$	$\textbf{3.3}\pm\textbf{0.3}$	1.0	3.5	5.7	1.0	6.0		
		5.0 ± 0.5	0.5	2.7	4.7	0.5	5.0			
Input capacitance	C _{IN}	_	0~5.5		4			—	pF	
Power dissipation	C _{PD}	(Note2)	3.3		20	_		_	pF	
capacitance		(Note2) —	5.5	—	27		_	—	Ч	

Note2: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation: $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

AC Characteristics Measurement Circuit

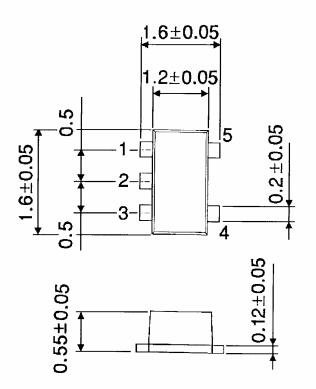


Characteristics	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	$V_{CC} \times 2$
t _{pHZ,} t _{pZH}	GND

Package Dimensions

SON5-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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