TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ07FE

NON-Inverter (Open Drain)

Features

High output current : ±24mA (min) at V_{CC} = 3V

• Super high speed operation : t_{pZL} 2.3 ns (typ.)

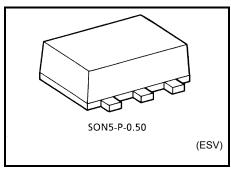
at V_{CC} = 5 V, 50 pF

• Operation voltage range : V_{CC (opr.)} = 1.65 to 5.5V

5.5-V tolerant input

• 5.5-V power down protection output

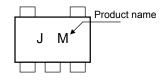
• Matches the performance of TC74LCX series when operated at $3.3\text{-V}\ \text{V}_{CC}$

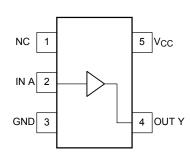


Weight: 0.003 g (typ.)

Marking

Pin Assignment (top view)





Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	–0.5 to 6	V
DC input voltage	V _{IN}	-0.5 to 6	V
DC output voltage	Vout	-0.5 to 6 (Note 1)	٧
Input diode current	lık	-20	mA
Output diode current	lok	-20 (Note 2)	mA
DC output current	lout	50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	P _D	150	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

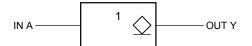
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Do not exceed $I_{\mbox{\scriptsize OUT}}$ of absolute maximum ratings.

Note 2: V_{OUT} < GND



IEC Logic Symbol



Truth Table

Α	Y
L	L
Н	Z

Z: High impedance

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.65 to 5.5	V
Supply voltage		1.5 to 5.5 (Note 3)	V
Input voltage	VIN	0 to 5.5	V
Output voltage	V _{OUT}	0 to 5.5	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 20 (V_{CC} = 1.8 V± 0.15 V, 2.5 V ± 0.2 V)	
		0 to 10 (V _{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 (V _{CC} = 5.0 V \pm 0.5 V)	

Note 3: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics Sym		Cumbal	ymbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
		Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High level	V _{IH}	_		1.65 to 1.95	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	V	
	VIH			2.3 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_		
Input voltage	Low level				1.65 to 1.95			V _{CC} × 0.25	_	V _{CC} × 0.25	V
Low level	V _{IL}	_		2.3 to 5.5	l	l	V _{CC} × 0.3		V _{CC} × 0.3		
Z-state output le current	akage	lkg	VIN = VIH VOUT = 0 to 5.5 V		1.65 to 5.5		-	±5	_	±10	μА
				I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	
Output voltage Low level					2.3		0	0.1	_	0.1	
	VoL	V _{IN} = V _{II}	100 μΑ	3.0	_	0	0.1	_	0.1	V	
				4.5	_	0	0.1	_	0.1		
			I _{OL} = 8 mA	2.3		0.1	0.3	_	0.3		
			$I_{OL} = 16 \text{ mA}$	3.0		0.15	0.4	_	0.4		
			I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55		
				$I_{OL} = 32 \text{ mA}$	4.5		0.22	0.55	_	0.55	
Input leakage current		I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_		±1	_	±10	μА
Power off leakage current		loff	V _{IN} or V _{OUT} = 5.5 V		0.0			1	_	10	μА
Quiescent supply current		Icc	V _{IN} = 5.5 V or GND		5.5			2	_	20	μА



AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40	Unit	
Gridiacieristics 5	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pZL	C_L = 50 pF, R_L = 500 Ω	1.8 ± 0.15	1.8	5.5	9.5	1.8	10.5	- ns
			2.5 ± 0.2	1.2	3.7	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.9	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	2.3	3.5	0.5	3.9	
	t _{pLZ}	$C_L = 50$ pF, $R_L = 500~\Omega$	1.8 ± 0.15	1.8	4.3	9.5	1.8	10.5	
			2.5 ± 0.2	1.2	2.8	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.1	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	1.4	3.5	0.5	3.9	
Input capacitance	C _{IN}	_	0 to 5.5	_	4	_	_	_	pF
Output capacitance	Cout	_	0 to 5.5	_	8	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 4)	3.3	_	20	_	_	_	pF
			5.5	_	26	_	_	_	μΓ

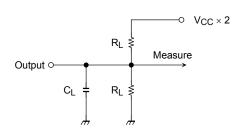
Note4: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

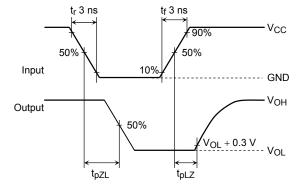
Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

AC Characteristics Measurement Circuit

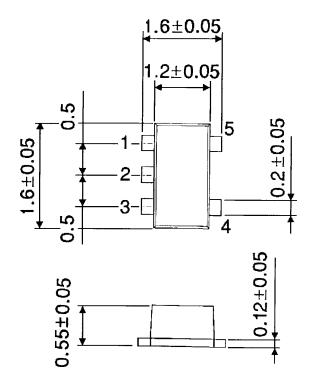
AC Waveforms





Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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