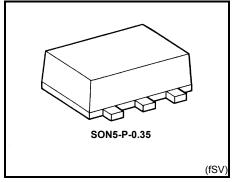
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ00AFS

2-Input NAND Gate

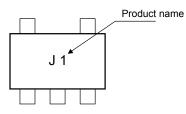
Features

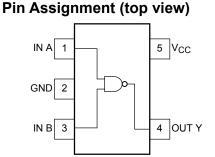
- High output drive: ±24 mA (min)
 - at V_{CC} = 3 V
- Super high speed operation: t_{pd} = 2.4 ns (typ.)
 - at V_{CC} = 5 V, 50 pF
- Operation voltage range: V_{CC (opr.)} = 1.65 to 5.5 V
- 5.5-V tolerant inputs



Weight : 0.001 g (typ.)

Marking





Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	–0.5 to 6	V
DC input voltage	VIN	–0.5 to 6	V
DC output voltage	Vout	–0.5 to V _{CC} +0.5V	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20(Note1)	mA
DC output current	I _{OUT}	±50	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1:VOUT<GND,VOUT>VCC

<u>TOSHIBA</u>

TC7SZ00AFS

IEC Logic Symbol

Truth Table



А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.65 to 5.5	V
		1.5 to 5.5 (Note 2)	v
Input voltage	VIN	0 to 5.5	V
Output voltage	VOUT	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	d _t /d _v	0 to 20 (V_{CC} = 1.8 V \pm 0.15 V, 2.5 V \pm 0.2 V)	
		0 to 10 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 (V_{CC} = 5.0 V \pm 0.5 V)	

Note 2: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Toot Condition			Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
		Test Condition V _{CC} (V)		Min	Тур.	Max	Min	Max	Unit	
High-level input			2		V _{CC} × 0.75	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$	_	v
voltage		V _{CC} × 0.7			_	_	V _{CC} × 0.7	_		
Low-level input	linput		1.65 to 1.95	_	_	$V_{CC} \times 0.25$	_	V _{CC} × 0.25	v	
voltage				_	_	$V_{CC} \times 0.3$	_	V _{CC} × 0.3	v	
				1.65	1.55	1.65	_	1.55		
			I _{OH} = –100 μA	2.3	2.2	2.3	_	2.2		V
			$10H = -100 \mu\text{A}$	3.0	2.9	3.0	_	2.9		
				4.5	4.4	4.5	—	4.4		
High-level output voltage	V _{OH}	V _{IN} = V _{IL} or V _{IH}	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52	—	1.29		
g-			$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9		
			$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	—	2.4		
			$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68	_	2.3		
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8		
				1.65		0	0.1		0.1	
		100 1	2.3		0	0.1	_	0.1	1	
			I _{OL} = 100 μA	3.0	_	0	0.1	_	0.1	
		L VIN = VIH		4.5		0	0.1	_	0.1	
Low-level output voltage	V _{OL}		$I_{OL} = 4 \text{ mA}$	1.65		0.08	0.24	_	0.24	
			I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
			$I_{OL} = 16 \text{ mA}$	3.0		0.15	0.4	_	0.4	
			$I_{OL} = 24 \text{ mA}$	3.0		0.22	0.55	_	0.55	
			I _{OL} = 32 mA	4.5	_	0.22	0.55		0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±1	_	±10	μΑ
Quiescent supply current	ICC	V _{IN} = 5.5V or GND		5.5	_	_	1	_	10	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

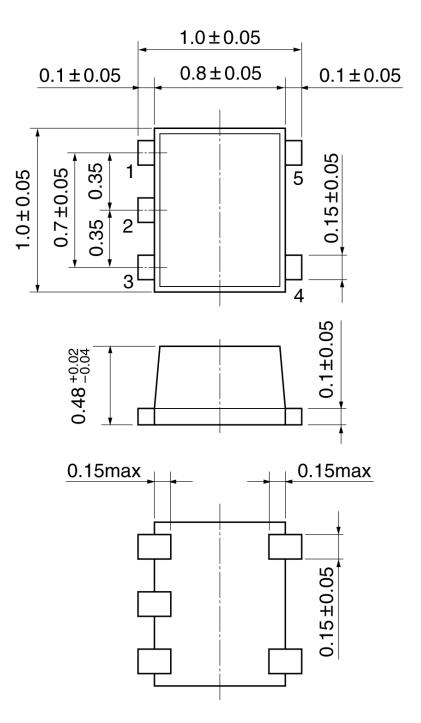
Characteristics	Sumbol	Symbol Test Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
Characteristics Sym	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH ^t pHL	C_L = 15 pF, R_L = 1 $M\Omega$ C_L = 50 pF, R_L = 500 Ω	1.8 ± 0.15	2.0	5.3	9.6	2.0	9.8	- ns
			2.5 ± 0.2	0.8	3.2	5.3	0.8	5.7	
			$\textbf{3.3}\pm\textbf{0.3}$	0.5	2.4	3.7	0.5	4.0	
			5.0 ± 0.5	0.5	1.9	2.9	0.5	3.2	
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	3.0	4.6	1.5	4.9	
			5.0 ± 0.5	0.8	2.4	3.6	0.8	3.9	
Input capacitance	C _{IN}	_	0 to 5.5		4			_	pF
Power dissipation capacitance	0	(Note 2)	3.3		19		_	_	5
	C _{PD} (Note 3)		5.5		27			_	pF

Note3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions



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Weight: 0.001 g (typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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