**TOSHIBA** TC7SZ08F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7SZ08F, TC7SZ08FU

# 2 INPUT AND GATE

#### **FEATURES**

 High Output Drive : ± 24 mA (Typ.)

 $(V_{CC} = 3 V)$ 

Super High Speed Operation : tpD = 2.7 ns (Typ.)

 $(V_{CC} = 5 V, 50 pF)$ 

• Operation Voltage Range :  $V_{CC (opr)} = 1.8 \sim 5.5 \text{ V}$ 

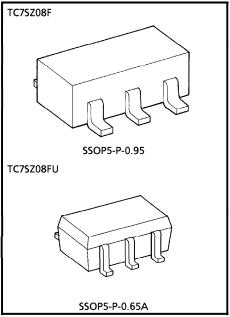
Supply Voltage Data Retention :  $V_{CC} = 1.5 \sim 5.5 \text{ V}$ 

5 V Toleratnt Function

• Matches the Performance of TC74LCX Series when Operated at 3.3 V VCC

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~6	V
DC Input Voltage	VIN	-0.5~6	٧
DC Output Voltage	Vout	-0.5~6	٧
Input Diode Current	Ικ	± 20	mA
Output Diode Current	lok	± 20	mA
DC Output Current	IOUT	± 50	mA
DC V <sub>CC</sub> / Ground Current	lcc	± 50	mA
Power Dissipation	PD	200	mW
Storage Temperature	T <sub>stg</sub>	<b>-65∼150</b>	°C
Lead Temperature (10 s)	TL	260	°C



Weight SSOP5-P-0.95 : 0.016 g (Typ.) SSOP5-P-0.65A : 0.006 g (Typ.)

## DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	CAMBOI	. TEST CONDITION		V	- Ta = 25°C		$Ta = -40 \sim 85^{\circ}C$		UNIT	
CHARACTERISTIC	3 TIVIBOL			Vcc (V)	MIN.	TYP.	MAX.	MIN.	MAX.	OINII
ligh-Level		1.8	0.88 × V <sub>C</sub> C	_	_	0.88 × V <sub>C</sub> C	_	V		
Input Voltage	V <sub>IH</sub>			2.3~ 5.5	0.75 × V <sub>CC</sub>	_	_	0.75 × V <sub>CC</sub>	_	
Low-Level	vel ,			1.8	_	_	0.12 × V <sub>CC</sub>	_	0.12 × V <sub>CC</sub>	V
Input Voltage	V <sub>IL</sub>			2.3~ 5.5	_	_	0.25 × V <sub>CC</sub>	_	0.25 × V <sub>CC</sub>	
				1.8	1.7	1.8	_	1.7	_	
			$I_{OH} = -100 \mu A$	2.3	2.2	2.3	_	2.2	_	
			$\mu$	3.0	2.9	3.0	_	2.9	_	
High-Level	VOH	V <sub>IN</sub> = V <sub>IH</sub>		4.5	4.4	4.5	_	4.4	_	l v
Output Voltage	VOH	VIIN - VIH	IOH = -8 mA	2.3	1.9	2.15	_	1.9	_	- - -
			$I_{OH} = -16  \text{mA}$	3.0	2.4	2.8	_	2.4	_	
			$I_{OH} = -24  \text{mA}$	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2		3.8	_	
			I <sub>OL</sub> = 100 μA	1.8	_	0	0.1	_	0.1	- V
				2.3	_	0	0.1	_	0.1	
			$ OL = 100 \mu\text{A}$	3.0	_	0	0.1	_	0.1	
Low-Level	VOL	$V_{IN} = V_{IH}$		4.5	_	0	0.1	_	0.1	
Output Voltage	VOL	or V <sub>IL</sub>	$I_{OL} = 8  mA$	2.3	_	0.1	0.3	_	0.3	
			$I_{OL} = 16  \text{mA}$	3.0	_	0.15	0.4	_	0.4	
			$I_{OL} = 24 \text{ mA}$	3.0	_	0.22	0.55	_	0.55	
			$I_{OL} = 32  \text{mA}$	4.5	_	0.22	0.55	_	0.55	
Input Leakage Current	IN	V <sub>IN</sub> = 5.5 \	or GND	0~ 5.5	_		± 1	_	± 10	μΑ
Power Off Leakage Current	lOFF	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V		0.0		_	1	_	10	μΑ
Quiescent Supply Current	lcc	V <sub>IN</sub> = V <sub>CC</sub>	or GND	5.5		_	2		20	μΑ

AC ELECTRICAL CHARAC	TERISTICS (Input	$t_r = t_f = 3 \text{ ns}$
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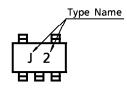
CHADACTERISTIC	CVMDOL	TEST CONDITION		Ta = 25°C		,C	Ta = -4	LINUT	
CHARACTERISTIC	SYMBOL	TEST CONDITION	V <sub>C</sub> C (V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
			1.8	2.0	5.2	10.0	2.0	10.5	
		$C_L = 15  pF$ ,	2.5 ± 0.2	0.8	3.4	7.0	0.8	7.5	
Propagation	t <sub>PLH</sub>	$R_L = 1 M\Omega$	3.3 ± 0.3	0.5	2.6	4.7	0.5	5.0	
Delay Time t <sub>PHL</sub>		5.0 ± 0.5	0.5	2.2	4.1	0.5	4.4	ns	
	$C_L = 50 \text{ pF},$	3.3 ± 0.3	1.5	3.3	5.2	1.5	5.5		
		$R_L = 500 \Omega$	5.0 ± 0.5	0.8	2.7	4.5	0.8	4.8	
Input Capacitance	C <sub>IN</sub>		0~5.5	_	4	_	_	_	рF
Power Dissipation CPD (	C==	(Note 1)	3.3	_	20	_	_	_	nE
	(Note 1)	5.5	_	25	_	_	_	pF	

(Note 1) CpD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

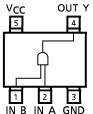
Average operating current can be obtained by the equation.

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

### **MARKING**



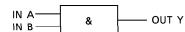
PIN ASSIGNMENT (TOP VIEW)



TRUTH TABLE

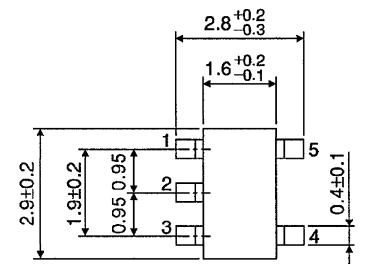
Α	В	Y
L	L	L
L	Н	L
Н	L	L
Η	Н	Ι

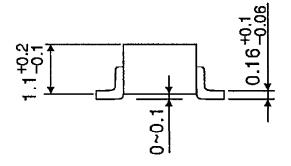
LOGIC DIAGRAM



## PACKAGE DIMENSIONS SSOP5-P-0.95

Unit: mm

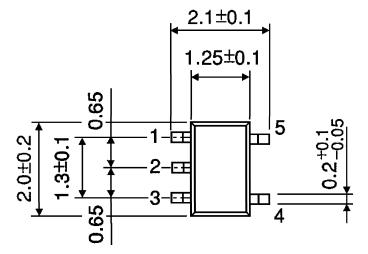


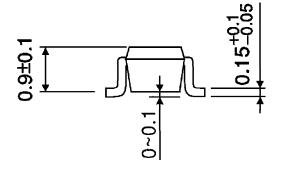


Weight: 0.016 g (Typ.)

## PACKAGE DIMENSIONS SSOP5-P-0.65A

Unit: mm





Weight: 0.006 g (Typ.)

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