TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ08AFE

2 Input AND Gate

Features

• High output drive: ±24 mA (min)

at $V_{CC} = 3 V$

• Super high speed operation: t_{pd} 2.7 ns (typ.)

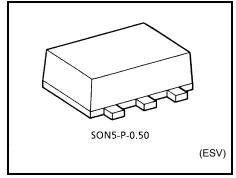
at $V_{CC} = 5 \text{ V}, 50 \text{ pF}$

Operation voltage range: V_{CC (opr.)} = 1.8~5.5 V

• Supply voltage data retention: V_{CC} = 1.5~5.5 V

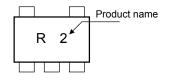
• 5.5-V tolerant inputs

 \bullet Matches the performance of TC74LCX series when operated at 3.3 -V V_{CC}

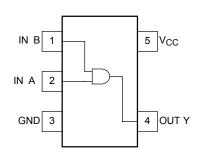


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10 s)	TL	260	°C



Logic Diagram



Truth Table

Α	В	Υ
L	L	L
٦	Н	L
Н	L	L
Н	Н	Н

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	Voc	1.8~5.5	V
Supply voltage	Vcc	1.5~5.5 (Note 1)	
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
		$0~20~(V_{CC} = 1.8~V,~2.5~V \pm 0.2~V)$	ns/V
Input rise and fall time	d _t /d _v	$0\sim10~(V_{CC}=3.3~V\pm0.3~V)$	
		$0~5~(V_{CC} = 5.5~V \pm 0.5~V)$	

Note 1: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition		et Condition		Ta = 25°C			Ta = -40~85°C		Unit	
		16	V _{CC} (V)		Min	Тур.	Max	Min	Max	Unit
High-level input		1.8		0.75 × V _{CC}		ı	0.75 × V _{CC}		V	
voltage		_		0.7 × V _{CC}		ı	0.7 × V _{CC}		V	
Low-level input	r-level input			1.8	_		0.25 × V _{CC}		0.25 × V _{CC}	V
voltage	_		2.3-5.5	_	_	0.3 × V _{CC}	_	0.3 × V _{CC}	V	
				1.8	1.7	1.8		1.7	_	
			I _{OH} = -100 μA	2.3	2.2	2.3	_	2.2	_	
			ΙΟΗ = -100 μΑ	3.0	2.9	3.0		2.9	_	
High-level	Voh	V(x) = V()		4.5	4.4	4.5	_	4.4	_	٧
output voltage	VOH	V _{IN} = V _{IH}	$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15		1.9	_	
			I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
		I _{OH} = -24 mA	3.0	2.3	2.68		2.3	_		
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
				1.8		0	0.1	_	0.1	
Low-level output VOL V	$V_{IN} = V_{IH}$	$I_{OL} = 100 \ \mu A$	2.3		0	0.1	_	0.1	·	
			3.0		0	0.1	_	0.1		
			4.5	_	0	0.1	_	0.1		
		$I_{OL} = 8 \text{ mA}$	2.3		0.1	0.3	_	0.3		
		I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4		
		I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55		
			I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0-5.5	_	_	±1	_	±10	μА
Quiescent supply current	ICC	V _{IN} = V _{CC} or GND		5.5	_	_	2	_	20	μА

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		- Unit	
	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
Propagation delay tpLH time tpHL		$\begin{aligned} C_L &= 15 \text{ pF}, \\ R_L &= 1 \text{ M}\Omega \end{aligned}$	1.8	2.0	5.2	9.5	2.0	10.5	- ns
			2.5 ± 0.2	0.8	3.4	7.0	0.8	7.5	
	t _{pLH}		3.3 ± 0.3	0.5	2.6	4.7	0.5	5.0	
	t_{pHL}		5.0 ± 0.5	0.5	2.2	4.1	0.5	4.4	
		C _L = 50 pF,	3.3 ± 0.3	1.5	3.3	5.2	1.5	5.5	
		$R_L = 500 \Omega$	5.0 ± 0.5	0.8	2.7	4.5	0.8	4.8	
Input capacitance	C _{IN}	_	0-5.5	_	4	_	_	_	pF
Power dissipation capacitance CPD	Con	(Note 2)	3.3	_	19	_	_	_	pF
	CPD	(Note 2)	5.5	_	26	_	_	_	ÞΓ

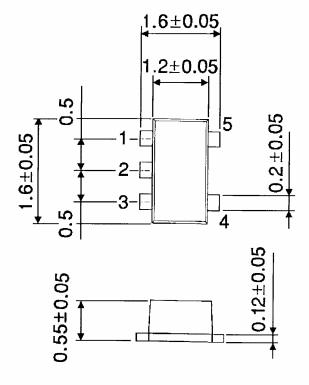
Note2: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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