

August 1986 Revised May 2000

DM74S03

Quad 2-Input NAND Gate with Open-Collector Outputs

General Description

This device contains four independent gates each of which performs the logic NAND function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} (Min) - V_{OH}}{N_1 (I_{OH}) + N_2 (I_{IH})}$$

$$R_{MIN} = \frac{V_{CC} \left(Max \right) - V_{OL}}{I_{OL} - N_3 \left(I_{IL} \right)}$$

Where: N_1 (I_{OH}) = total maximum output HIGH current

for all outputs tied to pull-up resistor

 N_2 (I_{IH}) = total maximum input HIGH current for all inputs tied to pull-up resistor

 N_3 (I_{IL}) = total maximum input LOW current for

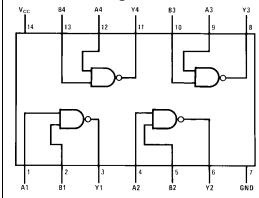
all inputs tied to pull-up resistor

Ordering Code:

Order Number	Package Number	Package Description
DM74S03MX	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow

This device is only available in Tape and Reel.

Connection Diagram



Function Table

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 5.5V Output Voltage 7V Operating Free Air Temperature Range 0°C to $+70^{\circ}$ C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
I _{OL}	LOW Level Output Current			20	mA
T _A	Free Air Operating Temperature	0		70	°C

 $-65^{\circ}C$ to $+150^{\circ}C$

Electrical Characteristics

Storage Temperature Range

over recommended operating free air temperature (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.2	V
I _{CEX}	HIGH Level Output Current	$V_{CC} = Min, V_O = 5.5V$ $V_{IL} = Max$			250	μΑ
V _{OL}	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$			0.5	V
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			50	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.5V$			-2	mA
I _{CCH}	Supply Current with Outputs HIGH	V _{CC} = Max		6.0	13.2	mA
I _{CCL}	Supply Current with Outputs LOW	V _{CC} = Max		20	36	mA

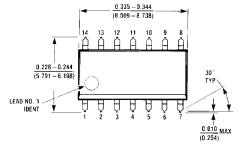
Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

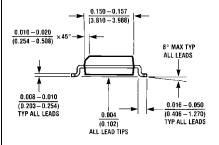
Switching Characteristics

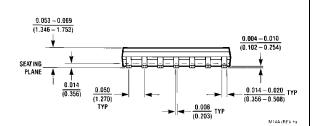
at $V_{CC}=5V$ and $T_A=25^{\circ}C$

	Parameter	$R_L = 280\Omega$				
Symbol		C _L = 15 pF		C _L = 50 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time	2	7.5	3	11	ns
	LOW-to-HIGH Level Output	2	7.5	3	''	115
t _{PHL}	Propagation Delay Time	2	7	3	11	ns
	HIGH-to-LOW Level Output		,	3	''	115









14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow Package Number M14A

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