

August 1986 Revised March 2000

DM74LS09

Quad 2-Input AND Gates with Open-Collector Outputs

General Description

This device contains four independent gates each of which performs the logic AND function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} \left(Min \right) - V_{OH}}{N_1 \left(I_{OH} \right) + N_2 \left(I_{IH} \right)}$$

$$R_{MIN} = \frac{V_{CC} (Max) - V_{OL}}{I_{OL} - N_3 (I_{IL})}$$

Where: N_1 (I_{OH}) = total maximum output high current

for all outputs tied to pull-up resistor

 $N_2 \ (I_{IH}) = total \ maximum \ input \ high \ current \ for \ all \ inputs \ tied \ to \ pull-up \ resistor$

 N_3 (I_{IL}) = total maximum input low current for

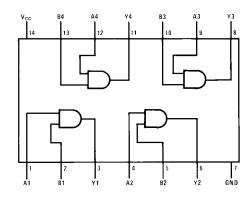
all inputs tied to pull-up resistor

Ordering Code:

Order Number	Package Number	Package Description
DM74LS09M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
DM74LS09N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel.

Connection Diagram



Function Table

 $\mathbf{Y} = \mathbf{A}\mathbf{B}$

H = HIGH Logic Level L = LOW Logic Level

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Absolute Maximum Ratings(Note 1)

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
OL	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

 $-65^{\circ}C$ to $+150^{\circ}C$

Electrical Characteristics

Storage Temperature Range

over recommended operating free air temperature range (unless otherwise noted)

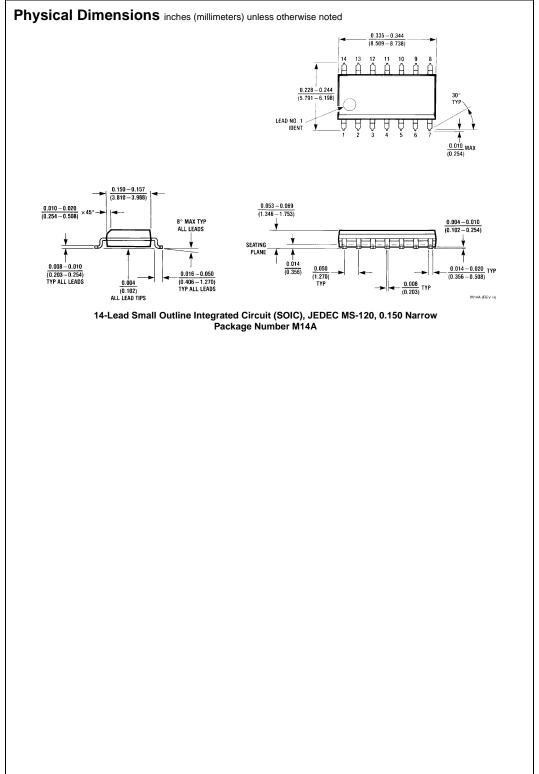
Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
I _{CEX}	HIGH Level	$V_{CC} = Min, V_O = 5.5V$			100	μА
	Output Current	$V_{IH} = Min$				
V _{OL}	LOW Level	$V_{CC} = Min, I_{OL} = Max$		0.35	0.5	V
	Output Voltage	$V_{IL} = Max$				
		I _{OL} = 4 mA, V _{CC} = Min		0.25	0.4	
II	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
I _{CCH}	Supply Current with Outputs HIGH	V _{CC} = Max		2.4	4.8	mA
I _{CCL}	Supply Current with Outputs LOW	V _{CC} = Max		4.4	8.8	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

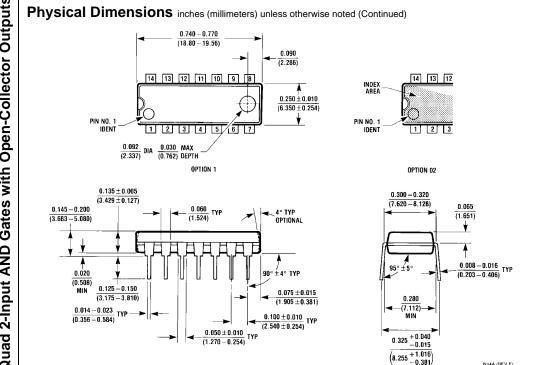
Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

	Parameter					
Symbol		C _L = 15 pF		C _L = 50 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	5	20	8	45	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	4	15	6	27	ns



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14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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