DM74LS574 **Octal D-Type Flip-Flop with 3-STATE Outputs**

General Description

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SEMICONDUCTOR

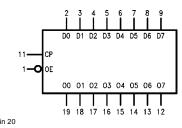
The DM74LS574 is a high speed low power octal flip-flop with a buffered common Clock (CP) and a buffered common Output Enable (\overline{OE}). The information presented to the D inputs is stored in the flip-flops on the LOW-to-HIGH Clock (CP) transition.

This device is functionally identical to the DM74LS374 except for the pinouts.

Ordering Code:

Order Number	Package Number	Package Description
DM74LS574WM	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide
DM74LS574N	N20A	20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
Devices also available	in Tape and Reel. Specify	y by appending the suffix letter "X" to the ordering code.

Logic Symbol



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ŌE -		20	-v _{cc}
D0 —	2	19	-00
D1 —	3	18	-01
D2 —	4	17	-02
D3 —	5	16	-03
D4 —	6	15	-04
D5 —	7	14	-05
D6 —	8	13	- 06
D7 —	9	12	- 07
GND —	10	11	- CP

Connection Diagram

V_{CC} = Pin 20 GND = Pin 10

Truth Table

In	Inputs		puts
Dn	CP	OE	On
Н	~	L	Н
L	~	L	L
Х	х	Н	Z

H = HIGH Voltage Level L = LOW Voltage Level

X = Immaterial

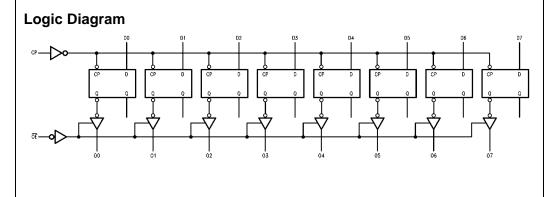
Z = High Impedance ~ = HIGH-to-LOW Clock (CP) transition

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Functional Description

The DM74LS574 consists of eight edge-triggered flip-flops with individual D-type inputs and 3-STATE true outputs. The buffered clock and buffered Outputs Enable are common to all flip-flops. The eight flip-flops will store the state of their individual D inputs that meet the setup and hold times requirements on the LOW-to-HIGH Clock (CP) tran-

sition. With the Output Enable ($\overline{\text{OE}}$) LOW, the contents of the eight flip-flops are available at the outputs. When the $\overline{\text{OE}}$ is HIGH, the outputs go to the high impedance state. Operation of the $\overline{\text{OE}}$ input does not affect the state of the flip-flops.



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Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

DM74LS574

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units	
V _{CC}	Supply Voltage	4.75	5	5.25	V	
V _{IH}	HIGH Level Input Voltage	2			V	
V _{IL}	LOW Level Input Voltage			0.8	V	
I _{OH}	HIGH Level Output Current			-2.6	mA	
I _{OL}	LOW Level Output Current			24	mA	
T _A	Free Air Operating Temperature	0		70	°C	
t _S (H) Setup Time HIGH or LOW		20			ns	
t _S (L)	Dn to CP	20			115	
t _H (H)	Hold Time HIGH or LOW	0			ns	
t _H (L)	Dn to CP	0			115	
t _W (H)	CP Pulse Width	15			-	
t _W (L)	HIGH or LOW	15			ns	

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 mA$			-1.5	V
V _{OH}	HIGH Level	$V_{CC} = Min, I_{OH} = Max,$	2.4	3.3		V
	Output Voltage	$V_{IL} = Max, V_{IH} = Min$	2.4	5.5		v
V _{OL}	LOW Level	$V_{CC} = Min, I_{OL} = Max,$		0.35	0.5	
	Output Voltage	$V_{IL} = Max, V_{IH} = Min$		0.55	0.5	V
		$I_{OL} = 12 \text{ mA}, V_{CC} = Min$		0.25	0.4	1
l _l	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
IIL	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-400	μΑ
I _{OZH}	OFF-State Output Current with	$V_{CC} = Max, V_O = 2.4V$			20	μA
	HIGH Level Output Voltage Applied	$V_{IH} = Min, V_{IL} = Max$			20	μΑ
I _{OZL}	OFF-State Output Current with	$V_{CC} = Max, V_O = 0.4V$			-20	
	LOW Level Output Voltage Applied	$V_{IH} = Min, V_{IL} = Max$			-20	μA
I _{OS}	Short Circuit Output Current (Note 3)	V _{CC} = Max	-30		-130	mA
Icc	Supply Current	V _{CC} = Max (Note 4)			45	mA

Note 2: All typicals are at V_{CC} = 5V, T_A = 25^{\circ}C.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 4: $I_{\rm CC}$ is measured with the DATA inputs grounded and the OUTPUT CONTROLS at 4.5V.

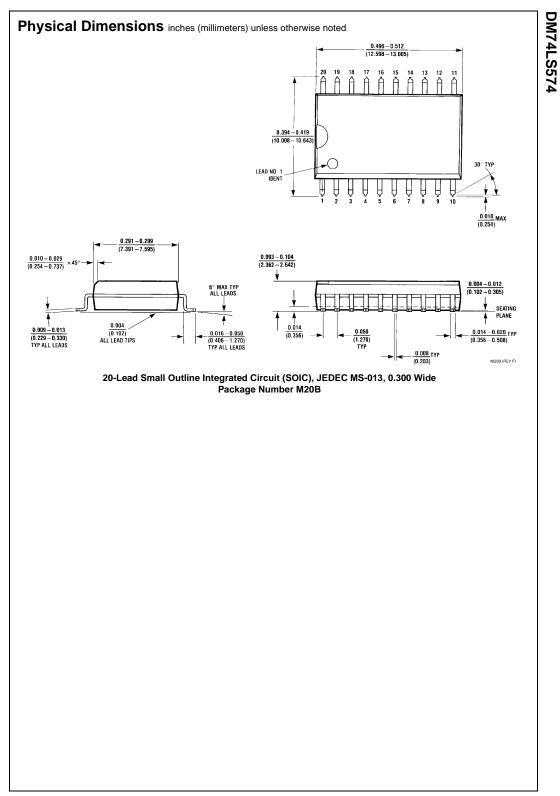
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Switching Characteristics

Symbol	Parameter	$\mathbf{R}_{\mathbf{L}} = 2 \ \mathbf{k} \Omega, \ \mathbf{C}_{\mathbf{L}} = 45 \ \mathbf{pF}$		Units	
	Farameter	Min	Max	Onits	
MAX	Maximum Clock Frequency	35		MHz	
PLH	Propagation Delay		28	ns	
PHL	CP to On		28		
PZH	Output Enable Time		28	ns	
PZL			28		
PHZ	Output Disable Time		20	ns	
PLZ			25		

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