

LM2936Q

Ultra-Low Quiescent Current LDO Voltage Regulator

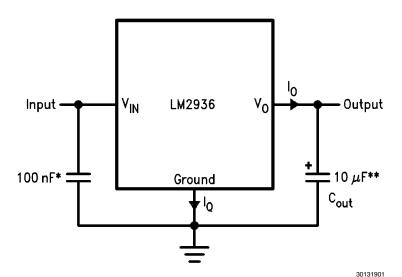
General Description

The LM2936Q ultra-low quiescent current regulator features low dropout voltage and low current in the standby mode. With less than 15 μA quiescent current at a 100 μA load, the LM2936Q is ideally suited for automotive and other battery operated systems. The LM2936Q retains all of the features that are common to low dropout regulators including a low dropout PNP pass device, short circuit protection, reverse battery protection, and thermal shutdown. The LM2936Q has a 40V maximum operating voltage limit, a -40°C to $+125^{\circ}\text{C}$ operating temperature range, and $\pm3\%$ output voltage tolerance over the entire output current, input voltage, and temperature range. The LM2936Q is available in a SO-8 package.

Features

- AEC-Q100 Grade 1 Qualified (-40°C to 125°C)
- Ultra low quiescent current ($I_Q \le 15 \mu A$ for $I_Q = 100 \mu A$)
- Fixed 3.0V, 3.3V or 5.0V with 50 mA output
- ±2% Initial output tolerance
- ±3% Output tolerance over line, load, and temperature
- Dropout voltage typically 200 mV @ I_O = 50 mA
- Reverse battery protection
- -50V reverse transient protection
- Internal short circuit current limit
- Internal thermal shutdown protection
- 40V operating voltage limit

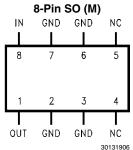
Typical Application



* Required if regulator is located more than 2 from power supply filter capacitor.

^{**} Required for stability. See Electrical Characteristics for required values. Must be rated over intended operating temperature range. Effective series resistance (ESR) is critical, see curve. Locate capacitor as close as possible to the regulator output and ground pins. Capacitance may be increased without bound.

Connection Diagram



Top View LM2936QM and LM2936QMX See NS Package Number M08A

Ordering Information

Output Voltage	High Voltage	Shutdown Pin	Order	Package Type	Package Drawing	Transport Media	Feature
2 201/	-	-	LM2936QM-3.3	8-Lead SOIC	M08A	Rail	AEC-Q100 Grade 1
3.30V	-	-	LM2936QMX-3.3	8-Lead SOIC	M08A	Tape/Reel	Qualified.
5.00V	-		LM2936QM-5.0	8-Lead SOIC	M08A	Rail	Automotive Grade
	-	-	LM2936QMX-5.0	8-Lead SOIC	M08A	Tape/Reel	Production Flow. *

^{*} Automotive grade (Q) product incorporates enhanced manufacturing and support processes for teh automotive market, including defect detection methodologies. Reliability qualification is compliant with the requirements

and temperature grades defined in the AEC-Q100 standard. Automotive grade products are identified with the letter Q. For more information go to http://www.national.com/automotive.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Input Voltage (Survival) +60V, -50V ESD Susceptibility (*Note 2*) 2000V Power Dissipation (*Note 3*) Internally limited Junction Temperature (T_{Jmax}) 150°C Storage Temperature Range -65°C to +150°C

Lead Temperature

(Soldering, 10 sec.) 260°C

Operating Ratings

Operating Temperature Range -40°C to +125°C Maximum Operating Input Voltage -

LM2936Q +40V

SO-8 (M08A) θ_{JA} 140°C/W SO-8 (M08A) θ_{JC} 45°C/W

Electrical Characteristics for LM2936Q-3.3

 V_{IN} = 14V, I_{O} = 10 mA, T_{J} = 25°C, unless otherwise specified. **Boldface** limits apply over entire operating temperature range

Parameter	Conditions	Min (<i>Note 5</i>)	Typical (<i>Note 4</i>)	Max (Note 5)	Units
All LM2936Q-3.3		, ,	, ,	,	1
		3.234	3.300	3.366	
Output Voltage	$4.0V \le V_{IN} \le 26V,$	3.201	3.300	3.399	V
	100 μA ≤ I _O ≤ 50 mA (<i>Note 6</i>)				
Quiescent Current	$I_{O} = 100 \ \mu A, \ 8V \le V_{IN} \le 24V$		15	20	μA
	$I_{O} = 10 \text{ mA}, 8V \le V_{IN} \le 24V$		0.20	0.50	mA
	$I_0 = 50 \text{ mA}, 8V \le V_{IN} \le 24V$		1.5	2.5	mA
Line Regulation	9V ≤ V _{IN} ≤ 16V		5	10	mV
	6V ≤ V _{IN} ≤ 40V, I _O = 1 mA		10	30	1
Load Regulation	100 μA ≤ I _O ≤ 5 mA		10	30	mV
	5 mA ≤ I _O ≤ 50 mA		10	30	
Dropout Voltage	I _O = 100 μA		0.05	0.10	V
	I _O = 50 mA		0.20	0.40	V
Short Circuit Current	V _O = 0V	65	120	250	mA
Output Impedance	I _O = 30 mAdc and 10 mArms,		450		mΩ
	_f = 1000 Hz				
Output Noise Voltage	10 Hz–100 kHz		500		μV
Long Term Stability			20		mV/1000 Hr
Ripple Rejection	V _{ripple} = 1V _{rms} , _{fripple} = 120 Hz	-40	-60		dB
Reverse Polarity	$R_L = 500\Omega, T = 1 \text{ ms}$	-50	-80		V
Transient Input Voltage					
Output Voltage with	$V_{IN} = -15V, R_L = 500\Omega$		0.00	-0.30	V
Reverse Polarity Input					
Maximum Line Transient	$R_L = 500\Omega, V_O \le 3.63V, T = 40ms$	60			V
Output Bypass	C _{OUT} = 22μF				Ω
Capacitance (C_{OUT}) ESR	0.1mA ≤ I _{OUT} ≤ 50mA	0.3		8	

Electrical Characteristics for LM2936Q-5.0

 V_{IN} = 14V, I_{O} = 10 mA, T_{J} = 25°C, unless otherwise specified. **Boldface** limits apply over entire operating temperature range

Parameter	Conditions	Min (<i>Note 5</i>)	Typical (Note 4)	Max (Note 5)	Units
AII LM2936Q-5.0					
		4.90	5.00	5.10	
Output Voltage	$5.5V \le V_{IN} \le 26V,$	4.85	5.00	5.15	V
	100 μA ≤ I _O ≤ 50 mA (<i>Note 6</i>)	4.03			
Quiescent Current	$I_{O} = 100 \ \mu A, \ 8V \le V_{IN} \le 24V$		9	15	μA
	$I_{O} = 10 \text{ mA}, 8V \le V_{IN} \le 24V$		0.20	0.50	mA
	$I_{O} = 50 \text{ mA}, 8V \le V_{IN} \le 24V$		1.5	2.5	mA
Line Regulation	9V ≤ V _{IN} ≤ 16V		5	10	mV
	6V ≤ V _{IN} ≤ 40V, I _O = 1 mA		10	30	
Load Regulation	100 μA ≤ I _O ≤ 5 mA		10	30	mV
	5 mA ≤ I _O ≤ 50 mA		10	30	
Dropout Voltage	Ι _Ο = 100 μΑ		0.05	0.10	V
	I _O = 50 mA		0.20	0.40	V
Short Circuit Current	V _O = 0V	65	120	250	mA
Output Impedance	I _O = 30 mAdc and 10 mArms,		450		mΩ
	_f = 1000 Hz				
Output Noise Voltage	10 Hz–100 kHz		500		μV
Long Term Stability			20		mV/1000 Hi
Ripple Rejection	V _{ripple} = 1V _{rms} , _{fripple} = 120 Hz	-40	-60		dB
Reverse Polarity	$R_L = 500\Omega$, $T = 1 \text{ ms}$	-50	-80		V
Transient Input Voltage					
Output Voltage with	$V_{IN} = -15V, R_{L} = 500\Omega$		0.00	-0.30	V
Reverse Polarity Input					
Maximum Line Transient	$R_L = 500\Omega, V_O \le 5.5V, T = 40ms$	60			V
Output Bypass	C _{OUT} = 10μF	0.2		8	Ω
Capacitance (C _{OUT}) ESR	0.1mA ≤ I _{OUT} ≤ 50mA	0.3			

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its specified operating ratings.

Note 2: Human body model, 100 pF discharge through a 1.5 $k\Omega$ resistor.

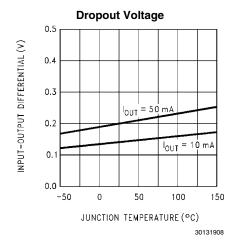
Note 3: The maximum power dissipation is a function of T_{Jmax} , θ_{JA} , and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{Jmax} - T_A)/\theta_{JA}$. If this dissipation is exceeded, the die temperature will rise above 150°C and the LM2936Q will go into thermal shutdown.

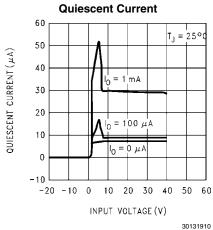
Note 4: Typicals are at 25°C (unless otherwise specified) and represent the most likely parametric norm.

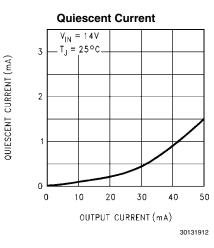
Note 5: Datasheet min/max specification limits are guaranteed by design, test, or statistical analysis.

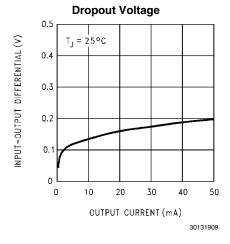
 $\textbf{Note 6:} \ \ \textbf{To ensure constant junction temperature, pulse testing is used.}$

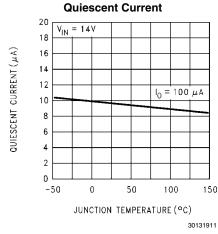
Typical Performance Characteristics

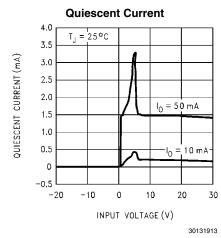


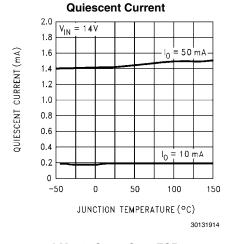


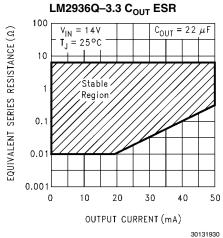


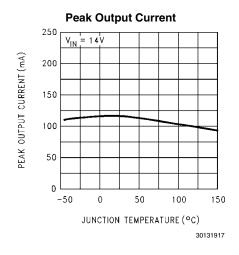


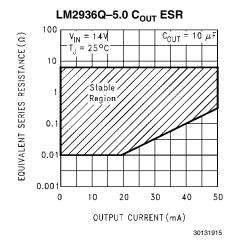


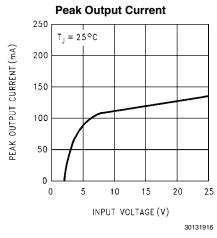


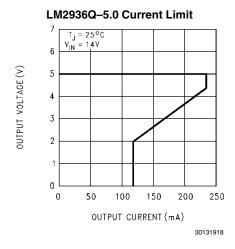




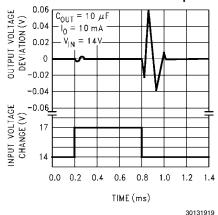




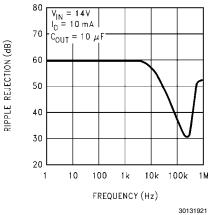




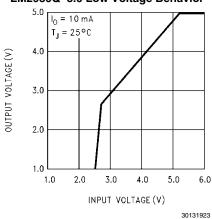
LM2936Q-5.0 Line Transient Response



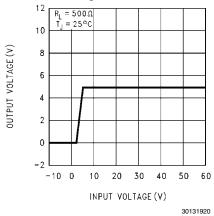
LM2936Q-5.0 Ripple Rejection



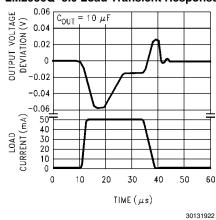
LM2936Q-5.0 Low Voltage Behavior



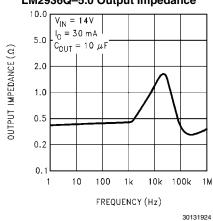
LM2936Q-5.0 Output at Voltage Extremes



LM2936Q-5.0 Load Transient Response



LM2936Q-5.0 Output Impedance



Applications Information

Unlike other PNP low dropout regulators, the LM2936Q remains fully operational to 40V. Owing to power dissipation characteristics of the available packages, full output current cannot be guaranteed for all combinations of ambient temperature and input voltage. As an example, consider an LM2936QZ–5.0 operating at 25°C ambient. Using the formula for maximum allowable power dissipation given in (*Note 3*), we find that $P_{Dmax}=641~\text{mW}$ at 25°C. Including the small contribution of the quiescent current to total power dissipation the maximum input voltage (while still delivering 50 mA output current) is 17.3V. The LM2936QZ–5.0 will go into thermal shutdown if it attempts to deliver full output current with an input voltage of more than 17.3V. Similarly, at 40V input and 25°C ambient the LM2936QZ–5.0 can deliver 18 mA maximum.

Under conditions of higher ambient temperatures, the voltage and current calculated in the previous examples will drop. For instance, at the maximum ambient of 125°C the LM2936QZ–5.0 can only dissipate 128 mW, limiting the input voltage to 7.34V for a 50 mA load, or 3.5 mA output current for a 40V input.

The junction to ambient thermal resistance θ_{JA} rating has two distinct components: the junction to case thermal resistance

rating $\theta_{JC};$ and the case to ambient thermal resistance rating $\theta_{CA}.$ The relationship is defined as: θ_{JA} = θ_{JC} + $\theta_{CA}.$

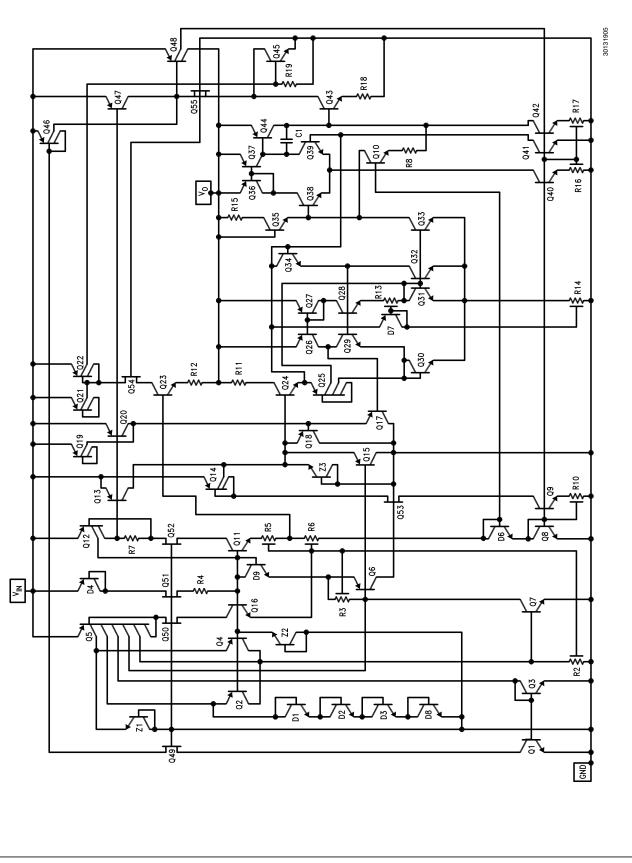
For the SO-8 the θ_{JA} rating can be improved by using the copper mounting pads on the printed circuit board as a thermal conductive path to extract heat from the package.

On the SO-8 package the four ground pins are thermally connected to the backside of the die. Adding approximately 0.04 square inches of 2 oz. copper pad area to these four pins will improve the $\theta_{\rm JA}$ rating to approximately 110°C/W. If this extra pad are is placed directly beneath the package there should not be any impact on board density.

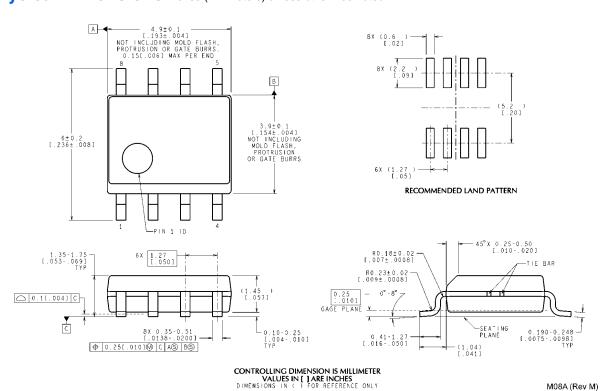
While the LM2936Q has an internally set thermal shutdown point of typically 160°C, this is intended as a safety feature only. Continuous operation near the thermal shutdown temperature should be avoided as it may have a negative affect on the life of the device.

While the LM2936Q maintains regulation to 60V, it will not withstand a short circuit above 40V because of safe operating area limitations in the internal PNP pass device. Above 60V the LM2936Q will break down with catastrophic effects on the regulator and possibly the load as well. Do not use this device in a design where the input operating voltage may exceed 40V, or where transients are likely to exceed 60V.

Equivalent Schematic Diagram

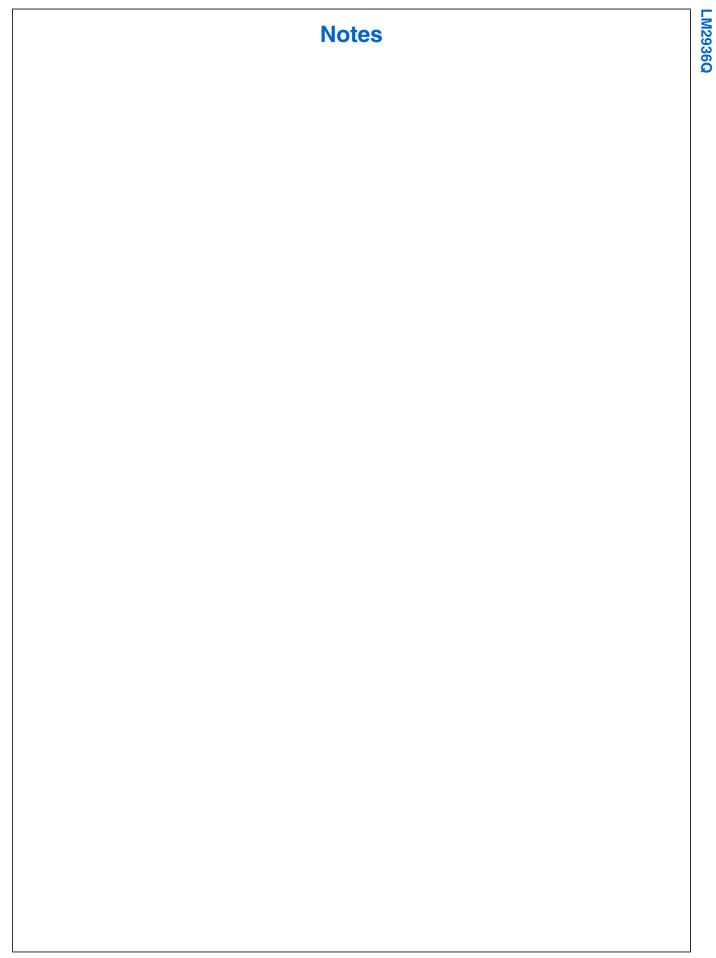


Physical Dimensions inches (millimeters) unless otherwise noted



8-Lead Small Outline Molded Package (M) NS Package Number M08A

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Notes

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