



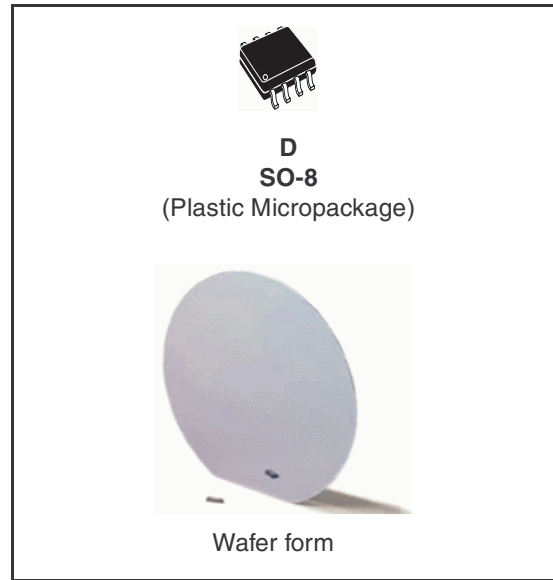
Low Power Dual Voltage Comparators

- Wide single supply voltage range or dual supplies +2V to +36V or $\pm 1V$ to $\pm 18V$
- Very low supply current (0.4mA) independent of supply voltage (1mW/comparator at +5V)
- Low input bias current: 25nA typ.
- Low input offset current: $\pm 5nA$ typ.
- Input common-mode voltage range includes ground
- Low output saturation voltage: 250mV typ. ($I_O = 4mA$)
- Differential input voltage range equal to the supply voltage
- TTL, DTL, ECL, MOS, CMOS compatible outputs

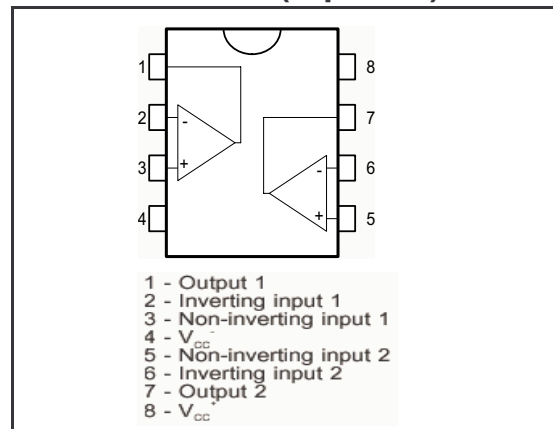
Description

This device consists of two independent low power voltage comparators designed specifically to operate from a single supply over a wide range of voltages. Operation from split power supplies is also possible.

These comparators also have a unique characteristic in that the input common-mode voltage range includes ground even though operated from a single power supply voltage.



Pin Connection (top view)



Order Codes

Part Number	Temperature Range	Package	Packaging	Marking
JLM2903H- E6D1	-40°C, +150°C	Wafer		
LM2903HD		SO-8	Tube or Tape & Reel	2903H
LM2903HYD/HYDT		SO-8 (automotive grade level)	Tube or Tape & Reel	2903HY

1 Absolute Maximum Ratings

Table 1. Key parameters and their absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	± 18 or 36	V
V_{ID}	Differential Input Voltage	± 36	V
V_I	Input Voltage	-0.3 to +36	V
	Output Short-circuit to Ground ⁽¹⁾	20	mA
P_d	Power Dissipation at $T_{AMB}=+25^\circ\text{C}$ ⁽²⁾	710	mW
T_{stg}	Storage Temperature Range	-65 to +150	$^\circ\text{C}$

- Short-circuit from the output to V_{CC}^+ can cause excessive heating and eventual destruction. The maximum output current is approximately 20mA, independent of the magnitude of V_{CC}^+
- P_d is calculated with $T_{amb} = +25^\circ\text{C}$, $T_{Junction} = +150^\circ\text{C}$ and $R_{thja} = 175^\circ\text{C/W}$ for SO8 package

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
V_{CC}^+	Supply Voltage	2 to 36	V
T_{oper}	Operating Free-Air Temperature Range	-40 to +150	$^\circ\text{C}$

2 Typical Application Schematics

Figure 1. Schematic diagram (1/2 LM2903)

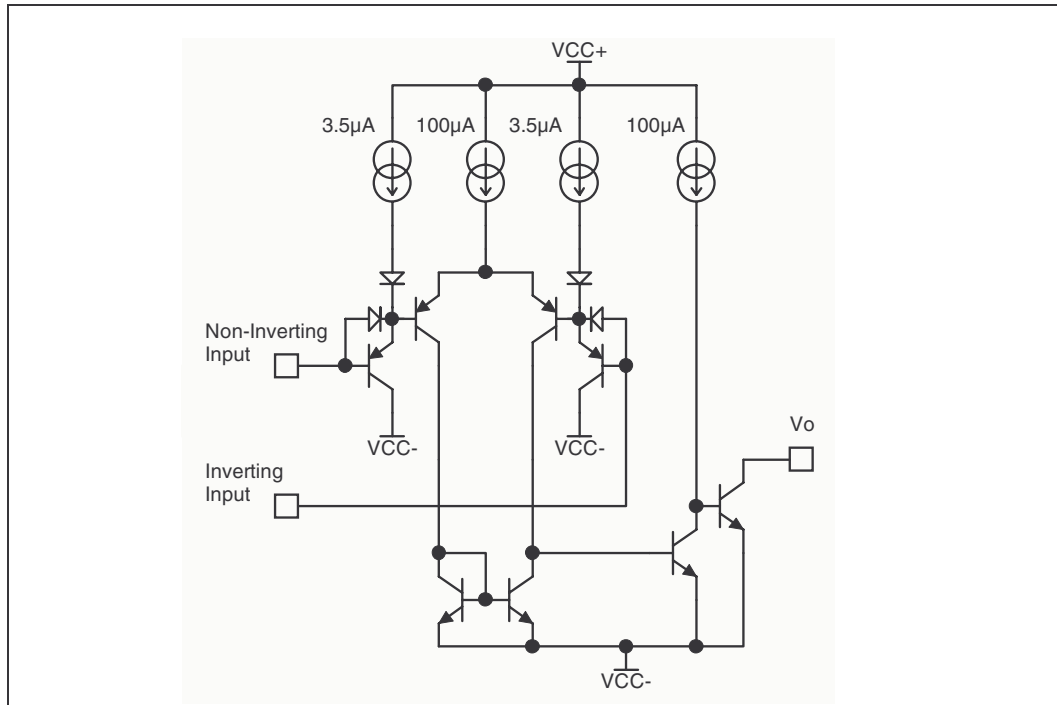
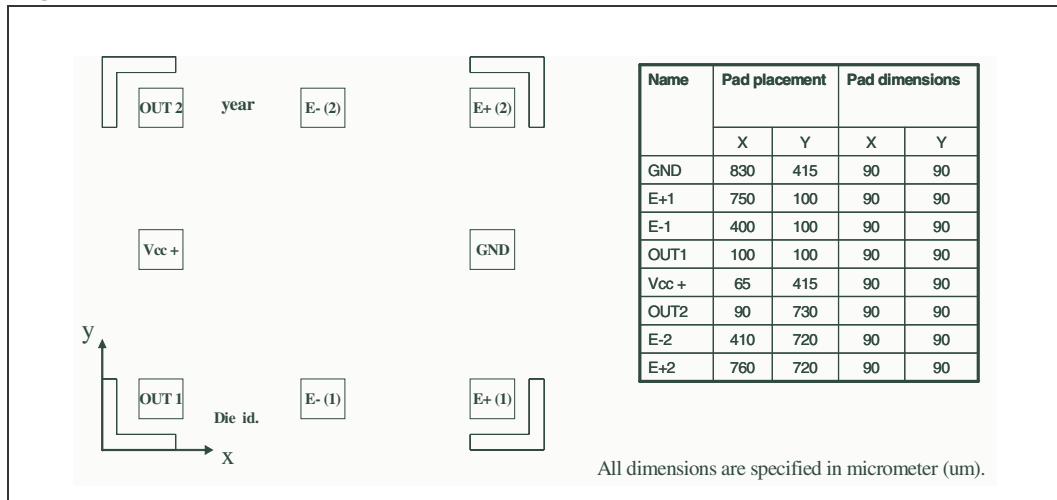


Figure 2. Pad locations



3 Electrical Characteristics

Table 3. $V_{CC}^+ = 5V$, $V_{CC}^- = GND$, $T_{amb} = 25^\circ C$ (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit
V_{IO}	Input Offset Voltage ⁽¹⁾ $T_{amb} = +25^\circ C$ $T_{min} \leq T_{amb} \leq T_{max}$.		1	7 15	mV
I_{IO}	Input Offset Current $T_{amb} = +25^\circ C$ $T_{min} \leq T_{amb} \leq T_{max}$.		5	50 150	nA
I_{IB}	Input Bias Current ⁽²⁾ $T_{amb} = +25^\circ C$ $T_{min} \leq T_{amb} \leq T_{max}$.		25	250 400	nA
A_{VD}	Large Signal Voltage Gain ($V_{CC} = 15V, R_L = 15k\Omega, V_O = 1$ to $11V$)	25	200		V/ mV
I_{CC}	Supply Current (all comparators) $V_{CC} = 5V$, no load $V_{CC} = 30V$, no load		0.4 1	1 2.5	mA
V_{ICM}	Input Common Mode Voltage Range ($V_{CC} = 30V$) ⁽³⁾ $T_{amb} = +25^\circ C$ $T_{min} \leq T_{amb} \leq T_{max}$.	0 0		$V_{CC}^+ - 1.5$ $V_{CC}^+ - 2$	V
V_{ID}	Differential Input Voltage ⁽⁴⁾			V_{CC}^+	V
V_{OL}	Low Level Output Voltage ($V_{id} = -1V, I_{sink} = 4mA$) $T_{amb} = +25^\circ C$ $T_{min} \leq T_{amb} \leq T_{max}$		250	400 800	mV
I_{OH}	High Level Output Current ($V_{CC} = V_O = 30V, V_{id} = 1V$) $T_{amb} = +25^\circ C$ $T_{min} \leq T_{amb} \leq T_{max}$		0.1	1	nA μA
I_{SINK}	Output Sink Current ($V_{id} = -1V, V_O = 1.5V$) $T_{amb} = +25^\circ C$ $T_{min} \leq T_{amb} \leq T_{max}$	6 2	16		mA
t_{RE}	Small Signal Response Time ⁽⁵⁾ ($R_L = 5.1k\Omega$ to V_{CC}^+)		1.3		μs
t_{REL}	Large Signal Response Time ⁽⁶⁾ TTL Input ($V_{ref} = +1.4V, R_L = 5.1k\Omega$ to V_{CC}^+) Output Signal at 50% of final value Output Signal at 95% of final value			500 1	ns μs

1. At output switch point, $V_O = 1.4V$, $R_S = 0\Omega$ with V_{CC}^+ from 5V to 30V, and over the full input common-mode range (0V to $V_{CC}^+ - 1.5V$).
2. The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output, so no loading charge exists on the reference of input lines
3. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is $V_{CC}^+ - 1.5V$, but either or both inputs can go to +30V without damage.
4. Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than -0.3V (or 0.3V below the negative power supply, if used)
5. The response time specified is for a 100mV input step with 5mV overdrive.
6. Maximum values are guaranteed by design & evaluation.

Figure 3. Supply current vs. supply voltage

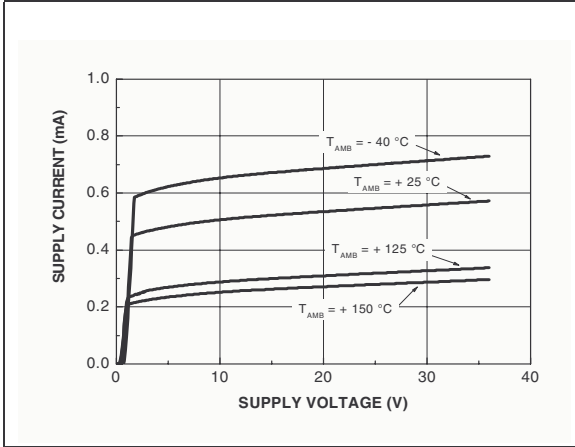


Figure 4. Input current vs. supply voltage

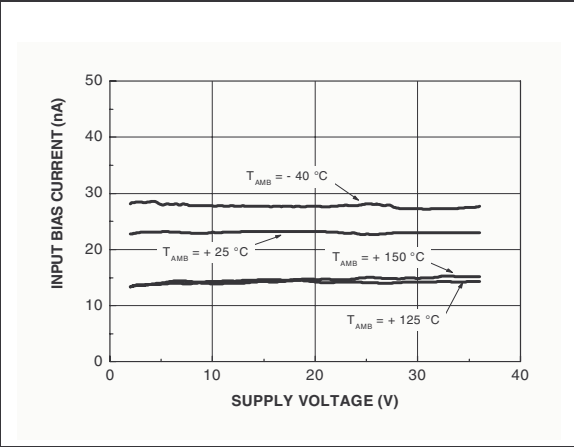


Figure 5. Output saturation voltage vs. output current ($V_{CC}=5V$)

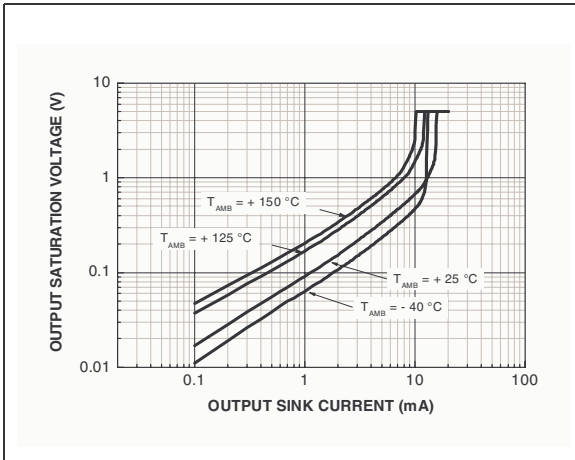


Figure 6. Response time for various input overdrives - negative transition

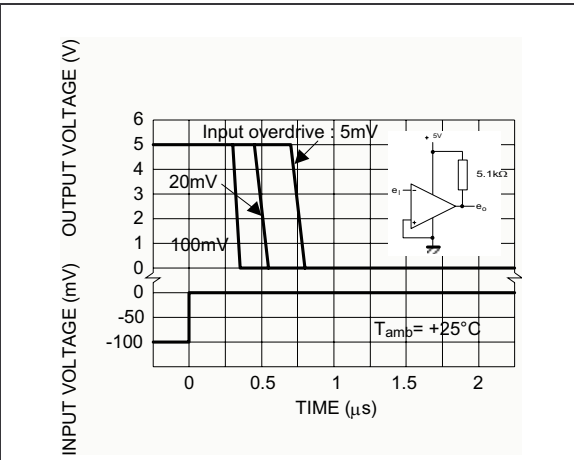
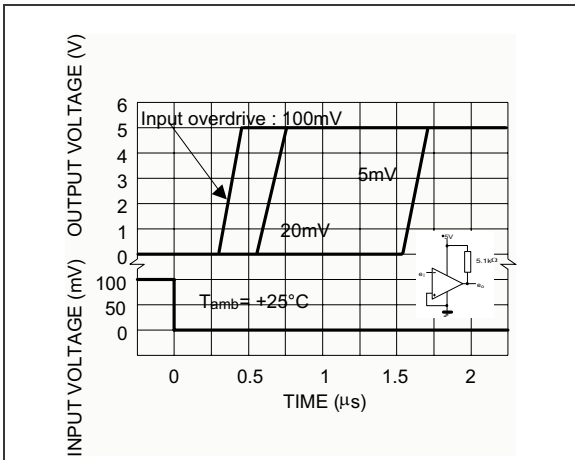


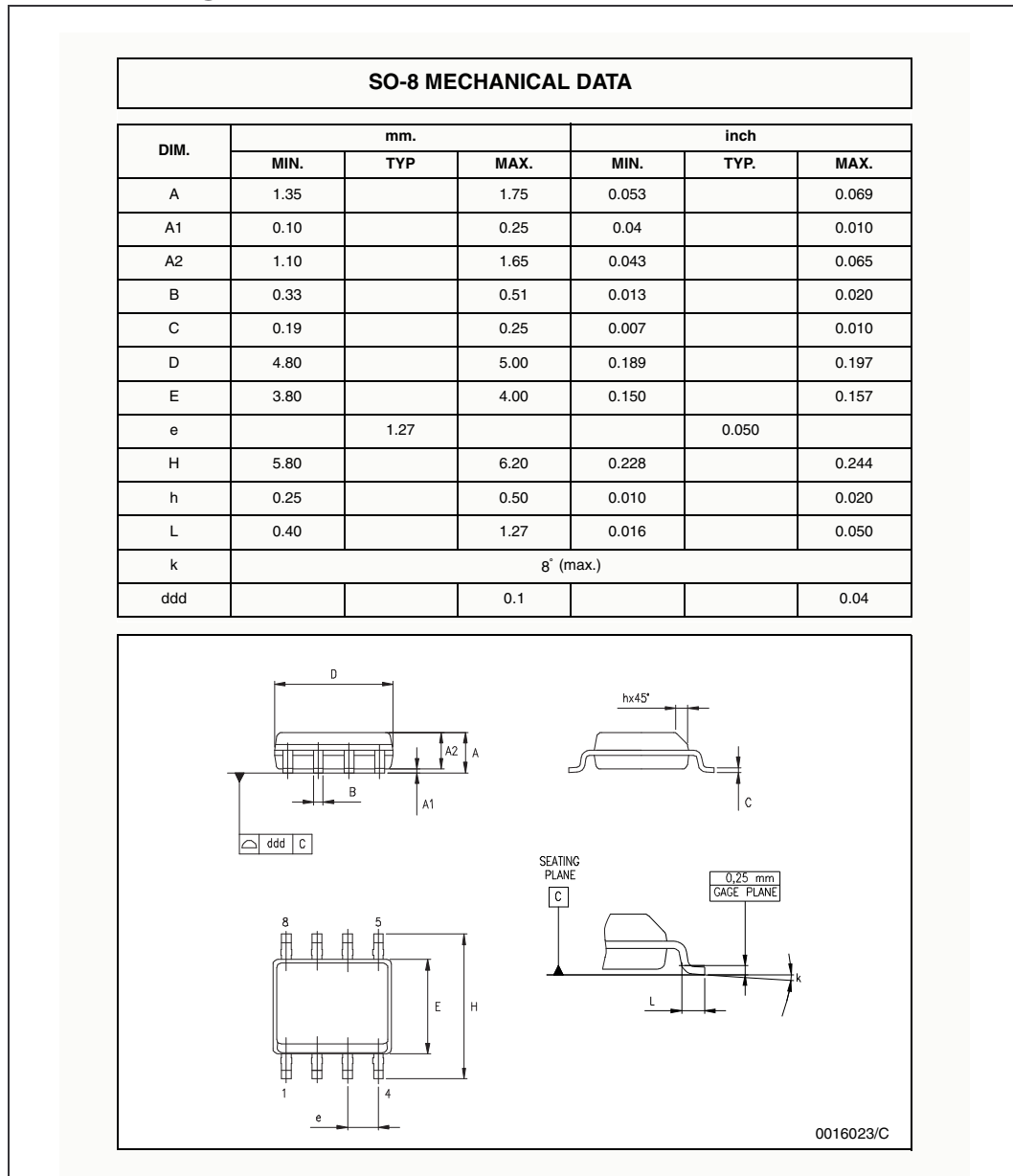
Figure 7. Response time for various input overdrives - positive transition



4 Package Mechanical Data

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

4.1 SO-8 Package



5 Revision History

Date	Revision	Changes
Sept. 2003	1	Initial release.
Aug. 2005	2	PPAP references inserted in the datasheet see : <i>Order Codes on page 1</i>

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