SIM Card Interface Filter and ESD Protection

**Small Signal Discretes** 



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## **BGF105 SIM Card Interface Filter and ESD Protection**

## BGF105

**Revision History: 2007-11-22, V2.1** 

Previous Version: 2007-10-31, V2.0							
Page	Subjects (major changes since last revision)						
4	Figure 1 replaced with schematic, 2nd schematic deleted						
6	Figure 3 background colour changed to white						
6	Figure 4 updated						
7	Figure 5 updated						
5	Table 2 typical values added						

## **BGF105**

#### **Features**

- · ESD protection circuit and interface filter for SIM cards
- Integrated ESD protection of external pins up to 15 kV contact discharge according to IEC61000-4-2
- · Wafer level package with SnAgCu solder balls
- 500 μm solder ball pitch
- · RoHS and WEEE compliant package



S-WLP-8-5



#### Description

The BGF105 is an ESD protection circuit and filtering interface for SIM cards. The external pins are protected against ESD up to 15 kV contact discharge according to IEC61000-4-2. The wafer level package is a green leadfree package with a size of only 1.45 mm x 1.45 mm and a total height of 0.65 mm

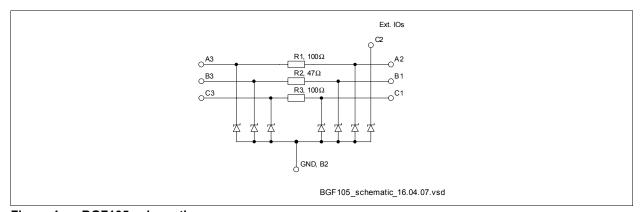


Figure 1 BGF105 schematic

Type	Package	Marking	Chip
BGF105	WLP-8-5	BGF105	N0721

Table 1 Maximum Ratings

Parameter	Symbol	Values			Unit	Note /
		Min.	Тур.	Max.		<b>Test Condition</b>
Voltage at all pins to GND	$V_{P}$	0	_	5	٧	_
Operating temperature range	$T_{OP}$	-40	_	+85	°C	_
Storage temperature range	$T_{STG}$	-65	_	+150	°C	_
Summed up into power for all pins	$P_{in}$	_	_	60	mW	T <sub>S</sub> < 70 °C
Electrostatic Discharge According to IEC61	000-4-2	•		•	•	
Contact discharge at internal pins A3, B3, C3	$V_{ESD}$	-2	_	2	kV	_
Contact discharge at external pins A2, B1, C1, C2	$V_{ESD}$	-15	_	15	kV	_



Table 2 Electrical Characteristics<sup>1)</sup>

Parameter	Symbol	Values			Unit	Note /
		Min.	Тур.	Max.		Test Condition
Resistors $R_1$ , $R_3$	R <sub>1,3</sub>	80	100	120	Ω	_
Resistor R <sub>2</sub>	$R_2$	37.6	47	56.4	Ω	_
Reverse current of ESD protection diodes	$I_{R}$		1 2	100 1000	nA nA	V = 3 V V = 5 V
Breakdown voltage of ESD diodes	$V_{(BR)}$	6.5	7.8	_	V	I <sub>(BR)</sub> = 1 mA
Line capacitance Capacitance of all lines to GND	$C_{T}$	-	17.6	20	pF	V = 0 V

<sup>1)</sup> at  $T_{\rm A}$  = 25 °C



Figure 2 **Insertion Loss** 

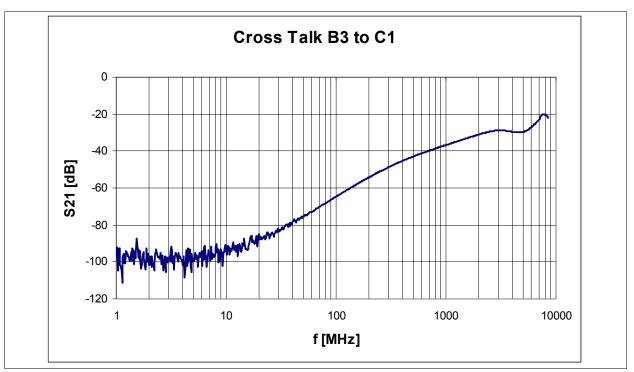


Figure 3 Cross Talk

## **Package Outlines**

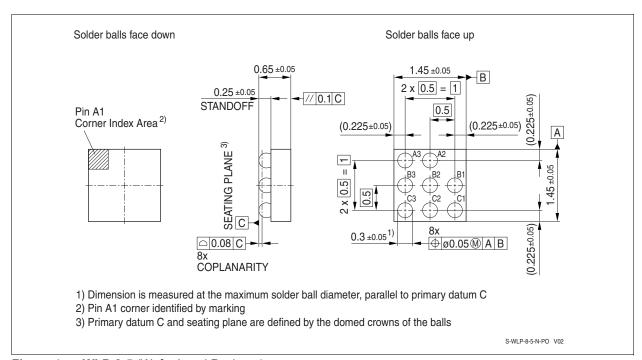


Figure 4 WLP-8-5 (Wafer Level Package)

## BGF105 SIM Card Interface Filter and ESD Protection

**BGF105** 

## Tape for BGF105

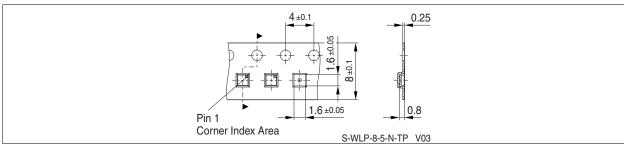


Figure 5 Tape for BGF105 / WLP-8-5

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