

EL5127C, EL5227C, EL5327C, EL5427C - Product Brief 2.5MHz 4-, 8-, 10- & 12-Channel Rail-to-Rail Buffers

Features

- 2.5MHz -3dB bandwidth
- Supply voltage = 4.5V to 16.5V
- Low supply current (per buffer) = $120\mu A$
- High slew rate = $1.5 V/\mu s$
- · Rail-to-rail input/output swing
- Ultra-small packages

Applications

- TFT-LCD drive circuits
- Electronic games
- Touch-screen displays
- Personal communication devices
- Personal digital assistants (PDA)
- Portable instrumentation

Ordering Information

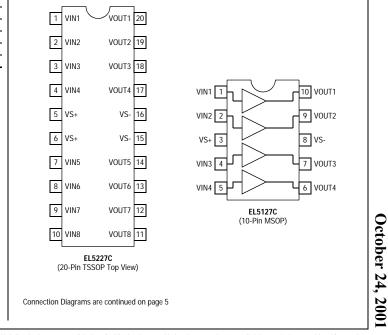
Part No	Package	Tape & Reel	Outline #
EL5127CY	10-Pin MSOP		MDP0043
EL5227CL	24-Pin LPP		MDP0046
EL5227CR	20-Pin TSSOP		MDP0044
EL5327CL	24-Pin LPP		MDP0046
EL5327CR	24-Pin TSSOP		MDP0044
EL5427CL	32-Pin LPP		MDP0046
EL5427CL	28-Pin TSSOP		MDP0044

General Description

The EL5127C, EL5227C, EL5327C, and EL5427C are low power, high voltage rail-to-rail input/output buffers designed for use in reference voltage buffering applications in small LCD displays. They are available in quad (EL5127C), octal (EL5227C), 10-channel (EL5327C), and 12-channel (EL5427C) topologies. All buffers feature a -3dB bandwidth of 2.5MHz and operate from just 120μ A per buffer. This family also features a continuous output drive capability of 30mA (sink and source).

The quad channel EL5127C is available in the 10-pin MSOP package. The 8-channel EL5227C is available in both the 20-pin TSSOP and 24-pin LPP packages, the 10-channel EL5327C in the 24-pin TSSOP and 24-pin LPP packages, and the 12-channel EL5427C in the 28-pin TSSOP and 32-pin LPP packages. All buffers are specified for operation over the full -40°C to +85°C temperature range.

Connection Diagrams



Note: All information contained in this data sheet has been carefully checked and is believed to be accurate as of the date of publication; however, this data sheet cannot be a "controlled document". Current revisions, if any, to these specifications are maintained at the factory and are available upon your request. We recommend checking the revision level before finalization of your design documentation.

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