TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC367AP,TC74HC367AF,TC74HC367AFN TC74HC368AP,TC74HC368AF,TC74HC368AFN

Hex Bus Buffer

TC74HC367AP/AF/AFN Non-Inverted (3-state)

TC74HC368AP/AF/AFN Inverted (3-state)

The TC74HC367A and TC74HC368A are high speed CMOS 3-STATE BUS BUFFERs fabricated with silicon gate C^2MOS technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

They contain six buffers; four buffers are controlled by an enable input ($\overline{G}1$), and the other two buffers are controlled by another enable input ($\overline{G}2$). The outputs of each buffer group are enabled when $\overline{G}1$ and/or $\overline{G}2$ inputs are held low; if held high, these outputs are in a high impedance state.

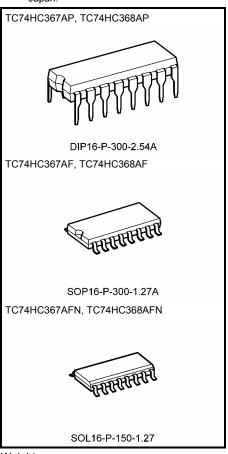
The TC74HC367A is a non-inverting output type, while the TC74HC368A is an inverting output type.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 11 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $ICC = 4 \mu A$ (max) at Ta = 25°C
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: | IOH | = IOL = 6 mA
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 74LS367/368

Note: xxxFN (JEDEC SOP) is not available in Japan.

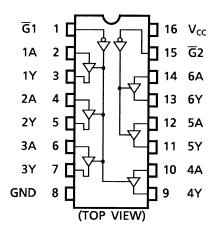


Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

Pin Assignment

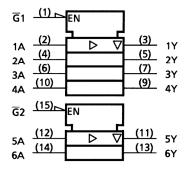
TC74HC367A



IEC Logic Symbol

TC74HC367A

HEX BUS BUFFER (3 - STATE)

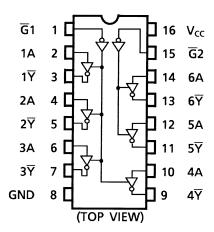


Truth Table

Inputs		Outputs					
Ğ	An	Y (367A)	√ (368A)				
L	L	L	Н				
L	Н	Н	L				
Н	Х	Z	Z				

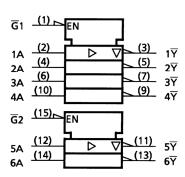
- X: Don't care
- Z: High impedance

TC74HC368A



TC74HC368A

HEX BUS BUFFER (3 - STATE / INV.)





Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±35	mA
DC V _{CC} /ground current	Icc	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	−40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 ($V_{CC} = 6.0 \text{ V}$)	

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		- Unit	
				V _{CC}	Min	Тур.	Max	Min	Max	Offic
		_		2.0	1.50		_	1.50	_	
High-level input voltage	V _{IH}			4.5	3.15	_	_	3.15	_	V
					4.20	_	_	4.20	_	
				2.0	_	_	0.50	_	0.50	
Low-level input voltage	V_{IL}	_		4.5	_	_	1.35	_	1.35	V
				6.0	_	_	1.80	_	1.80	
		V _{IN} = V _{IH} or V _{IL}		2.0	1.9	2.0	_	1.9	_	
	V _{OH}		$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0	5.9	6.0	_	5.9	_	V
			$I_{OH} = -6 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -7.8 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
	V _{OL}	V _{IN} = V _{IH} or V _I L		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \ \mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage				6.0	_	0.0	0.1	_	0.1	V
			I _{OL} = 6 mA	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 7.8 \text{ mA}$	6.0	_	0.18	0.26	_	0.33	
3-state output off-state current	loz	$V_{IN} = V_{IH}$ or V_{IL} $V_{OUT} = V_{CC}$ or GND		6.0	_	_	±0.5	_	±5.0	μА
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0			±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		6.0			4.0	_	40.0	μА



AC Characteristics (input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
CHALACICHSLICS	Symbol		CL (pF)	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Output transition time	t _{TLH}		50	2.0 4.5	_	25 7	60 12		75 15	ns
Output transition time	t _{THL}	_	30	6.0		6	10	_	13	113
				2.0	_	36	95	_	120	
			50	4.5	_	12	19	_	24	
Propagation delay time	t_{pLH}	_		6.0	_	10	16	_	20	ns
ume	t _{pHL}			2.0	_	40	130	_	165	
			150	4.5	_	16	26	_	33	
				6.0		14	22		28	
	t _{pZL} t _{pZH}	$R_L = 1 \text{ k}\Omega$		2.0	_	36	120	_	150	
			150	4.5	_	12	24	_	30	- ns
Output enable time				6.0		10	20	_	26	
				2.0	_	40	160	_	200	
				4.5	_	16	32	_	40	
				6.0	_	14	27	_	34	
	t_{pLZ}			2.0	_	35	120	_	150	
Output disable time	t _{pHZ}	$R_L = 1 \text{ k}\Omega$	50	4.5	_	15	24	_	30	ns
	-			6.0	_	13	20	_	26	
Input capacitance	C _{IN}	_			_	5	10	_	10	pF
Output capacitance	C _{OUT}	_			_	10	_	_	_	pF
Power dissipation	C_{PD}	TC74HC367A			_	36	_	_	_	pF
capacitance	(Note)	TC74HC368A			_	30	_	_	_	μ.

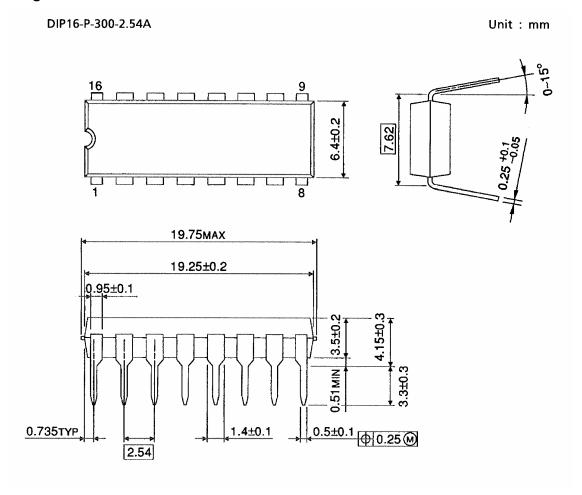
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per bit)



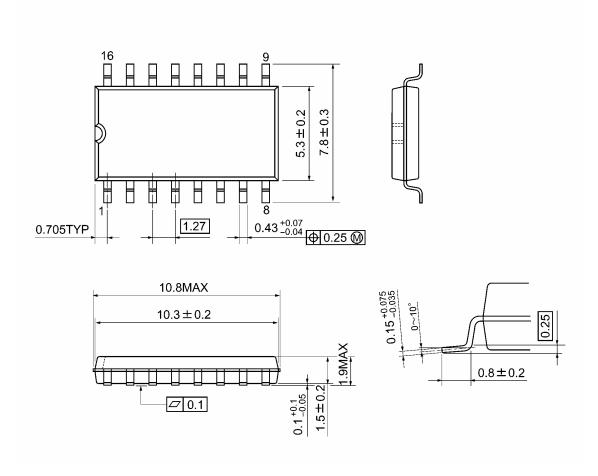
Package Dimensions



Weight: 1.00 g (typ.)

Package Dimensions

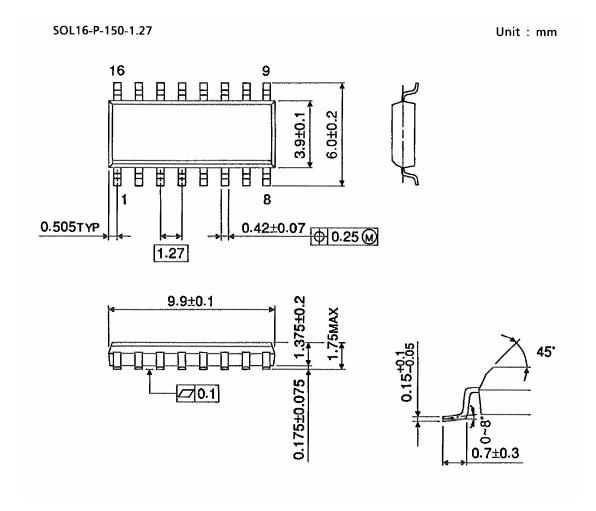
SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)



Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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2007-10-01