TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VCX138FT, TC74VCX138FK

Low Voltage 3-to-8 Line Decoder with 3.6 V Tolerant Inputs and Outputs

The TC74VCX138 is a high performance CMOS 3-to-8 decoder which is guaranteed to operate from 1.2-V to 3.6-V. Designed for use in 1.5 V, 1.8 V, 2.5 V or 3.3 V systems, it achieves high speed operation while maintaining the CMOS low power dissipation.

It is also designed with over voltage tolerant inputs and outputs up to $3.6~\mathrm{V}.$

When the device is enabled, 3 binary select inputs (A, B and C) determine which one of the outputs $(\overline{Y}0 \cdot \overline{Y}0)$ will go low.

When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go high.

 $G1, \overline{G}2A$ and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems

All inputs are equipped with protection circuits against static discharge.

Features

- Low voltage operation: VCC = 1.2~3.6 V
- High speed operation: $t_{pd} = 3.5 \text{ ns (max) (VCC} = 3.0 \sim 3.6 \text{ V)}$

 $t_{pd} = 4.1 \text{ ns (max) (VCC} = 2.3 \sim 2.7 \text{ V)}$

 $t_{pd} = 8.2 \text{ ns (max) (VCC} = 1.65 \sim 1.95 \text{ V})$

 $t_{pd} = 16.4 \text{ ns (max) (V}_{CC} = 1.4 \sim 1.6 \text{ V})$

 $t_{pd} = 41.0 \text{ ns (max) (V}_{CC} = 1.2 \text{ V})$

- 3.6 V tolerant inputs and outputs.
- Output current: $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$

 $I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$

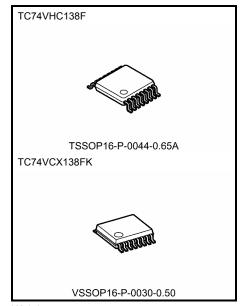
 $I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.65 \text{ V)}$

 $I_{OH}/I_{OL} = \pm 2 \text{ mA (min) (V}_{CC} = 1.4\text{V})$

- Latch-up performance: -300 mA
- ESD performance: Machine model $\geq \pm 200 \text{ V}$ Human body model $\geq \pm 2000 \text{ V}$

Package: TSSOP and VSSOP (US)

• Power down protection is provided on all inputs and outputs.



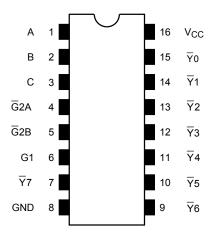
Weight

TSSOP16-P-0044-0.65A : 0.06 g (typ.) VSSOP16-P-0030-0.50 : 0.02 g (typ.)

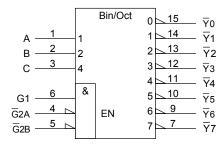
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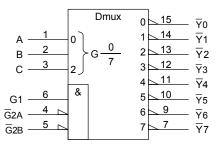


Pin Assignment (top view)



IEC Logic Symbol





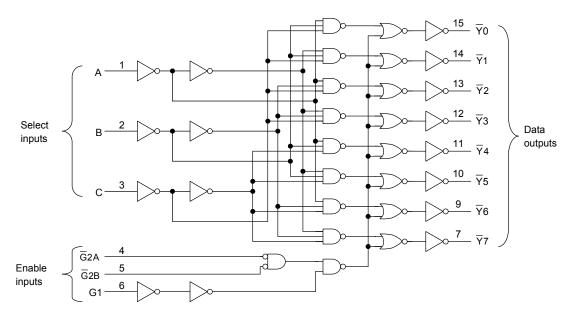
Truth Table

		Inp	uts				Outputs							
	Enable			Select		₹0	\overline{Y}_0 \overline{Y}_1	$\overline{Y}_1 \mid \overline{Y}_2 \mid$		<u>7</u> 4	<u>7</u> 5	₹6	7 7	Selected Output
G1	G2A	G ₂ B	C	В	Α	YU	ΥΊ	Y Z	Y 3	Y 4	Y 5	Yb	Υ /	
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Н	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	₹0
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	₹1
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Ÿ2
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Y 3
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Ÿ4
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	<u>7</u> 5
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	₹6
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	\ 77

X: Don't care



System Diagram



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5~4.6	V
DC input voltage	V _{IN}	-0.5~4.6	٧
DC output voltage	Vout	−0.5~4.6 (Note 2)	V
DC output voltage	VOU1	-0.5~V _{CC} + 0.5 (Note 3)	V
Input diode current	lıK	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	P _D	180	mW
DC V _{CC} /ground current	ICC/IGND	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.2~3.6	V	
Input voltage	V _{IN}	-0.3~3.6	V	
Output voltage	Vout	0~3.6 (Note 2)	V	
Output voltage	VOU1	0~V _{CC} (Note 3)]	
		±24 (Note 4)		
Output current	IOH/IOL	±18 (Note 5)	A	
Output current	IOH/IOL	±6 (Note 6)	- mA	
		±2 (Note 7)		
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	0~10 (Note 8)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: $V_{CC} = 0 V$

Note 3: High or low state

Note 4: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 5: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 6: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 7: V_{CC} = 1.4~1.6V

Note 8: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

Electrical Characteristics

DC Characteristics (Ta = $-40\sim85$ °C, 2.7 V < V_{CC} \leq 3.6 V)

Character	Characteristics		Tes	Test Condition			Max	Unit
Input voltage	High level	ViH		2.7~3.6	2.0	_	V	
input voitage	put voltage Low level			_	2.7~3.6	_	0.8	· l
				I _{OH} = -100 μA	2.7~3.6	V _C C - 0.2	_	
Output voltage	High level	Voh	$V_{IN} = V_{IH}$ or V_{IL}	I _{OH} = -12 mA	2.7	2.2	_	
				$I_{OH} = -18 \text{ mA}$	3.0	2.4	_	٧
				I _{OH} = -24 mA	3.0	2.2	_	
	Low level	V _{OL}	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 100 \ \mu A$	2.7~3.6	_	0.2	
				$I_{OL} = 12 \text{ mA}$	2.7	_	0.4	
	Low level			I _{OL} = 18 mA	3.0	_	0.4	1
				$I_{OL} = 24 \text{ mA}$	3.0	_	0.55	
Input leakage curre	ent	I _{IN}	V _{IN} = 0~3.6 V		2.7~3.6	_	±5.0	μΑ
Power off leakage	current	I _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μА
Quiescent supply current		loo	$V_{IN} = V_{CC}$ or GND		2.7~3.6	_	20.0	
		Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$	2.7~3.6	_	±20.0	μΑ	
Increase in I _{CC} per	r input	Δl _{CC}	$V_{IH} = V_{CC} - 0.6 V$		2.7~3.6	_	750	

4



DC Characteristics (Ta = -40~85°C, 2.3 V \leq V_{CC} \leq 2.7 V)

Characteris	stics	Symbol	Test C	Condition		Min	Max	Unit
Ondradiens	31103	Cymbol	1031 0	V _{CC} (V)	IVIIII	Wax	Offic	
Input voltage	High level	VIH		2.3~2.7	1.6	_	V	
input voltage	Low level	V _{IL}		_	2.3~2.7		0.7	V
				$I_{OH} = -100 \mu A$	2.3~2.7	V _{CC} - 0.2		
	High level	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	I _{OH} = -6 mA	2.3	2.0	_	V
				I _{OH} = -12 mA	2.3	1.8	_	
Output voltage				$I_{OH} = -18 \text{ mA}$	2.3	1.7	_	
		V _{OL}		$I_{OL} = 100 \ \mu A$	2.3~2.7		0.2	
	Low level		$V_{IN} = V_{IH}$ or V_{IL}	$I_{OL} = 12 \text{ mA}$	2.3		0.4	
				$I_{OL} = 18 \text{ mA}$	2.3		0.6	
Input leakage current		I _{IN}	V _{IN} = 0~3.6 V		2.3~2.7		±5.0	μΑ
Power off leakage current		l _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0		10.0	μΑ
Quiescent supply current		Icc	$V_{IN} = V_{CC}$ or GND	2.3~2.7		20.0	μА	
Quidocent supply ct	ai i Ci il	icc	$V_{CC} \leqq V_{IN} \leqq 3.6 \ V$		2.3~2.7	_	±20.0	μΛ

DC Characteristics (Ta = -40~85°C, 1.65 V \leq V $_{CC}$ < 2.3 V)

Characteri	Characteristics		Test (Condition		Min	Max	Unit	
					V _{CC} (V)				
Input voltage	High level	V _{IH}		_	1.65~2.3	0.65 × V _{CC}	_	V	
input voitage	Low level	V _{IL}		_	1.65~2.3	_	0.2 × V _{CC}	V	
	High level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -100 \mu A$	1.65~2.3	V _{CC} - 0.2	_		
Output voltage				$I_{OH} = -6 \text{ mA}$	1.65	1.25	_	V	
	Low level	V _{OL}	V _{IN} = V _{IH} or V _{II}	$I_{OL}=100\;\mu A$	1.65~2.3		0.2		
	LOW level	VOL	AIM – AIH OI AIL	$I_{OL} = 6 \text{ mA}$	1.65		0.3		
Input leakage curre	nt	I _{IN}	V _{IN} = 0~3.6 V		1.65	_	±5.0	μΑ	
Power off leakage of	current	I _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μА	
Quiescent supply current		loo	$V_{IN} = V_{CC}$ or GND	1.65~2.3	_	20.0	^		
Quiescent supply co	unciii	Icc	$V_{CC} \leqq V_{IN} \leqq 3.6 \ V$	1.65~2.3	_	±20.0	μΑ		



DC Characteristics (Ta = $-40 \sim 85$ °C, $1.4V \le V_{CC} < 1.65V$)

Characteris	Characteristics		Test Condition		V _{CC} (V)	Min	Max	Unit	
Input voltage	High level	V _{IH}	_		1.4~1.65	0.65 V _{CC}	_	V	
input voitage	Low level	V _{IL}	_		1.4~1.65	_	0.05 × V _{CC}	V	
	High level	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OH} = -100 \mu A$	1.4~1.65	V _{CC} - 0.2	_		
Output voltage				$I_{OH} = -2 \text{ mA}$	1.4	1.05	_	V	
	Low level	VOI	V _{IN} = V _{IH} or V _{II}	$I_{OL} = 100 \mu A$	1.4~1.65		0.05		
	Low level	VOL	VIN = VIH OI VIL	I _{OL} = 2 mA	1.4	_	0.3		
Input leakage currer	Input leakage current		V _{IN} = 0~3.6 V		1.4~1.65	_	±5.0	μΑ	
Power off leakage current		I _{OFF}	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μА	
Quiescent supply current		loo	V _{IN} = V _{CC} or GND	1.4~1.65		20.0	μА		
Quiescent supply co	iii eiit	Icc	$V_{CC} \leqq V_{IN} \leqq 3.6 \ V$		1.4~1.65	_	±20.0	μΑ	

DC Characteristics (Ta = -40~85°C, 1.2 V \leq V_{CC} < 1.4 V)

Characteristics		Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	High level	V _{IH}	_		1.2~1.4	0.8 × V _{CC}	_	V
input voitage	Low level	V _{IL}	-	_	1.2~1.4		0.05 × V _{CC}	•
Output voltage	High level	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OH} = -100 \mu A$	1.2	V _C C - 0.1	_	٧
	Low level	VoL	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 100 \ \mu A$	1.2	-	0.05	
Input leakage currer	nt	I _{IN}	V _{IN} = 0~3.6 V		1.2		±5.0	μΑ
Power off leakage current		loff	V _{IN} , V _{OUT} = 0~3.6 V		0	ı	10.0	μΑ
Quiescent supply current		loo	$V_{IN} = V_{CC}$ or GND		1.2		20.0	μА
Quiescent supply co	III GIIL	Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$	1.2	_	±20.0	μΑ	



AC Characteristics (Ta = -40~85°C, Input: $t_r = t_f = 2.0$ ns) (Note)

Characteristics	Symbol	Tes	t Condition		Min	Max	Unit
Characteriotics	Cymbol	1.00				Max	010
			$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$	1.2	3.0	41.0	
	4		OL - 13 β1 , INL - 2 KΩ2	1.4 ± 0.1	2.0	16.4	
Propagation delay time (A, B, C- \overline{Y})	t _{pLH} t _{pHL}	Figure 1, Figure 2		1.8 ± 0.15	1.5	8.2	ns
	ФПС		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	2.5 ± 0.2	0.8	4.1	
				3.3 ± 0.3	0.6	3.5	
			$C_{I} = 15 \text{ pF}, R_{I} = 2 \text{ k}\Omega$	1.2	3.0	41.0	
	t _{pLH}	Figure 1, Figure 2	CL = 15 pr, κL = 2 κΩ	1.4 ± 0.1	2.0	16.4	
Propagation delay time (G1- \overline{Y})			$C_L = 30 \text{ pF}, R_L = 500 \Omega$	1.8 ± 0.15	1.5	8.2	ns
				2.5 ± 0.2	8.0	4.1	
				3.3 ± 0.3	0.6	3.5	
			$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$	1.2	3.0	41.0	
	4		CL = 15 pr, κL = 2 κΩ	1.4 ± 0.1	2.0	16.4	
Propagation delay time ($\overline{G}2 - \overline{Y}$)	t _{pLH}	Figure 1, Figure 2		1.8 ± 0.15	1.5	8.2	ns
	t _{pHL}		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	2.5 ± 0.2	8.0	4.1	
				3.3 ± 0.3	0.6	3.5	

Note: For $C_L = 50$ pF, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, Input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition		Тур.	Unit
	Í		V _{CC} (V)	,,	
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 1.8	0.25	
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 2.5	0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 3.3	0.8	
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 1.8	-0.25	
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 2.5	-0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 3.3	-0.8	
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 1.8	1.5	
Quiet output minimum dynamic V _{OH}	V _{OHV}	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 2.5	1.9	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No.	e) 3.3	2.2	

Note: This parameter is guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol		Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		_		1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C _{PD}	$f_{IN} = 10 \text{ MHz}$		(Note)	1.8, 2.5, 3.3	40	pF

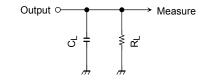
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$



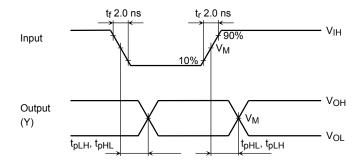
AC Test Circuit



	V _{CC}					
Symbol	$\begin{array}{c} 3.3 \pm 0.3 \text{ V} \\ 2.5 \pm 0.2 \text{ V} \\ 1.8 \pm 0.15 \text{ V} \end{array}$	1.5 ± 0.1 V 1.2V				
R_L	500 Ω	2 kΩ				
CL	30 pF	15 pF				

Figure 1

AC Waveform



Symbol	Vcc				
	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2~\textrm{V}$	1.8 ± 0.15 V	$1.5\pm0.1~\text{V}$	1.2 V
V_{IH}	2.7 V	V _{CC}	V _{CC}	V _{CC}	V _{CC}
V_{M}	1.5 V	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2

Figure 2 t_{pLH}, t_{pHL}

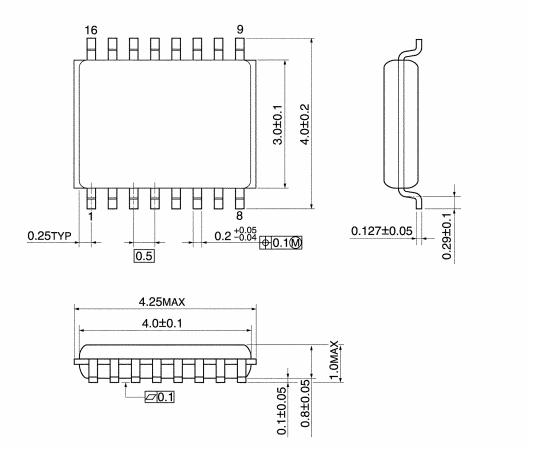
Package Dimensions

TSSOP16-P-0044-0.65A Unit: mm 6.4±0.2 0.65 $0.22^{+0.09}_{-0.06}$ 0.225TYP | |0.13M 5.4MAX 5.0±0.1 1.2MAX 1.0±0.05 0~10° 0.1±0.05 S Ø.1S (0.5)0.45~0.75

Weight: 0.06 g (typ.)

Package Dimensions

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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20070701-EN GENERAL

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