TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# T C 7 W T 2 4 0 F U

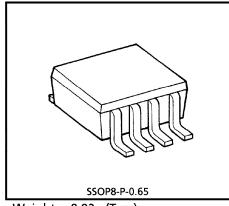
# **INVERTED, 3-STATE OUTPUT**

The TC7WT240FU is a high speed CMOS DUAL BUS BUFFERS fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The input threshold levels are compatible with TTL output voltage.

It is an inverting 3-state buffer having two active-low output enables.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

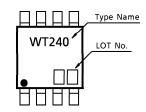


Weight: 0.02g (Typ.)

#### **FEATURES**

- High Speed  $\cdots t_{pd} = 13$ ns (Typ.) at  $V_{CC} = 5V$
- Low Power Dissipation ......  $I_{CC} = 2\mu A$  (Max.) at  $Ta = 25^{\circ}C$
- Compatible with TTL outputs  $\cdots$   $V_{IL} = 0.8V$  (Max.),  $V_{IH} = 2.0V$  (Min.)
- Output Drive Capability ...... 15 LSTTL Loads
- Symmetrical Output Impedance  $\cdots |I_{OH}| = I_{OL} = 6mA$  (Min.)

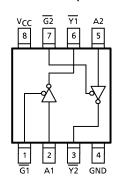
#### **MARKING**



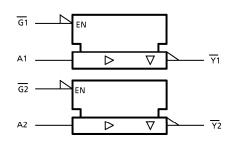
# MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~7	V
DC Input Voltage	VIN	-0.5~V <sub>CC</sub> +0.5	٧
DC Output Voltage	Vout	-0.5~V <sub>CC</sub> +0.5	V
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	lok	± 20	mA
DC Output Current	lout	± 35	mA
DC V <sub>CC</sub> /Ground Current	lcc	± 37.5	mA
Power Dissipation	PD	300	mW
Storage Temperature	T <sub>stg</sub>	<b>-65∼150</b>	°C
Lead Temperature (10 s)	TL	260	°C

PIN ASSIGNMENT (TOP VIEW)



## **LOGIC DIAGRAM**



## TRUTH TABLE

INP	UTS	OUTPUTS			
G	Α	Y			
L	L	Н			
L	Н	L			
Н	×	Z			

x : Don't Care Z : High Impedance

## **RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	4.5~5.5	V
Input Voltage	$v_{IN}$	0~V <sub>CC</sub>	V
Output Voltage	Vout	0~V <sub>CC</sub>	V
Operating Temperature	T <sub>opr</sub>	<b>- 40∼85</b>	°C
Input Rise and Fall Time	t <sub>r</sub> , t <sub>f</sub>	0~500	ns

#### DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC SYMBO		TEST CONDITION		Vcc	Ta = 25°C			$Ta = -40 \sim 85^{\circ}C$		UNIT
CHARACTERISTIC	STIVIBOL	TEST CONDITION		(V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
High-Level	VIH			4.5~	2.0			2.0		\ \
Input Voltage	VIH			5.5	2.0	-	_	2.0		·
Low-Level	\/			4.5~			0.8		0.8	<
Input Voltage	VIL			5.5	_	_	0.8	-	0.8	v
High-Level	\/	V – V	$I_{OH} = -20\mu A$	4.5	4.4	4.5	_	4.4	_	V
Output Voltage	Voн	$V_{IN} = V_{IL}$	$I_{OH} = -6mA$	4.5	4.18	4.31	_	4.13	_	
Low-Level	1/	V <sub>IN</sub> = V <sub>IH</sub>	$I_{OL} = 20 \mu A$	4.5	_	0.0	0.10	_	0.10	V
Output Voltage	VOL	or V <sub>IL</sub>	I <sub>OL</sub> = 6mA	4.5	_	0.17	0.26	_	0.33	V
3-State Output	lo-	V <sub>IN</sub> = = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = V <sub>CC</sub> or GND		5.5	i	_	± 0.5	_	± 5.0	μΑ
Off-State Current	loz									
Input Leakage	lini	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5			± 0.1		± 1.0	μΑ
Current	IN			3.5	_		<u> -</u> 5. I		_ 1.0	$\mu$ A
	ICC	$V_{IN} = V_{CC}$ or GND		5.5			2.0		20.0	$\mu$ A
Quiescent Supply		PER INPUT	: V <sub>IN</sub> = 0.5V							
Current	<sup>I</sup> CCT		or 2.4V	5.5	_	—	2.0	—	2.9	mΑ
	OTHER INPUT: V <sub>CC</sub> or GND									

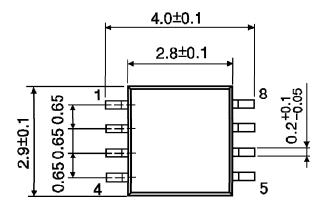
AC ELECTRICAL CHARACTERISTICS (Input  $t_r = t_f = 6 \text{ns}$ )

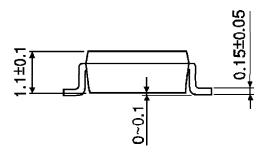
CHADACTERICTIC	SYMBOL	TEST CO	TEST CONDITION		Ta = 25°C			Ta = -40~85°C		UNIT
CHARACTERISTIC	STIVIBOL		CL	Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Output Transition	tTLH		F0	4.5	_	7	12	_	15	nc
Time	tTHL	_	50	5.5	_	6	11	_	14	ns
		_	50	4.5	_	15	25	_	31	ns
Propagation Delay	t <sub>pLH</sub>			5.5	_	13	22	_	28	
Time	tpHL		150	4.5	_	21	33	_	41	
	'			5.5	_	18	29	_	37	
	<sup>t</sup> pZL <sup>t</sup> pZH	$R_L = 1k\Omega$	50 150	4.5	_	17	30	_	38	ns
Output Enable Time				5.5	_	14	27	_	34	
				4.5	_	23	38	_	48	
				5.5	_	20	34	_	43	
Output Disable Time	t <sub>pLZ</sub>	$R_L = 1k\Omega$	50	4.5	_	16	30	_	38	nc
				5.5	_	13	27	_	34	ns
Input Capacitance	CIN	_	_	_	_	5	10	_	10	рF
Output Capacitance	COUT	_	_	_	_	10	_	_	_	рF
Power Dissipation Capacitance	C <sub>PD</sub>	(Note 1)			_	32	_	_	_	pF

(Note 1):  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:  $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 2$  (per Gate)

# PACKAGE DIMENSIONS SSOP8-P-0.65

Unit: mm





Weight: 0.02g (Typ.)

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000707EBA

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