TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WBD126AFK

Dual Bus Switch with Level Shift

The TC7WBD126AFK is a low on-resistance, high-speed CMOS 2-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

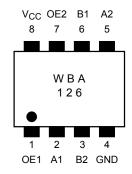
The device is enable to realize the shift of signal level from 5 V to 3.3 V.

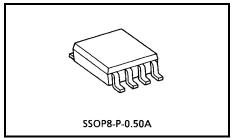
All inputs are equipped with protector circuits to protect the device from static discharge.

Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed operation: $t_{pd} = 0.32 \text{ ns} (max)$
- Ultra-low on resistance: $RON = 5 \Omega$ (typ.)
- ESD performance: Machine model $\geq \pm 200 \text{ V}$ Human body model $\geq \pm 2000 \text{ V}$
- TTL level input (control input)
- Low Power Dissipation: Icc = $10 \mu A (max.)$
- Package: US8

Pin Assignment (top view)





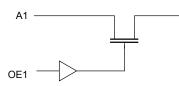
Weight: 0.01 g (typ.)

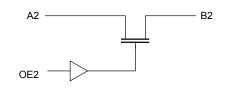
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Truth Table

| Inputs | Function | | |
|--------|-----------------|--|--|
| OE | Function | | |
| L | Disconnect | | |
| Н | A port = B port | | |

System Diagram





Absolute Maximum Ratings (Note)

| Characteristics | Symbol | Rating | Unit |
|---------------------------------|-----------------------------------|----------|------|
| Power supply range | V _{CC} | -0.5~7.0 | V |
| DC input voltage | V _{IN} | -0.5~7.0 | V |
| DC switch voltage | VS | -0.5~7.0 | V |
| Input diode current | I _{IK} | -50 | mA |
| Continuous channel current | IS | 128 | mA |
| Power dissipation | PD | 200 | mW |
| DC V _{CC} /GND current | I _{CC} /I _{GND} | ±100 | mA |
| Storage temperature | T _{stg} | -65~150 | °C |

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Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

| Characteristics | Symbol | Rating | Unit |
|--------------------------|------------------|---------|------|
| Supply voltage | V _{CC} | 4.5~5.5 | V |
| Input voltage | V _{IN} | 0~5.5 | V |
| Switch voltage | VS | 0~5.5 | V |
| Operating temperature | T _{opr} | -40~85 | °C |
| Input rise and fall time | dt/dv | 0~10 | ns/V |

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

| Charac | teristics | Symbol | Test Condition | | V _{CC} (V) | Min | Typ. (Note 1) | Max | Unit | | |
|-------------------------------|--------------|--|---------------------------------------|-------------------------|---------------------|------|------------------|------|------|---|---|
| Input voltage | "H" level | VIH | _ | - | 4.5~5.5 | 2.0 | _ | _ | v | | |
| Input voltage | "L" level | V _{IL} | _ | _ | 4.5~5.5 | _ | _ | 0.8 | v | | |
| High lovel outr | ut voltogo | | | | 4.75 | 2.3 | 2.8 | 3.2 | | | |
| High-level outp | (Note 2) | V _{OH} | $IOH=-1\mu A$ $V_{IS} = V_{CC}$ | | 5.0 | 2.5 | 3.0 | 3.4 | V | | |
| | (NOLE 2) | | | | 5.25 | 2.7 | 3.2 | 3.6 | | | |
| Input leakage of | current | I _{IN} | V _{IN} = 0~5.5 V | | 4.5~5.5 | _ | _ | ±1.0 | μA | | |
| Power off leaka | age current | I _{OFF} | A, B, OE = 0~5.5 V | | 0 | _ | _ | ±1.0 | μA | | |
| Off-STATE lea (switch off) | kage current | I _{SZ} | A, B = 0~5.5 V, OE = V | /cc | 4.5~5.5 | _ | _ | ±1.0 | μA | | |
| | | | | I _{IS} = 64 mA | 4.5 | _ | 5 | 9 | - | | |
| | | | | | 4.75 | _ | 5 | 8 | | | |
| ON resistance | | | | D | $V_{IS} = 0 V$ | 1 20 | 4.5 | _ | 5 | 9 | Ω |
| | (Note 3) | RON | R _{ON} | I _{IS} = 30 mA | 4.75 | _ | 5 | 8 | 12 | | |
| | | $V_{IS} = 2.3 \text{ V}, \text{ I}_{IS} = 15 \text{ mA}$ | | 4.5 | _ | 35 | 65 | | | | |
| | | | | | 4.75 | _ | 35 | 50 | | | |
| Quiescent sup | oly current | ICC | VIN = VCC or GND,I _{OUT} = 0 | | 5.5 | _ | _ | 10 | μA | | |
| Increase in I _{CC} | ; per input | ΔI_{CC} | V _{IN} = 3.4 V (one input) | | 5.5 | | _ | 2.5 | mA | | |

Note 1: Typical values are at $V_{CC} = 5 V$, Ta = 25°C.

- Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a VOH voltage level when using Pull-down resistance for an output terminal.
- Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics (Ta = -40~85°C)

| Characteristics | Symbol | Test Condition | V _{CC} (V) | Min | Max | Unit |
|------------------------|------------------|---------------------------|---------------------|-----|------|------|
| Propagation delay time | t _{pLH} | Figure 1, Figure 2 (Note) | 4.5 | | 0.32 | ns |
| (bus to bus) | t _{pHL} | | 4.5 | | 0.52 | 115 |
| Output enable time | t _{pZL} | Figure 1, Figure 3 | 4.5 | | 4.5 | ns |
| | t _{pZH} | | 4.5 | | ч.0 | 113 |
| Output disable time | t _{pLZ} | Figure 1, Figure 3 | 4.5 | | 5.5 | ns |
| | t _{pHZ} | | 4.5 | | 0.0 | 113 |

Note: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

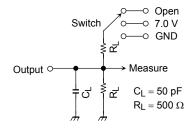
Capacitive Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | V _{CC} (V) | Тур. | Unit |
|-------------------------------|------------------|-----------------------------|---------------------|------|------|
| Control pin input capacitance | CIN | (Note) | 5.0 | 3 | pF |
| Switch terminal capacitance | C _{I/O} | OE = V _{CC} (Note) | 5.0 | 10 | pF |

Note: This parameter is guaranteed by design.

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AC Test Circuit



| Parameter | Switch |
|-------------------------------------|--------|
| t _{pLH} , t _{pHL} | Open |
| tpLZ, tpZL | 7.0 V |
| t _{pHZ} , t _{pZH} | GND |



AC Waveform

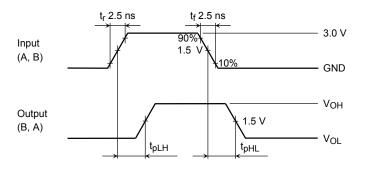


Figure 2 t_{pLH}, t_{pHL}

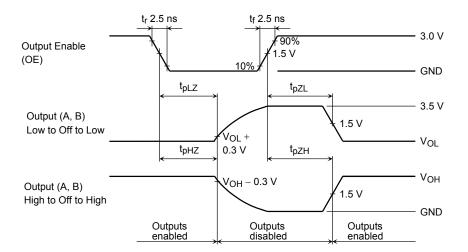


Figure 3 $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$

V_{OH} – V_{CC} Characteristics (typ.)

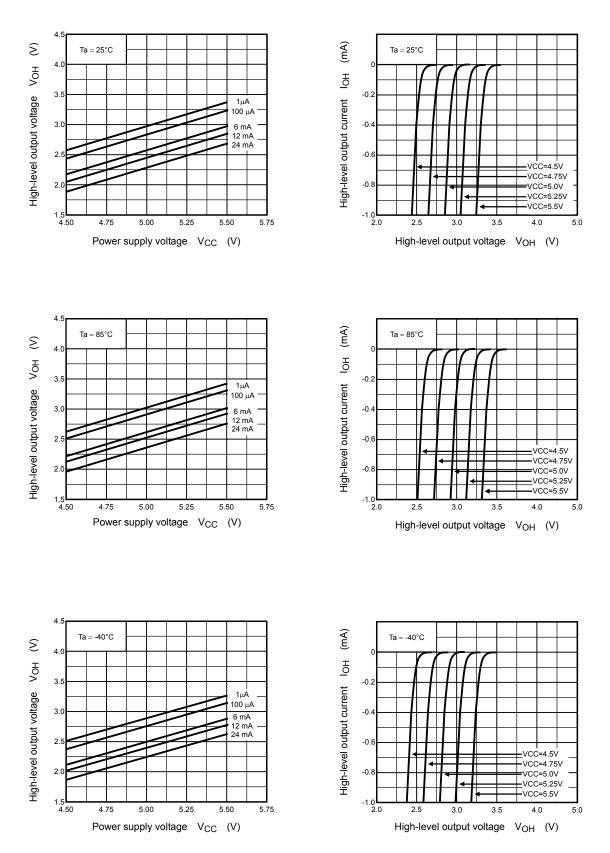
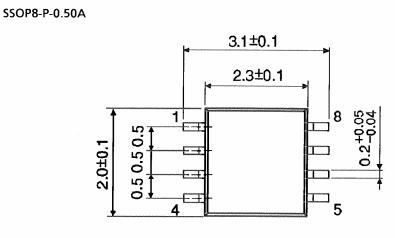
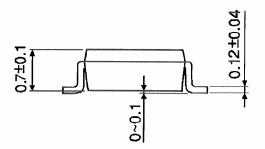


Figure 4

Package Dimensions





Weight: 0.01 g (typ.)

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Unit : mm

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20070701-EN GENERAL

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