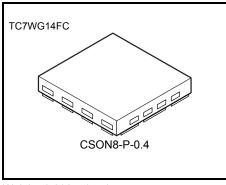
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG14FC

Triple Schmitt Inverter

Features

- High-level output current: I_{OH}/I_{OL} = ±8 mA (min) at V_{CC} = 3 V
- High-speed operation: t_{pd} = 4.0 ns (typ.)
 at V_{CC} = 3.3 V,15pF
- Operating voltage range: V_{CC} = 0.9~3.6 V
- 5.5-V tolerant inputs
- 3.6-V power down protection outputs



Weight: 0.002 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit		
Power supply viltage	V _{CC}	-0.5~4.6	V		
DC input voltage	V _{IN}	-0.5~7.0	V		
DC output voltage	\/a=	−0.5~4.6 (Note 1)	V		
	V _{OUT}	-0.5~V _{CC} + 0.5 (Note 2)	V		
Input diode current	I _{IK}	-20	mA		
Output diode current	lok	-20 (Note 3)	mA		
DC output current	lout	±25	mA		
DC V _{CC} /GND current	Icc	±50	mA		
Power dissipation	P_{D}	150 (Note 4)	mW		
Storage temperature	T _{stg}	-65~150	°C		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{CC} = 0V

Note 2: High or Low State.

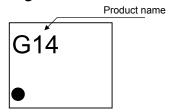
I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

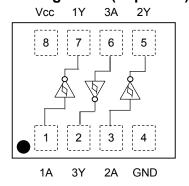
Note 4: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$

Marking



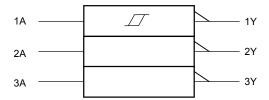
Pin Assignment (top view)



Truth Table

A Y L H H L

IEC Logic Symbol



Operating Ranges

Characteristics	Symbol	Value	Unit		
Power supply voltage	Vcc	0.9~3.6	V		
Input voltage	V _{IN}	0~5.5	V		
Output voltage	Va	0~3.6 (Note 5)	V		
	Vout	0~V _{CC} (Note 6)	V		
Output Current	loh/lot	±8.0 (Note 7)			
		±4.0 (Note 8)			
		±3.0 (Note 9)	mA		
		±1.7 (Note 10)	IIIA		
		±0.3 (Note 11)			
		±0.02 (Note 12)			
Operating temperature	T _{opr}	-40~85	°C		

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state.

Note 7: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 9: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 10: $V_{CC} = 1.4 \sim 1.6 \text{ V}$

Note 11: V_{CC} = 1.1~1.3 V

Note 12: $V_{CC} = 0.9 V$

Electrical Characteristics

DC Electrical Characteristics

Characteristics S		Cumbal	Total Constition		Ta = 25°C			;	Ta = -40~85°C		Lleit
		Symbol	rest	Test Condition		Min	Тур.	Max	Min	Max	Unit
High level				0.9	_	_	0.73	_	0.80	- - - -	
					_	_	0.86	_	0.93		
	.,				_	_	1.07	_	1.12		
	VP	_		1.65	_	_	1.23	_	1.25		
				2.3	_	_	1.66	_	1.68		
				3.0	_	_	2.14	_	2.15		
Threshold voltage					0.9	0.18	_	_	0.07	_	V
					1.1	0.26	_	_	0.18	_	
		.,			1.4	0.36	_	_	0.31	_	
	Low level	V _N		_		0.45	_	_	0.41	_	
				2.3	0.69	_	_	0.64		1	
						0.96	_	_	0.91	_	
					0.9	0.20	_	0.38	0.15	0.53	V
						0.25	_	0.41	0.21	0.53	
Liveteresia veltere		.,,				0.35	_	0.48	0.34	0.57	
Hysteresis voltage		V _H	_		1.65	0.42	_	0.56	0.40	0.60	
					2.3	0.60	_	0.74	0.61	0.76	
					3.0	0.79	_	0.93	0.80	0.94	
		Vон	$V_{IN} = V_{IL}$	I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	-	
				$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
	High level			$I_{OH} = -1.7 \text{ mA}$	1.4~1.6	V _{CC} × 0.75	ı		V _{CC} × 0.75	ı	
				$I_{OH} = -3.0 \text{ mA}$	1.65~ 1.95	V _{CC} -0.45		_	V _{CC} -0.45		
				$I_{OH} = -4.0 \text{ mA}$	2.3~2.7	2.0	_	_	2.0	_	
Output voltage				$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48	_	_	2.48	_	
Cutput voltage				$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
Low level	V _{OL}	$V_{IN} = V_{IH}$	$I_{OL} = 0.3 \text{ mA}$	1.1~1.3			V _{CC} × 0.25	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		
			I _{OL} = 1.7 mA	1.4~1.6			V _{CC} × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		
				$I_{OL} = 3.0 \text{ mA}$	1.65~ 1.95			0.45		0.45	
			F	$I_{OL} = 4.0 \text{ mA}$	2.3~2.7	_	_	0.4	_	0.4	
				$I_{OL} = 8.0 \text{ mA}$	3.0~3.6	_	_	0.4	_	0.4	
Input leakage curre	nt	I _{IN} V _{IN} = 0~5.5V		0~3.6	_	_	±0.1	_	±1.0	μΑ	
Power off leakage of	Power off leakage current I _{OFF}		V _{IN} = 0~5.5V V _{OUT} = 0~3.6V		0			1.0		10.0	μА
Quiescent supply c	urrent	Icc	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND		_	_	1.0	_	10.0	μА



AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C)	Ta = -40~85°C		Unit
			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	[‡] рLН [‡] рНL	C_L = 10 pF, R_L = 1 $M\Omega$	0.9	_	41.3	_	_	_	
			1.1~1.3	_	18.0	25.4	1.0	40.8	
			1.4~1.6	_	9.5	12.2	1.0	13.5	- ns
			1.65~ 1.95		7.0	8.7	1.0	9.3	
			2.3~2.7	_	4.7	5.7	1.0	6.2	
			3.0~3.6	_	3.7	4.5	1.0	4.7	
		C_L = 15 pF, R_L = 1 $M\Omega$	0.9	_	44.4	_	_	_	
			1.1~1.3	_	19.3	27.7	1.0	46.9	
			1.4~1.6	_	10.2	13.1	1.0	14.7	
			1.65~ 1.95	_	7.5	9.3	1.0	9.9	
			2.3~2.7	_	5.0	5.9	1.0	6.4	
			3.0~3.6		4.0	4.8	1.0	5.2	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	55.8	_	_	_	
			1.1~1.3	_	24.7	36.3	1.0	59.6	
			1.4~1.6	_	12.9	16.8	1.0	19.2	
			1.65~ 1.95	_	9.2	11.5	1.0	12.9	
			2.3~2.7	_	5.9	7.1	1.0	8.3	
			3.0~3.6	_	4.9	5.7	1.0	6.6	
Input capacitance	C _{IN}	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 ~ 3.6	_	11	_	_	_	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

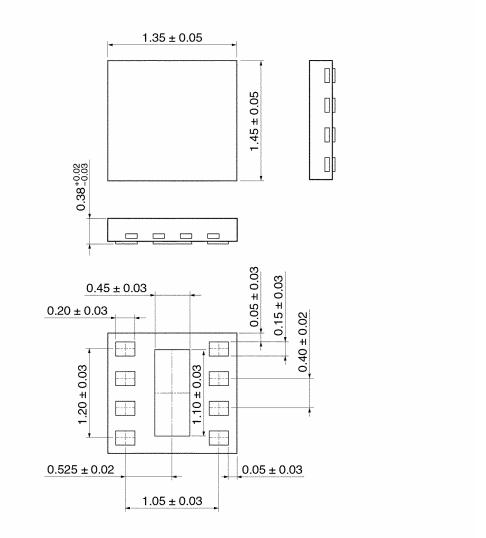
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Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

Package Dimensions





Weight: 0.002 g (typ.)

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20070701-EN GENERAL

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