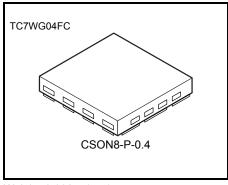
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WG04FC

#### Triple Inverter

#### **Features**

- High-level output current: I<sub>OH</sub>/I<sub>OL</sub> = ±8 mA (min) at V<sub>CC</sub> = 3 V
- High-speed operation:  $t_{pd}$  = 2.7 ns (typ.) at  $V_{CC}$  = 3.3 V,15pF
- Operating voltage range: V<sub>CC</sub> = 0.9~3.6 V
- 5.5-V tolerant inputs
- 3.6-V power down protection outputs



Weight: 0.002 g (typ.)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V
DC input voltage	V <sub>IN</sub>	-0.5~7.0	٧
DC output voltage	\/-·	-0.5~4.6 (Note 1)	٧
	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5 (Note 2)	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /GND current	Icc	±50	mA
Power dissipation	PD	150 (Note 4)	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0V$ 

Note 2: High or Low State.

I<sub>OUT</sub> absolute maximum rating must be observed.

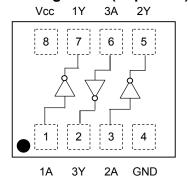
Note 3: V<sub>OUT</sub> < GND

Note 4: Mounted on an FR4 board.  $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t, Cu Pad: } 11.56 \text{ mm}^2)$ 

#### Marking

G04

#### Pin Assignment (top view)



### **Truth Table**



## **IEC Logic Symbol**



## **Operating Ranges**

Characteristics	Symbol	Value	Unit	
Power supply voltage	V <sub>CC</sub>	0.9~3.6	٧	
Input voltage	V <sub>IN</sub>	0~5.5	V	
Output voltage	V <sub>OUT</sub>	0~3.6 (Note 5)	V	
	VO01	0~V <sub>CC</sub> (Note 6)	V	
Output Current		±8.0 (Note 7)		
		±4.0 (Note 8)	mA	
	1/1	±3.0 (Note 9)		
	I <sub>OH</sub> /I <sub>OL</sub>	±1.7 (Note 10)		
		±0.3 (Note 11)		
		±0.02 (Note 12)		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Input rise and fall time	dt/dV	0~10 (Note 13)	ns/V	

Note 5:  $V_{CC} = 0V$ 

Note 6: High or Low state.

Note 7:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 9:  $V_{CC} = 1.65 \sim 1.95 \text{ V}$ 

Note 10: V<sub>CC</sub> = 1.4~1.6 V

Note 11: V<sub>CC</sub> = 1.1~1.3 V

Note 12:  $V_{CC} = 0.9 \text{ V}$ 

Note 13:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 

## **Electrical Characteristics**

## **DC Electrical Characteristics**

Characteristics Symbol			ast Condition		٦	Γa = 25°0	2	Ta = -40~85°C		Unit
Ondracteristics Symbol		1030	Test Condition		Min	Тур.	Max	Min	Max	Onic
High-level VIH input voltage				$V_{CC}$			V <sub>CC</sub>	_	V	
	_		1.1~1.3	$\begin{array}{c} V_{CC} \\ \times \ 0.7 \end{array}$	_	_	V <sub>CC</sub> × 0.7	_		
			1.4~1.6	V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65			
			1.65~1.95	V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65			
			2.3~2.7	1.7		_	1.7	_		
			3.0~3.6	2.0	_	_	2.0	_		
Low-level V <sub>IL</sub> input voltage			0.9	_	_	GND	_	GND	V	
		_		_	_	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3		
						V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35		
					_	V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35		
				_	_	0.7		0.7		
			3.0~3.6	_	_	8.0		8.0		
			I <sub>OH</sub> =-0.02 mA	0.9	0.75	_	_	0.75	_	
High-level VOH output voltage	$V_{IN} = V_{IL}$	$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75		V	
		I <sub>OH</sub> = -1.7 mA	1.4~1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_		
		$I_{OH} = -3.0 \text{ mA}$	1.65~ 1.95	V <sub>CC</sub> -0.45	_	_	V <sub>CC</sub> -0.45			
		I <sub>OH</sub> = -4.0 mA	2.3~2.7	2.0	_	_	2.0	_		
		I <sub>OH</sub> = -8.0 mA	3.0~3.6	2.48	_	_	2.48	_		
Low-level V <sub>OL</sub> output voltage	V <sub>IN</sub> = V <sub>IH</sub>	$I_{OL} = 0.02 \text{ mA}$	0.9		_	0.1	_	0.1		
		$I_{OL} = 0.3 \text{ mA}$	1.1~1.3			V <sub>CC</sub> × 0.25		V <sub>CC</sub> × 0.25	٧	
		I <sub>OL</sub> = 1.7 mA	1.4~1.6		_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25		
		I <sub>OL</sub> = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45		
		I <sub>OL</sub> = 4.0 mA	2.3~2.7	_		0.4	_	0.4		
		I <sub>OL</sub> = 8.0 mA	3.0~3.6		_	0.4	_	0.4		
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5V		0~3.6	_	_	±0.1	_	±1.0	μА
Power off leakage current	l <sub>OFF</sub>	V <sub>IN</sub> = 0~5.5V V <sub>OUT</sub> = 0~3.6V		0		ĺ	1.0	_	10.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		3.6	_	_	1.0	_	10.0	μΑ



# AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
		$C_L$ = 10 pF, $R_L$ = 1 M $\Omega$	0.9	_	24.4	_	_	_	
			1.1~1.3	_	11.6	21.7	1.0	40.5	
			1.4~1.6	_	6.5	9.8	1.0	11.6	
			1.65~ 1.95	_	4.9	7.0	1.0	7.6	
			2.3~2.7	_	3.2	4.4	1.0	4.9	
			3.0~3.6	_	2.4	3.5	1.0	4.1	
	t <sub>pLH</sub> t <sub>pHL</sub>	$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	0.9	_	26.9	_	_	_	
			1.1~1.3	_	12.7	24.2	1.0	42.1	
Propagation delay time			1.4~1.6	_	7.1	10.7	1.0	12.9	ns
			1.65~ 1.95	_	5.3	7.5	1.0	7.7	
			2.3~2.7	_	3.5	4.8	1.0	5.5	
			3.0~3.6	_	2.7	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	37.0	_	_	_	
			1.1~1.3	_	17.1	33.9	1.0	64.1	
			1.4~1.6	_	9.3	14.3	1.0	17.4	
			1.65~ 1.95	_	6.9	9.8	1.0	10.2	
			2.3~2.7	_	4.6	6.2	1.0	6.6	-
			3.0~3.6	_	3.7	4.8	1.0	5.2	
Input capacitance	C <sub>IN</sub>	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 14)	0.9 ~ 3.6		10	_	_	_	pF

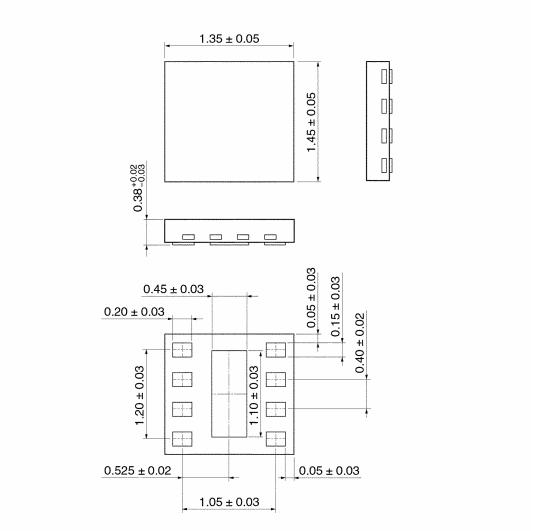
Note 14: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ 

## **Package Dimensions**





Weight: 0.002 g (typ.)

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20070701-EN GENERAL

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