TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG08FU,TC7WG08FK

Dual 2-Input AND Gate

Features

 High-level output current: I_{OH}/I_{OL} = ±8 mA (min) at V_{CC} = 3 V

• High-speed operation: t_{pd} = 2.5 ns (typ.)

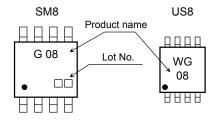
at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

• Operating voltage range: V_{CC} = 0.9~3.6 V

• 5.5-V tolerant inputs

• 3.6-V power down protection outputs

Marking



SSOP8-P-0.65 TC7WG08FK SSOP8-P-0.50A

Weight

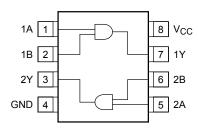
TC7WG08FU

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	-0.5~4.6	V	
DC input voltage	VIN	-0.5~7.0	V	
DC output voltage	V _{OUT}	-0.5~4.6 (Note 1)	V	
DC dutput voltage	VOU1	-0.5~V _{CC} + 0.5 (Note 2)	٧	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	-20 (Note 3)	mA	
DC output current	lout	±25	mA	
DC V _{CC} / ground current	Icc	±50	mA	
Power dissipation	PD	300 (SM8) 200 (US8)	mW	
Storage temperature	T _{stg}	-65~150	°C	

Pin Assignment (top view)



Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

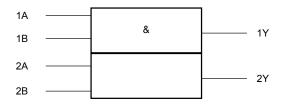
Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

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IEC Logic Symbol

TOSHIBA



Truth Table

Inp	Outputs			
А	В	Y		
L	L	L		
L	Н	L		
Н	L	L		
Н	Н	Н		

Operating Ranges

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	0.9~3.6	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V	0~3.6 (Note 4)	- V	
	Vout	0~V _{CC} (Note 5)		
Output Current		±8.0 (Note 6)	- mA	
	IOH/IOL	±4.0 (Note 7)		
		±3.0 (Note 8)		
		±1.7 (Note 9)		
		±0.3 (Note 10)	İ	
		±0.02 (Note 11)		
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V	

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 9: $V_{CC} = 1.4 \sim 1.6 \text{ V}$

Note 10: V_{CC} = 1.1~1.3 V

Note 11: $V_{CC} = 0.9 \text{ V}$

Note 12: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$



Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition		Condition		Ta = 25°C			Ta = -40~85°C		Unit		
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic			
				0.9	V _{CC}	_	_	V _{CC}	_	-	
High leve		Vih	_		1.1~1.3	V _{CC} × 0.7	1	_	V _{CC} × 0.7		_
	High level				1.4~1.6	V _{CC} × 0.65		_	V _{CC} × 0.65		_
					1.65~ 1.95	V _{CC} × 0.65		_	V _{CC} × 0.65		_
					2.3~2.7	1.7	_	_	1.7	_	
Input voltage					3.0~3.6	2.0	_	_	2.0	_	V
input voltage					0.9	_		GND	_	GND	V
					1.1~1.3	_		V _{CC} × 0.3		$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	
	Low level	V _{IL}		_	1.4~1.6	_	_	V _{CC} × 0.35	_	V _{CC} × 0.35	
					1.65~ 1.95	_	_	V _{CC} × 0.35	_	V _{CC} × 0.35	
					2.3~2.7	_		0.7	_	0.7	
					3.0~3.6	_	_	0.8	_	0.8	
		Vон		I _{OH} =-0.02 mA	0.9	0.75		_	0.75	_	V
	High level		$V_{IN} = V_{IH}$	$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
				I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
				$I_{OH} = -3.0 \text{ mA}$	1.65~ 1.95	V _{CC} -0.45		_	V _{CC} -0.45	_	
				I _{OH} = -4.0 mA	2.3~2.7	2.0	_	_	2.0	_	
0.4				$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48	_	_	2.48	_	
Output voltage				$I_{OL} = 0.02 \text{ mA}$	0.9	_		0.1	_	0.1	
				I _{OL} = 0.3 mA	1.1~1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
	Low level	V _{OL}	VIN = VIH	I _{OL} = 1.7 mA	1.4~1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
			or VIL	I _{OL} = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	1
				I _{OL} = 4.0 mA	2.3~2.7	_	_	0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage cur	rent	I _{IN} V _{IN} = 0~5.5 V		0~3.6		_	±0.1	_	±1.0	μΑ	
Power off leakage current I_{OFF} $V_{IN} = 0 \sim 5.5 \text{ V}$ $V_{OUT} = 0 \sim 3.6 \text{ V}$		5 V ·3.6 V	0	_	_	1.0	_	10.0	μΑ		
Quiescent supply current I_{CC} $V_{IN} = V_{CC}$ or GND		or GND	3.6	_		1.0	_	10.0	μА		

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AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		C _L = 10 pF,	0.9	_	26.9	_	_	_	ns
			1.1~1.3	_	10.9	20.7	1.0	38.6	
			1.4~1.6	_	5.9	9.6	1.0	11.3	
		$R_L = 1 M\Omega$	1.65~ 1.95	_	4.5	7.0	1.0	7.5	
			2.3~2.7	_	2.9	4.4	1.0	4.9	
			3.0~3.6	_	2.2	3.5	1.0	4.1	
		C_L = 15 pF, R_L = 1 M Ω	0.9	_	30.0	_	_	_	
	^t pLH t _{pHL}		1.1~1.3	_	12.0	24.2	1.0	42.0	
Propagation delay time			1.4~1.6	_	6.5	10.5	1.0	12.6	
Topagation delay time			1.65~ 1.95	_	5.0	7.7	1.0	8.0	
			2.3~2.7		3.2	4.9	1.0	5.6	
			3.0~3.6		2.5	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	45.0	_	_	_	
			1.1~1.3	_	18.0	33.4	1.0	63.2	
			1.4~1.6	_	8.9	14.8	1.0	17.9	
			1.65~ 1.95	_	6.9	10.3	1.0	10.8	
			2.3~2.7	_	4.4	6.4	1.0	6.8	
			3.0~3.6		3.5	4.9	1.0	5.4	
Input capacitance	C _{IN}		3.6		3		_	_	pF
Power dissipation capacitance	C _{PD}	(Note13)	0.9 ~ 3.6	_	10	_	_	_	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

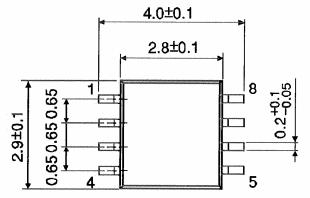
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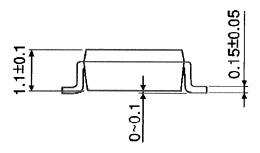
Average operating current can be obtained by the equation:

 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Package Dimensions

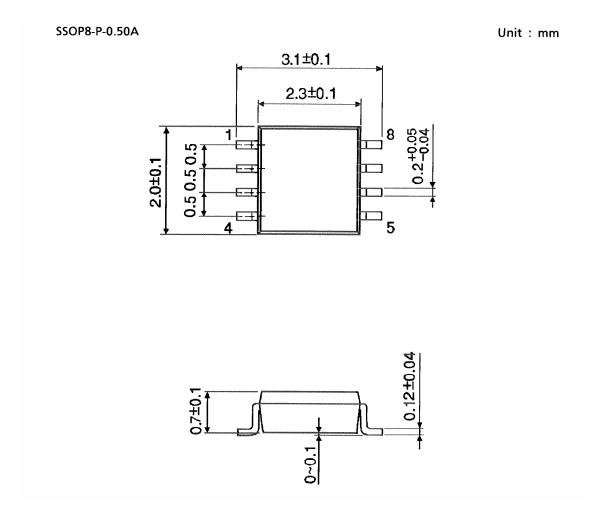






Weight: 0.02 g (typ.)

Package Dimensions



Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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