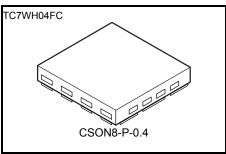
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WH04FC

#### **Triple Inverter**

#### Features

- High-speed
- Low power dissipation
- High noise immunity
- Operation voltage range
- 5.5-V Tolerant inputs.
- : $t_{pd}$  = 3.8ns (Typ.) at V<sub>CC</sub> = 5 V
- :I<sub>CC</sub>=2µA(Max.) at Ta=25°C
- :V<sub>NIH</sub>=V<sub>NIL</sub>=28%V<sub>CC</sub>(Min.)
- :V<sub>CC</sub>(opr.)=2~5.5V



Weight: 0.002g (typ.)

#### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Ratingh	Unit
Power supply viltage	V <sub>CC</sub>	-0.5~7.0	V
DC input voltage	VIN	-0.5~7.0	V
DC output voltage	Vout	$-0.5 \sim V_{CC} + 0.5$ (Note1)	V
Input diode current	Iк	-20	mA
Output diode current	IOK	±20 (Note2)	mA
DC output current	I <sub>OUT</sub>	±25	mA
DC V <sub>CC</sub> /GND current	ICC	±50	mA
Power dissipation	PD	150 (Note3)	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C

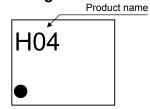
Note1 : High or Low State.

IOUT abusolute maximum rating must be observed.

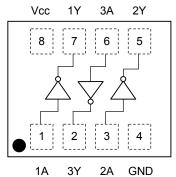
- Note2 :  $V_{OUT} < GND$  ,  $V_{OUT} > V_{CC}$
- Note3 : Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$ 

# Marking



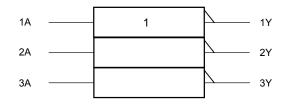
## Pin Assignment (top view)



## Truth Table

Α	Y
L	Н
Н	L

#### **IEC Logic Diagram**



# **Recommended Operationg Conditions**

Characteristics	Symbol	Rathing	Unit
Power supply voltage	V <sub>CC</sub>	2.0~5.5	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40~85	°C
Input rise and fall time	dt/dv	0~100 (V_{CC} = 3.3 V $\pm$ 0.3 V)	ns/V
	abav	0~20 (V <sub>CC</sub> = 5 V $\pm$ 0.5 V)	115/ V

# **DC** Electrical Characteristics

Characteristic Sym		Test condition			Ta = 25°C			Ta = -40~85°C		unit
				V <sub>CC</sub> (V)	Min.	Тур.	Max.	Min.	Max.	unit
				2.0	1.5	_	_	1.5	—	
High-level input voltage	VIH		_	3.0~5.5	V <sub>CC</sub> × 0.7			V <sub>CC</sub> × 0.7		V
				2.0			0.5		0.5	V
Low-level input voltage V <sub>IL</sub>		_	3.0~5.5			V <sub>CC</sub> × 0.3		V <sub>CC</sub> × 0.3		
High-level output voltage		V <sub>IN =</sub> V <sub>IL</sub>	I <sub>OH</sub> = –50 μA	2.0	1.9	2.0		1.9		- V
				3.0	2.9	3.0	_	2.9	_	
	V <sub>OH</sub>			4.5	4.4	4.5	_	4.4	_	
			I <sub>OH</sub> = –4 mA	3.0	2.58	_	_	2.48	_	
			I <sub>OH</sub> = –8 mA	4.5	3.94			3.80		
Low-level output voltage		VIN = VIH	I <sub>OL</sub> = 50 μΑ	2.0	_	0.0	0.1		0.1	
				3.0		0.0	0.1		0.1	
	VoL			4.5		0.0	0.1		0.1	
			I <sub>OL</sub> = 4 mA	3.0			0.36		0.44	
			I <sub>OL</sub> = 8 mA	4.5	_	_	0.36		0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN =</sub> 5.5 V	V <sub>IN =</sub> 5.5 V or GND				±0.1		±1.0	μΑ
Quiescent supply current	ICC	VIN = VCC	VIN = V <sub>CC</sub> or GND		_	_	2.0		20.0	μA

# AC Electrical Characteristics (Input : $t_r = t_f = 3 \text{ ns}$ )

Characteristic	Symbol		Test condition		Ta = 25°C			Ta = -40~85°C		Unit
			V <sub>CC</sub> (V)	C <sub>L (</sub> pF)	Min.	Тур.	Max.	Min.	Max.	Unit
Propagation delay time	<sup>t</sup> pLH t <sub>pHL</sub>	$3.3\pm0.3$	15	_	5.0	7.1	1.0	8.5		
			0.0 ± 0.0	50	_	7.5	10.6	1.0	12.0	ns
			$5.0 \pm 0.5$	15	_	3.8	5.5	1.0	6.5	
		5.0 ± 0.5	5.0 ± 0.5	50		5.3	7.5	1.0	8.5	
Input capacitance	C <sub>IN</sub>				_	4	10	_	10	pF
Power dissipation capacitanse	C <sub>PD</sub>		(Note 4)		_	18	_	_	_	pF

Note 4 : C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

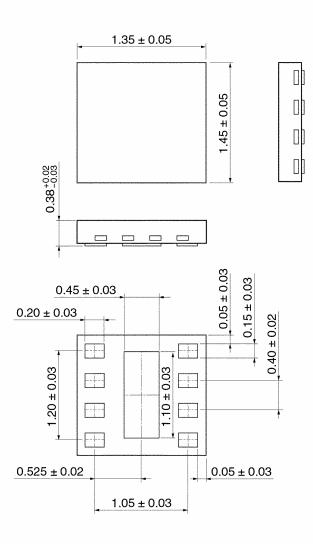
Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$ 

# TOSHIBA

# Package Dimensions

CSON8-P-0.4



Weight: 0.002 g (Typ.)

Unit: mm

4

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