PRODUCT BRIEF





QT1010 9.9Gb/s- 11.1Gb/s XFP COMPATIBLE BIDIRECTIONAL CDR

Features

- Integrated clock and data recovery for receive and transmit data path
- XFP MSA (telecom and datacom applications),
- IEEE802.3ae and 10GFC compliant
- Footprint and features optimized for XFP
- 9.95-11.1Gb/s modes of operation
 0.74 W typical power dissipation
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 Receive side Loss of Signal (LOS) indicator
- with adjustable threshold
- Integrated limiting amplifier (LA) on both receive and transmit side
- System and line side loopback
- XFI input equalization
- I2C or SPI configuration and monitoring
- Selectable input data polarity
- Adjustable output voltage swing
- Power down mode (independent receive and transmit paths)
- 10GHz recovered clock output option
- Transmit and Receive CDR output monitors
- Low noise 10GHz VCOs
- · Transmit and receive lock detection
- 7x7mm RoHS Compliant Package (0.8mm ball pitch)
- +3.3V and +1.8V power supplies
- ESD tolerant to 2kV on the control pins (high speed I/Os 400V)

Applications

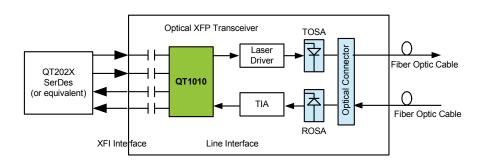
 Typical applications include XFP transceivers for 10Gb/s LAN/MAN/WAN/SAN Ethernet, Fibre Channel and SONET data processing, switches, routers, electrical cross-connects and optical monitoring equipment.

Description

The QT1010 is a fully integrated bidirectional clock and data recovery device for optical LAN/WAN/MAN/SAN Ethernet, Fibre Channel and SONET applications deploying XFP modules.

The QT1010 provides the interface between a 10Gb/s1 SerDes and the optical subassemblies in an XFP module. In the transmit direction, the QT1010 compensates for loss and distortion caused by the electrical XFI interface from the host SerDes. In the more sensitive receive direction, the QT1010 performs clock and data recovery and regeneration of the signal from the receive optical subassembly.

The retimed signal is provided to the host SerDes.



System Block Diagram with the QT1010

QT1010



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For technical support inquiries, submit your product related questions to support@appliedmicro.com.

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