



### **General Description**

The MAX2205–MAX2208 wideband (800MHz to 2GHz) power detectors are ideal for GSM/EDGE (MAX2206), TDMA (MAX2207), and CDMA (MAX2205/MAX2208) applications. The MAX2206/MAX2207/MAX2208 take an RF signal from a directional coupler at the input, and output a highly repeatable voltage. The output voltage increases monotonically with increasing input power. The output is compensated for temperature and process shifts, reducing the worst-case variation to less than  $\pm$ 1dB at full power and  $\pm$ 2.5dB at the lowest power.

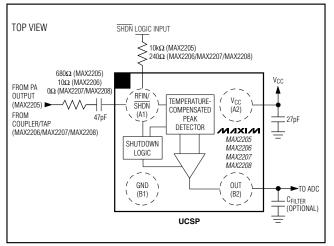
The MAX2206 features 40dB dynamic range, making it ideally suited to GSM/EDGE applications. The MAX2207 offers reduced current consumption for TDMA applications. The MAX2205/MAX2208 each have an integrated filter to allow for average power detection of CDMA signals over a 25dB dynamic range. The MAX2206/MAX2207/MAX2208 offer internal 50 $\Omega$  termination for interfacing with a directional coupler. The MAX2205 has a high-impedance input to provide a low-loss resistive tap in CDMA applications. All devices allow the user to control the averaging time constant externally.

The MAX2205–MAX2208 come in a space-saving 2  $\times$  2, 0.5mm-pitch UCSP<sup>TM</sup> and require only three external components.

#### **Applications**

Dual-Band GSM/EDGE Handsets Dual-Band CDMA/TDMA Handsets WCDMA Handsets PA Modules

# Pin Configuration/Functional Diagram/Typical Operating Circuit



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MAX2205-MAX2208

### Features

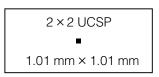
- Space-Saving 2 × 2 UCSP Occupies Only 1mm<sup>2</sup>
- Internal Temperature Compensation Gives ±0.3dB Detection Accuracy
- No External Filter or Op Amp Required
- Power-Detection Range 40dB (MAX2206) 25dB (MAX2205/MAX2207/2208)

### \_Ordering Information

PART	TEMP RANGE	BUMP-PACKAGE
MAX2205EBS	-40°C to +85°C	2 × 2 UCSP*
MAX2205EBS+	-40°C to +85°C	2 × 2 UCSP*
MAX2206EBS	-40°C to +85°C	2 × 2 UCSP*
MAX2206EBS+	-40°C to +85°C	2 × 2 UCSP*
MAX2207EBS	-40°C to +85°C	2 × 2 UCSP*
MAX2208EBS	-40°C to +85°C	2 × 2 UCSP*
MAX2208EBS+	-40°C to +85°C	2×2 UCSP*

\*Requires solder temperature profile described in the Absolute Maximum Ratings section.

+ Indicates lead-free package.



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For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

#### **ABSOLUTE MAXIMUM RATINGS**

V <sub>CC</sub> to GND	0.3V to +6.5V
RFIN/SHDN to GND	0.3V to (V <sub>CC</sub> + 0.3V)
RF Input Power (800MHz)	
(MAX2206/MAX2207/MAX2208)	+20dBm
RF Input Power (2GHz)	
(MAX2206/MAX2207/MAX2208)	
RF Input Voltage (800MHz) (MAX2205)	
RF Input Voltage (2GHz) (MAX2205)	0.8V <sub>P</sub>

Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )	
2 × 2 UCSP (derate 3.8mW/°C above +70°C	)
Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	-65°C to +160°C
Bump Temperature (soldering) (Note 1)	
Infrared (15s) (leaded)	+220°C
Vapor Phase (60s) (leaded)	+215°C
Infrared (15s) (lead-free)	+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### DC ELECTRICAL CHARACTERISTICS (MAX2205-MAX2208)

 $(V_{CC} = +2.7V \text{ to } +5.0V, \overline{SHDN} = +2.0V, \text{ no RF signal applied}, T_A = -40^{\circ}C \text{ to } +85^{\circ}C.$  Typical values are at  $V_{CC} = +2.85V$  and  $T_A = +25^{\circ}C$ , unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS	
Supply Voltage	Vcc		2.7		5.0	V	
Idle Supply Current	lini r	MAX2206		3.5	5.5	5 mA	
Idle Supply Current	IDLE	MAX2205/MAX2207/MAX2208		2	3.5	ША	
Shutdown Supply Current	ISHDN	SHDN = 0V		0.5	10	μA	
OUT Voltage During Shutdown	Vout	SHDN = 0V			0.01	V	
Logic-High Threshold	V <sub>H</sub>		2.0			V	
Logic-Low Threshold	VL				0.6	V	
SHDN Input Current	IIН	$\overline{\text{SHDN}} = +2.0\text{V}$	-1		+10		
SHDN Input Current	١ <sub>١</sub> ٢	$\overline{\text{SHDN}} = +0.6\text{V}$	-1		+1	μA	
Output Current Source Capability		MAX2206/MAX2207, V <sub>OUT</sub> = +2.5V	400			μA	
Output Current Sink Capability		MAX2206/MAX2207, $V_{OUT} = 0V$	300			μA	

### AC ELECTRICAL CHARACTERISTICS (MAX2205)

(MAX2205 EV kit, V<sub>CC</sub> = +2.7V to +5.0V,  $\overline{SHDN}$  = +2.0V, f<sub>RF</sub> = 800MHz to 2GHz, 50 $\Omega$  system, T<sub>A</sub> = -40°C to +85°C. Typical values are at V<sub>CC</sub> = +2.85V and T<sub>A</sub> = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	МАХ	UNITS
RF Input Frequency	f <sub>RF</sub>			800		2000	MHz
Turn-On Time	ton				2		μs
Response Time	t <sub>R</sub>	(Note 3)			15		μs
Variation Due to Tampa anture		V <sub>CC</sub> = +2.85V,	High input power (Note 4)		±0.3	±1	dD
Variation Due to Temperature		$T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$	Low input power (Note 5)		±1.3	±2.5	dB

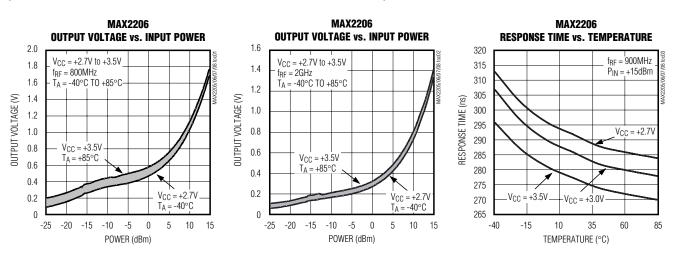
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#### AC ELECTRICAL CHARACTERISTICS (MAX2206/MAX2207/MAX2208)

(MAX2206/MAX2207/MAX2208 EV kit, V<sub>CC</sub> = +2.7V to +5.0V,  $\overline{SHDN}$  = 2.0V, f<sub>RF</sub> = 800MHz to 2GHz, 50 $\Omega$  system, T<sub>A</sub> = -40°C to +85°C. Typical values are at V<sub>CC</sub> = +2.85V and T<sub>A</sub> = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	MAX	UNITS
RF Input Frequency	f <sub>RF</sub>			800		2000	MHz
RF Input VSWR	VSWR				2:1		
Turn-On Time	ton				2		μs
Beenenge Time (Note 2)	ta	MAX2206/MAX2207			300		ns
Response Time (Note 3)	tR	MAX2208			15		μs
Variation Due to Temperature		$V_{CC} = +2.85V,$	High input power (Note 6)		±0.3	±1	dB
		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	Low input power (Note 7)		±1.3	±2.5	uБ

- Note 1: This device is constructed using a unique set of packaging techniques that imposes a limit on the thermal profile the device can be exposed to during board-level solder attach and rework. This limit permits only the use of the solder profiles recommended in the industry-standard specification, JEDEC 020 rev. C or later, paragraph 7.6, Table 3 for IR/VPR and convection reflow. Preheating is required. Hand or wave soldering is not allowed.
- **Note 2:** Specifications over  $T_A = -40^{\circ}$ C to  $+85^{\circ}$ C are guaranteed by design. Production tests are performed at  $T_A = +25^{\circ}$ C.
- Note 3: Response time is taken from the time the RF signal is applied to 90% of the final value of VOUT.
- Note 4: At 800MHz, output voltage is held at a value that nominally results from the final value of +31dBm input power. Deviation from +31dBm is specified. At 2GHz, output voltage is held at a value that nominally results from +28dBm input power. Deviation from +28dBm is specified.
- Note 5: At 2GHz, output voltage is held 22dB lower than specified in Note 4. At 800MHz, output voltage is held 25dB lower than specified in Note 4.
- Note 6: At 800MHz, output voltage is held at a value that nominally results from +15dBm input power. Deviation from +15dBm is specified. At 2GHz, output voltage is held at a value that nominally results from +13dBm input power. Deviation from +13dBm is specified.
- **Note 7:** For MAX2206, the output voltage is held at 40dB lower input power than specified in Note 6; for MAX2207/MAX2208, output voltage is held at a value that nominally results from 25dB lower input power than specified in Note 6. Deviation from the nominal input power is specified.



### **Typical Operating Characteristics**

(MAX2206/MAX2207/MAX2208 EV kit,  $T_A = +25^{\circ}C$ , unless otherwise noted.)

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**MAX2205-MAX2208** 

#### **Typical Operating Characteristics (continued)** (MAX2206/MAX2207/MAX2208 EV kit, $T_A = +25^{\circ}C$ , unless otherwise noted.) MAX2207 MAX2207/MAX2208 MAX2207/MAX2208 **RESPONSE TIME vs. TEMPERATURE OUTPUT VOLTAGE vs. INPUT POWER OUTPUT VOLTAGE vs. INPUT POWER** 310 1.8 2.5 V<sub>CC</sub> = +2.7V to +3.5V $f_{BF} = 900 MHz$ V<sub>CC</sub> = +2.7V to +3.5V f<sub>RF</sub> = 800MHz 1.6 $f_{RF} = 2GHz$ $P_{IN} = +15 dBm$ 305 $T_A = -40^{\circ}C TO +85^{\circ}C$ 2.0 T<sub>A</sub> = -40°C TO +85°C. 1.4 300 300 295 295 290 290 285 OUTPUT VOLTAGE (V) S $V_{CC} = +2.7V$ 1.2 **OUTPUT VOLTAGE** 1.5 1.0 0.8 1.0 $V_{CC} = +3.0V$ $V_{CC} = +3.5V$ 06 $V_{CC} = +3.5V$ 285 $T_A = +85^{\circ}C$ $T_A = +85^{\circ}C$ 0.4 0.5 $V_{CC} = +3.5V$ 280 $V_{CC} = +2.7V$ $T_A = -40^{\circ}C$ $V_{CC} = +2.7V$ 0.2 $T_A = -40^{\circ}C$ 0 275 0 -10 -5 0 5 10 15 -40 -15 10 35 60 85 -10 -5 0 5 10 15 POWER (dBm) POWER (dBm) TEMPERATURE (°C) MAX2205 MAX2205/MAX2208 MAX2205 **OUTPUT VOLTAGE vs. INPUT POWER RESPONSE TIME vs. TEMPERATURE OUTPUT VOLTAGE vs. INPUT POWER** 19 2.5 0.8 $V_{CC} = +2.7V \text{ to } +3.5V$ $f_{RF} = 1880MHz$ V<sub>CC</sub> = +2.7V to +3.5V $f_{RF} = 900 MHz$ f<sub>RF</sub> = 836MHz 18 P<sub>IN</sub> = +15dBm (MAX2208) 0.7 $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$ $T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$ $P_{IN} = +31 dBm (MAX2205)$ 2.0 17 0.6 MAX2205 EV KIT MAX2205 EV KIT RESPONSE TIME (µs) OUTPUT VOLTAGE (V) $\geq$ 16 $V_{CC} = +2.7V$ 0.5 **OUTPUT VOLTAGE** 1.5 15 V<sub>CC</sub> = +3.5V 0.4 $V_{CC} = +3.0V$ $T_A = +85^{\circ}C$ 14 $V_{CC} = +3.5V$ 1.0 0.3 $T_A = +85^{\circ}C$ 13 $V_{CC} = +3.5V$ 0.2 12 0.5 V<sub>CC</sub> = +2.7V $V_{CC} = +2.7V$ 0.1 $T_A = -40^{\circ}C$ 11 $T_A = -40^{\circ}C$ 0 10 0 6 10 14 18 22 26 -40 -15 35 60 85 6 11 21 26 31 10 16 INPUT POWER (dBm) INPUT POWER (dBm) TEMPERATURE (°C)

### **Pin Description**

/N/IXI/N

PIN	NAME	FUNCTION			
A1 RFIN/SHDN RF Input and Shutdown Logic Input. AC-couple the RF input to this pin and apply the shutdown log input through a resistor. Drive low to turn the part off, drive high, or connect to V <sub>CC</sub> to turn the part of					
A2	V <sub>CC</sub>	ower-Supply Pin. Bypass to GND with a capacitor as close to the bump as possible.			
B1	GND	Ground Connection. Multiple ground vias placed as close to the IC as possible should be used to connect the ground pin to the ground plane. Connect to PC board ground plane with as low inductance as possible.			
B2	OUT	Detector Output			

MAX2205-MAX2208

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The UCSP is a unique package that greatly reduces

board space compared to other packages. UCSP relia-

bility is integrally linked to the user's assembly methods,

circuit board material, and usage environment. The user

should closely review these areas when considering using a UCSP. This form factor might not perform equally

to a packaged product through traditional mechanical

reliability tests. Performance through operating life test

and moisture resistance remains uncompromised, as it

is determined primarily by the wafer-fabrication process.

Mechanical stress performance is a greater considera-

tion for a UCSP. UCSP solder-joint contact integrity must

be considered because the package is attached through

direct solder contact to the user's PC board. Testing

done to characterize the UCSP reliability performance

shows that it is capable of performing reliably through

environmental stresses. Results of environmental stress tests and additional usage data and recommendations

are detailed in the UCSP application note, which can be

found on Maxim's website, www.maxim-ic.com.

#### **Applications Information**

The MAX2205–MAX2208 have internal termination resistors for use with directional couplers. The application circuit is shown in Figure 1. The output of the detector goes to an op amp in an analog GSM power-control scheme, or to an ADC in other systems such as TDMA or discrete-time GSM power control.

The MAX2205 has high-input impedance for use with high-value resistive tapping from a CDMA power amplifier. This coupling method is the lowest cost and lowest loss when used with an isolator. The application circuit is shown in Figure 2. Connect CFILTER from the MAX2205 output to GND to reduce residual amplitude ripple. For IS98A reverse channel signal with peak-to-avg ratio of 3.9dB, a 1.5nF capacitor gives 43mVP-P ripple at 28dBm PA output and 390µs response time. For CDMA2000 (pilot + DCCH) with peak-to-avg ratio of 5.4dB, the ripple is about 65mVP-P at 26dBm PA output.

The MAX2205 input impedance is listed in Table 1.

<u>Layout</u>

As with any RF circuit, the layout of the MAX2205–MAX2208 circuits affects performance. Use a short 50 $\Omega$  line at the input with multiple ground vias along the length of the line. The input capacitor and resistor should be placed as close to the IC as possible. The V<sub>CC</sub> input should be bypassed as close as possible to the IC with multiple vias connecting the capacitor to ground. Refer to the MAX2205–MAX2208 EV kit data sheet for a sample layout and details.

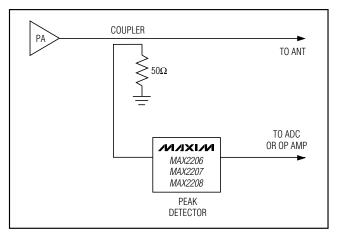


Figure 1. MAX2206/MAX2207/MAX2208 Typical Application Circuit

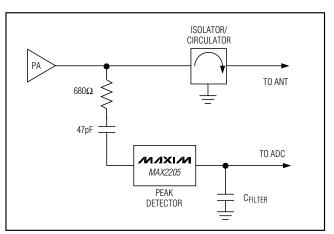


Figure 2. MAX2205 Typical Application Circuit

Chip Information

**UCSP** Reliability

TRANSISTOR COUNT: 344

	P <sub>IN</sub> = -3	0dBm	P <sub>IN</sub> = +	5dBm
FREQUENCY (GHz)	REAL	IMAG	REAL	IMAG
0.8	189.9	-51.7	199.4	-54.0
0.9	177.3	-47.4	185.5	-49.4
1.0	165.8	-43.6	175.2	-45.7
1.1	155.2	-40.3	167.0	-42.5
1.2	146.4	-37.6	158.8	-39.8
1.3	138.8	-35.0	150.9	-37.3
1.4	131.5	-32.9	144.0	-35.1
1.5	123.3	-30.7	139.4	-33.3
1.6	115.0	-29.1	131.6	-31.8
1.7	107.2	-27.5	132.0	-30.9
1.8	110.7	-26.7	126.6	-29.3
1.9	105.3	-25.2	120.3	-27.9
2.0	94.7	-23.6	111.4	-26.7

### Table 1. MAX2205 Input Impedance (R || jX, PC Board De-Embedded)

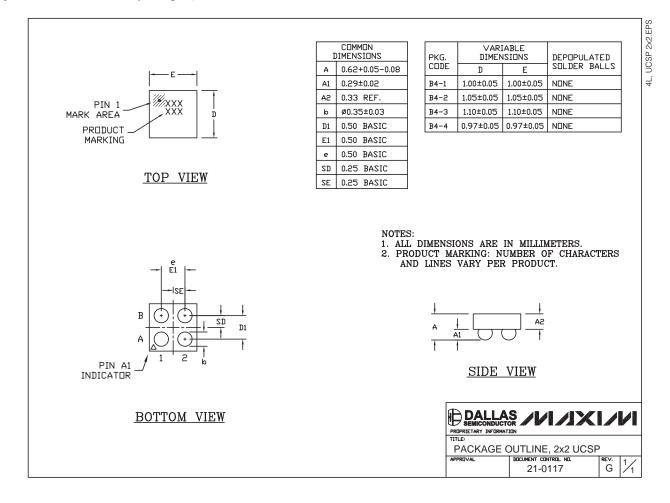
# Table 2. MAX2205–MAX2208 Device Marking Codes

DEVICE	CODE
MAX2205EBS	AFR
MAX2206EBS	AFO
MAX2207EBS	AFP
MAX2208EBS	AFQ

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### **Package Information**

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to <u>www.maxim-ic.com/packages</u>.)



**Revision History** 

Pages changed at Rev 6: 1, 2, 3, 7

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