

General Description

Information from subscriber lines needs to be transferred to time-slots on system internal PCM communication highways. The EPIC-S concentrates the circuitry necessary to do this interfacing for a large number of transmission lines on a single IC. Therefore, it is not necessary to repeat this PCM interface circuitry for every line. Applications of the EPIC-S include communications multiplexers, concentrators, central switches, PBXs, as well as peripheral ISDN and analog line cards.

Features

- PCM interface controller for up to 16 ISDN or 32 analog subscribers
- Time-slot assignment freely programmable for all subscribers
- Non-blocking switch for 64 channels
- Switching of 16-, 32-, 64-kbit/s channels (128 kbit/s via two consecutive 64-kbit/s channels)

Type	Package
PEB 2054-N	P-LCC-44-1 (SMD)
PEF 2054-N	P-LCC-44-1 (SMD)

- Two serial interfaces: PCM and configurable
- Programmable for a wide range of data rates (PCM up to 8 Mbit/s, configurable up to 4 Mbit/s)
- Data rates of PCM and configurable interfaces independent of each other (data rate adaption)
- Change detection (“last look”) logic for C/I or signaling channels
- 16-byte FIFO for monitor or feature control channels
- Standard μ P interface with multiplexed address/data bus or separate address and data busses
- Advanced low power CMOS technology
- Also available with extended temperature range – 40 to 85 °C (PEF 2054-N)
- Pin compatible with PEB 2055

