

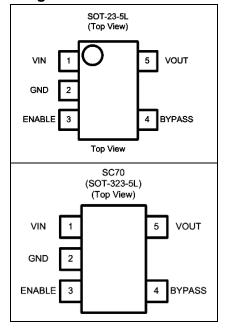
SE5508

Description

The SE5508 series of fixed output low dropout linear regulators are designed for portable battery powered applications, which require low noise environment, fast enable response time, and low dropout voltage. An optional bypass capacitor can be added for better low-noise performance. Each device contains a voltage reference unit, an error amplifier, a PMOS power transistor, and resistors for setting output voltage, and current limit and temperature limit protection circuits.

The SE5508 has been designed to be used with low cost capacitors and requires a minimum output capacitor of $1.0\mu F$. Standard voltage versions are 1.5, 1.8, 2.5, 2.8, 3.0, and 3.3V.

Pin Configuration



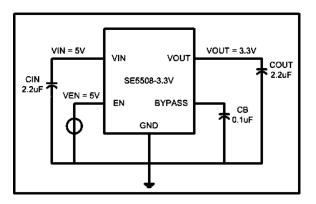
Features

- Excellent Noise Rejection at 62 dB.
- Typical Low Dropout Voltage of 200mV at 75mA
- Fast Enable Turn-On Time of 20µs (Typ.)
- Excellent Line and Load Regulation.
- > High Accuracy Output Voltage of 2%.
- > Typical Low Ground Current at 50µA
- > Typical Disable Current Less than 0.1µA
- > Thermal Protection.
- Standard SOT23-5L and SC70 (SOT353) Package.
- Available in Lead-Free Packages.

Applications

- Cellphones.
- Wireless LAN's.
- Hand-Held Instrumentation.
- Portable Video Game Devices.
- Digital Cameras.

Application Diagram



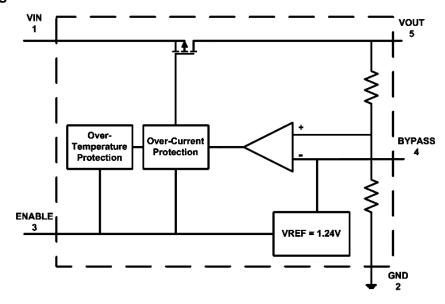


SE5508

Ordering/Marking Information

Package	Ordering Information		Marki	ng Information		
SOT-23-5L (Top View)	3.3V	SE5508ALG	5 <u>0</u> 8ALz			
VIN 1 5 VOUT	2.8V	SE5508BLG	5 <u>0</u> 8BLz	Starting with 5, a bar on top		
	2.5V	SE5508CLG	5 <u>0</u> 8CLz	of 5 is for production year		
GND 2	1.8V	SE5508DLG	5 <u>0</u> 8DLz	2001, and underlined 5 is for		
ENABLE 3 4 BYPASS	1.5V	SE5508ELG	5 <u>0</u> 8ELz	year 2002. The next character is marked on top for 2003, and		
Top View	3.0V	SE5508FLG	5 <u>0</u> 8FLz	underlined for 2004. The		
SC70 (SOT-323-5L)	3.3V	SE5508APG	8 <u>A</u> P	naming pattern continues with		
(Top View)	2.8V	SE5508BPG	8 <u>B</u> P	consecutive characters for later years.		
VIN 1 5 VOUT	2.5V	SE5508CPG	8 <u>C</u> P	The last character is the		
GND 2	1.8V	SE5508DPG	8 <u>D</u> P	week code. (A-Z: 1-26, a-z:		
ENABLE 3 4 BYPASS	1.5V	SE5508EPG	8 <u>E</u> P	27-52)		
	3.0V	SE5508FPG	8 <u>F</u> P			

Block Diagram







Absolute Maximum Rating (1)

Parameter	Symbol	Value	Units
Input Voltage	V _{IN}	6	V
Enable Voltage	V_{EN}	-0.3 to V_{IN}	V
Output Voltage	V _{OUT}	-0.3 to V _{IN}	V
Power Dissipation	P _D	Internally Limited ⁽³⁾	
Output Short Circuit Duration		Infinite	
Thermal Resistance, Junction-to-Ambient	Θ_{JA}	230	°C/W
Lead Temperature (Soldering, 5 sec.)		260	°C
Junction Temperature	TJ	0 to +150	°C
Storage Temperature	Ts	-40 to +150	°C

Operating Rating (2)

Parameter	Symbol	Value	Units
Supply Input Voltage	V _{IN}	+2.0V to +5.5	V
Junction Temperature	TJ	0 to +125	°C

Electrical Characteristics

 V_{IN} = 5V; C_{IN} = 2.2 μ F; C_{OUT} = 2.21 μ F; I_{OUT} = 10mA; T_{J} = 25°C; unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		SE5508 – 1.5	1.470	1.5	1.530	
		SE5508 – 1.8	1.764	1.8	1.836	V
V _{оит}	Output Voltage	SE5508 – 2.5	2.450	2.5	2.550	V
	Accuracy	SE5508 – 2.8	2.744	2.8	2.856	V
		SE5508 - 3.0	2.940	3.0	3.060	V
		SE5508 - 3.3	3.234	3.3	3.366	V
ΔV_{OUT}	Line Regulation	$V_{IN} = V_{OUT} + 1V$ to 6V, $I_{OUT} = 10$ mA		1		%/V
		$(V_{OUT} = 3.3V)$				
ΔV_{OUT}	Load Degulation (5)	V_{IN} = 5V; I_{OUT} = 10mA to 150mA		1		%
	Load Regulation (5)	$(V_{OUT} = 3.3V)$		'		70
	Output Voltage					
ΔV _{OUT} /ΔΤ	Temperature	Note 4		0.033		mV/°C
	Coefficient					



SE5508

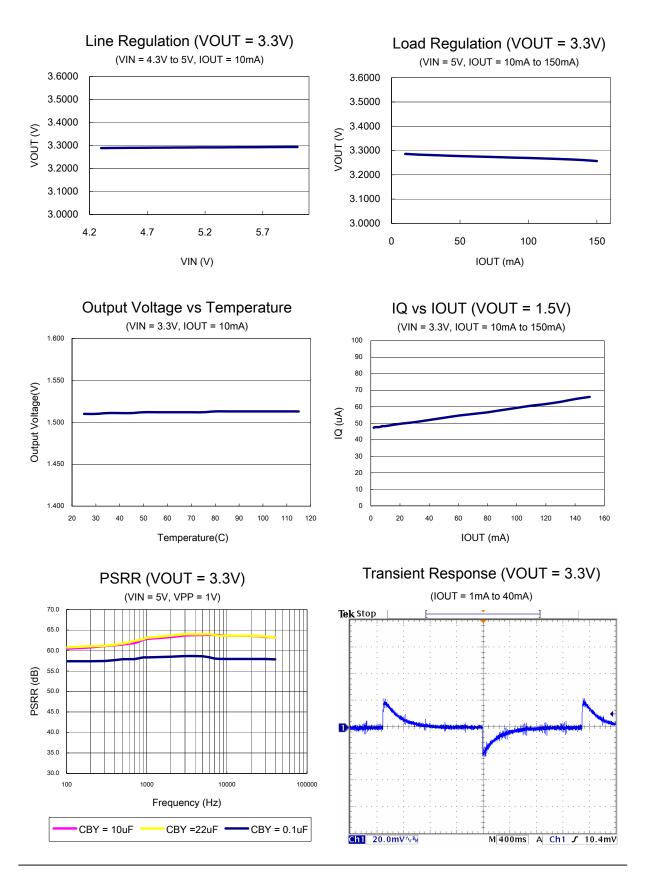
Electrical Characteristics (Continued)

 V_{IN} = 5V; C_{IN} = 2.2 μ F; C_{OUT} = 2.2 μ F; I_{OUT} = 10mA; T_J = 25°C; unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
$V_{\text{IN}} - V_{\text{OUT}}$	Dropout Voltage (6)	I _{OUT} = 10mA		20	1		
		I _{OUT} = 75mA		200		mV	
		I _{OUT} = 150mA		500	-		
T _{PROTECTION}	Thermal Protection	Thermal Protection Temperature		150	-	°C	
		Protection Hysterisys		20			
PSRR	Ripple Rejection	f = 120 Hz		62	1	dB	
IQ	Quiescent Current	V _{EN} = 0V (VOUT = 1.5V)		0.1	-1		
		$V_{EN} = V_{TH(EN)}; I_{OUT} = 10mA$ (VOUT = 1.5V)		50		μΑ	
$V_{TH(EN)}$	Enable Input	Voltage Increasing, Output Turns On, Logic High	1.6			V	
	Threshold Voltage	Voltage Decreasing, Output Turns Off, Logic Low			0.4		
I _{LIMIT}	Current Limit		300			mA	

- Note 1: Exceeding the absolute maximum rating may damage the device.
- Note 2: The device is not guaranteed to function outside its operating rating.
- Note 3: The maximum allowable power dissipation at any T_A (ambient temperature) is calculated using: $P_{D(MAX)} = (T_{J(MAX)} T_A)/\Theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown. See "Thermal Consideration" section for details
- Note 4: Output voltage temperature coefficient is the worst case voltage change divided by the total temperature range.
- **Note 5:** Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 150mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- **Note 6:** Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.







SE5508

Application Hints

Like any low dropout regulator, SE5508 requires external capacitors to ensure stability. The external capacitors must be carefully selected to ensure performance.

Input Capacitor

An input capacitor of at least $1\mu F$ is required. Ceramic or Tantalum can be used. The value can be increase without upper limit.

Output Capacitor

An output capacitor is required for stability. It must be placed no more than 1 cm away from the V_{OUT} pin, and connected directly between V_{OUT} and GND pins. The minimum value is $1\mu\text{F}$ but may be increase without limit.

Thermal Considerations

It is important that the thermal limit of the package is not exceeded. The SE5508 has built-in thermal protection. When the thermal limit is exceeded, the IC will enter protection, and V_{OUT} will be pulled to ground. The power dissipation for a given application can be calculated as following:

The power dissipation (P_D) is

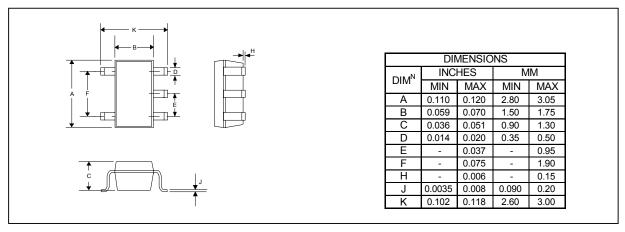
 $P_D = I_{OUT} * [V_{IN} - V_{OUT}]$

The thermal limit of the package is then limited to $P_{D(MAX)} = [T_J - T_A]/\Theta_{JA}$ where T_J is the junction temperature, TA is the ambient temperature, and Θ_{JA} is around 230°C/W for SE5508. SE5508 is designed to enter thermal protection at 150°C. For example, if T_A is 25°C then the maximum P_D is limited to about 0.6W. In other words, if $I_{OUT(MAX)} = 150$ mA, then $[V_{IN} - V_{OUT}]$ cannot exceed 3.6V.

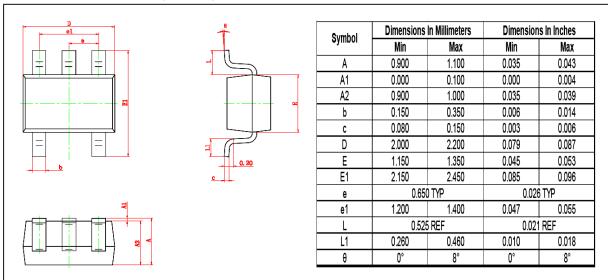


SE5508

OUTLINE DRAWING SOT-23-5L



OUTLINE DRAWING SC70 (SOT353)





SE5508

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