4-bit Latch/4-to-16-line Decoder

HITACHI

Description

This device presents 4-to-16 line decoder with latched inputs. The HD74HC4515 presents a low level at the selected output.

This device consists of four storage latches with common strobe and inhibit (\overline{G}) inputs. When a low signal is applied to the strobe input, the input data is stored, decoded, and presented to the output. When inhibit is high, all HD74HC4515 outputs are a high logic level.

Features

• High Speed Operation: t_{pd} (Data to S) = 20 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

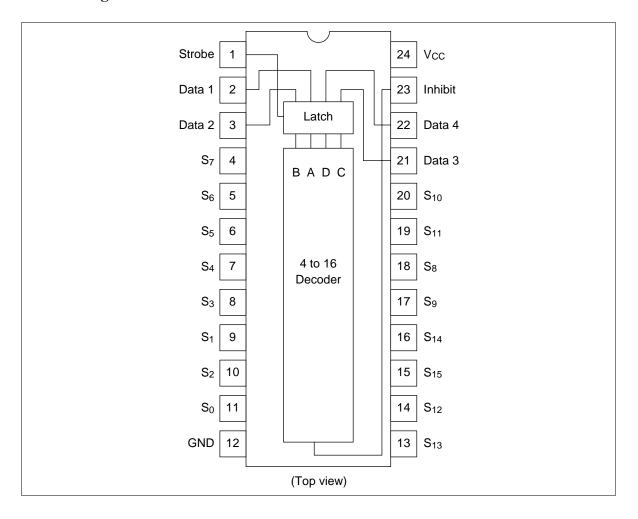


Function Table (Strobe = High)

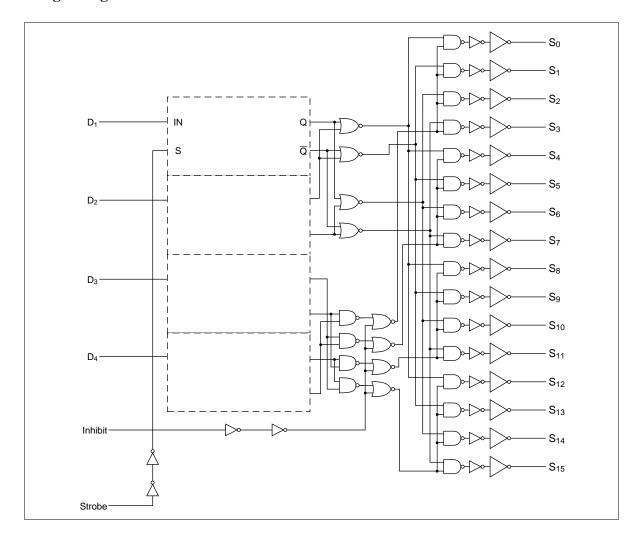
Data Inputs

	p :				
Inhibit	D	С	В	Α	Select Outputs
L	L	L	L	L	S ₀
L	L	L	L	Н	S ₁
L	L	L	Н	L	S ₂
L	L	L	Н	Н	S ₃
L	L	Н	L	L	S ₄
L	L	Н	L	Н	S ₅
L	L	Н	Н	L	S ₆
L	L	Н	Н	Н	S ₇
L	Н	L	L	L	S ₈
L	Н	L	L	Н	S ₉
L	Н	L	Н	L	S ₁₀
L	Н	L	Н	Н	S ₁₁
L	Н	Н	L	L	S ₁₂
L	Н	Н	L	Н	S ₁₃
L	Н	Н	Н	L	S ₁₄
L	Н	Н	Н	Н	S ₁₅
Н	Х	Х	X	Х	All output "H"

Pin Arrangement



Logic Diagram



DC Characteristics

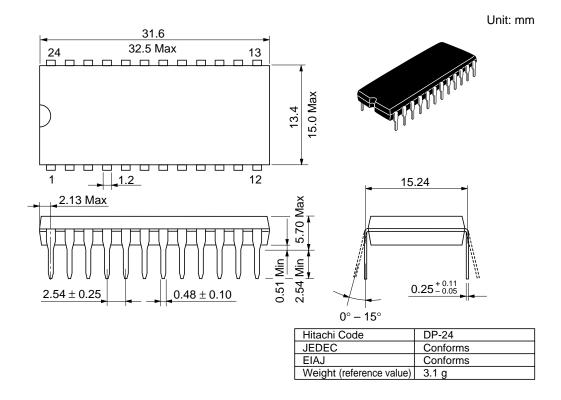
	Ta = -40 to
Ta = 25°C	+85°C

			1a = 23 C +03 C							
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	ns
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_	_		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	=		
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	Vin = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_	-		
		6.0	5.9	6.0	_	5.9	_	-		
		4.5	4.18	_	_	4.13	_	_		$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_	_	5.63	_	_		$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	Vin = V _{IH} or V _{IL}	I _{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1	-		
		6.0	_	0.0	0.1	_	0.1	_		
		4.5	_	_	0.26	_	0.33	-		I _{OL} = 4 mA
		6.0	_	_	0.26	_	0.33	_		I _{OL} = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GN	ND
Quiescent supply current	I _{cc}	6.0	_	_	4.0	_	40	μΑ	Vin = V _{cc} or Gf	ND, lout = 0 μA

AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

Ta = -40 to $Ta = 25^{\circ}C$ +85°C

	Symbol	V _{cc} (V)	1a = 25°C		+85°C				
Item			Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	225	_	280	ns	Data to output
time	$t_{\tiny PHL}$	4.5	_	20	45	_	56	_	
		6.0	_	_	38	_	48		
	t_{PLH}	2.0	_	_	230	_	290	ns	Strobe to output
	$t_{\tiny PHL}$	4.5	_	21	46	_	58		
		6.0	_	_	39	_	49		
	t _{PLH}	2.0	_	_	175	_	220	ns	Inhibit to output
	$t_{\tiny PHL}$	4.5	_	15	35	_	44	=	
		6.0	_	_	30	_	37		
Pulse width	t _w	2.0	80	_	_	100	_	ns	Strobe
		4.5	16	5	_	20	_		
		6.0	14	_	_	17	_		
Setup time	t _{su}	2.0	100	_	_	125	_	ns	Data to Strobe
		4.5	20	1	_	25	_		
		6.0	17	_	_	21	_		
Hold time	t _h	2.0	5	_	_	5	_	ns	Strobe to Data
		4.5	5	-1	_	5	_		
		6.0	5	_	_	5	_	_	
Output rise/fall	t _{TLH}	2.0	_	_	75	_	95	ns	
time	t_{THL}	4.5	_	5	15	_	19	=	
		6.0	_	_	13	_	16	_	
Input capacitance	Cin	_	_	5	10	_	10	pF	



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