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# HD74LVC533

## Octal D-type Transparent Latches with 3-state Outputs

# HITACHI

ADE-205-070B(Z)

Rev.2

September 1995

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### Description

The HD74LVC533 has eight D type latches with three state outputs in a 20 pin package. When the latch enable input is high, the Q outputs will follow the D inputs. When the latch enable goes low, data at the D inputs will be retained at the outputs until latch enable returns high again. When a high logic level is applied to the output control input, all outputs go to a high impedance state, regardless of what signals are present at the other inputs and the state of the storage elements. Low voltage and high speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

### Features

- $V_{CC} = 2.0\text{ V to }5.5\text{ V}$
- All inputs  $V_{IH} (\text{Max.}) = 5.5\text{ V} (@V_{CC} = 0\text{ V to }5.5\text{ V})$
- Typical  $V_{OL}$  ground bounce  $< 0.8\text{ V} (@V_{CC} = 3.3\text{ V}, T_a = 25^\circ\text{C})$
- Typical  $V_{OH}$  undershoot  $> 2.0\text{ V} (@V_{CC} = 3.3\text{ V}, T_a = 25^\circ\text{C})$
- High output current  $\pm 24\text{ mA} (@V_{CC} = 3.0\text{ V to }5.5\text{ V})$

### Function Table

Inputs			Output $\bar{Q}$
$\bar{G}$	LE	D	
H	X	X	Z
L	H	L	H
L	H	H	L
L	L	X	$Q_0$

H: High level

L: Low level

X: Immaterial

Z: High impedance

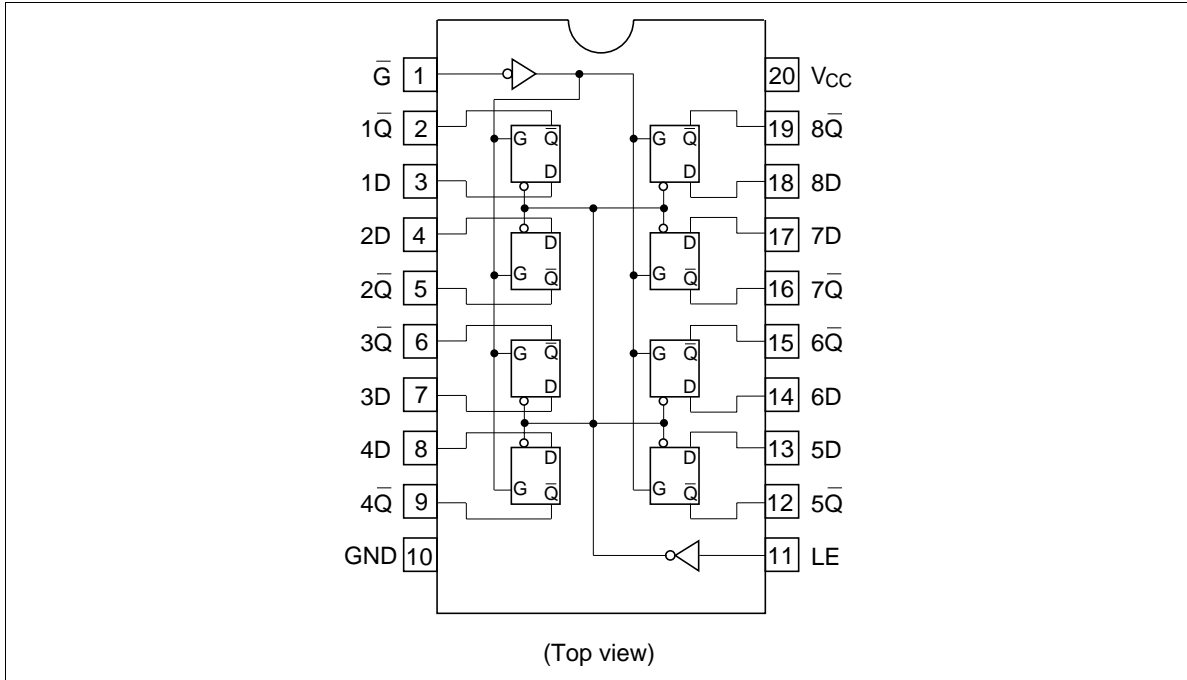
 $Q_0$ : Level of  $\bar{Q}$  before the indicated steady input conditions were established.

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# HD74LVC533

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## Pin Arrangement



**Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	$V_{CC}$	-0.5 to 6.0	V	
Input diode current	$I_{IK}$	-50	mA	$V_I = -0.5\text{ V}$
Input voltage	$V_I$	-0.5 to 6.0	V	
Output diode current	$I_{OK}$	-50	mA	$V_O = -0.5\text{ V}$
		50	mA	$V_O = V_{CC}+0.5\text{ V}$
Output voltage	$V_O$	-0.5 to $V_{CC} +0.5$	V	
Output current	$I_O$	$\pm 50$	mA	
$V_{CC}$ , GND current / pin	$I_{CC}$ or $I_{GND}$	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

**Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	$V_{CC}$	1.5 to 5.5	V	Data retention
		2.0 to 5.5	V	At operation
Input / output voltage	$V_I$	0 to 5.5	V	$\overline{G}$ , LE, D
	$V_O$	0 to $V_{CC}$	V	$\overline{Q}$
Operating temperature	Ta	-40 to 85	°C	
Output current	$I_{OH}$	-12	mA	$V_{CC} = 2.7\text{ V}$
		-24 <sup>2</sup>	mA	$V_{CC} = 3.0\text{ V to }5.5\text{ V}$
	$I_{OL}$	12	mA	$V_{CC} = 2.7\text{ V}$
		24 <sup>2</sup>	mA	$V_{CC} = 3.0\text{ V to }5.5\text{ V}$
Input rise / fall time <sup>1</sup>	$t_r, t_f$	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform : Refer to test circuit of switching characteristics.

2. duty cycle  $\leq 50\%$

## HD74LVC533

### Electrical Characteristics

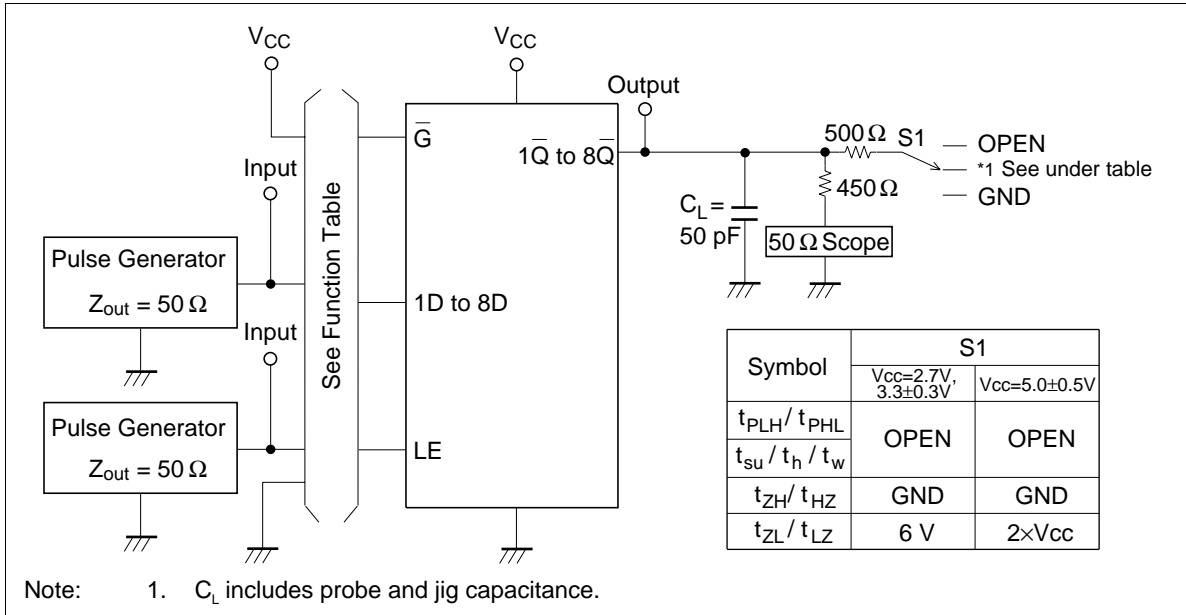
Item	Symbol	V <sub>CC</sub> (V)	Ta = -40 to 85°C		Unit	Test Conditions
			Min	Max		
Input voltage	V <sub>IH</sub>	2.7 to 3.6	2.0	—	V	
		4.5 to 5.5	V <sub>CC</sub> ×0.7	—	V	
	V <sub>IL</sub>	2.7 to 3.6	—	0.8	V	
		4.5 to 5.5	—	V <sub>CC</sub> ×0.3	V	
Output voltage	V <sub>OH</sub>	2.7 to 5.5	V <sub>CC</sub> -0.2	—	V	I <sub>OH</sub> = -100 μA
		2.7	2.2	—	V	I <sub>OH</sub> = -12 mA
		3.0	2.4	—	V	
		3.0	2.0	—	V	I <sub>OH</sub> = -24 mA
		4.5	3.8	—	V	
	V <sub>OL</sub>	2.7 to 5.5	—	0.2	V	I <sub>OL</sub> = 100 μA
		2.7	—	0.4	V	I <sub>OL</sub> = 12 mA
		3.0	—	0.55	V	I <sub>OL</sub> = 24 mA
		4.5	—	0.55	V	
Input current	I <sub>IN</sub>	0 to 5.5	—	±5.0	μA	V <sub>IN</sub> = 5.5 V or GND
Off state output current	I <sub>OZ</sub>	5.5	—	±10	μA	V <sub>IN</sub> = V <sub>CC</sub> , GND V <sub>OUT</sub> = V <sub>CC</sub> or GND
Quiescent supply current	I <sub>CC</sub>	5.5	—	20	μA	V <sub>IN</sub> = V <sub>CC</sub> or GND
	ΔI <sub>CC</sub>	3.0 to 3.6	—	500	μA	V <sub>IN</sub> = one input at (V <sub>CC</sub> -0.6)V, other inputs at V <sub>CC</sub> or GND

**Switching Characteristics**

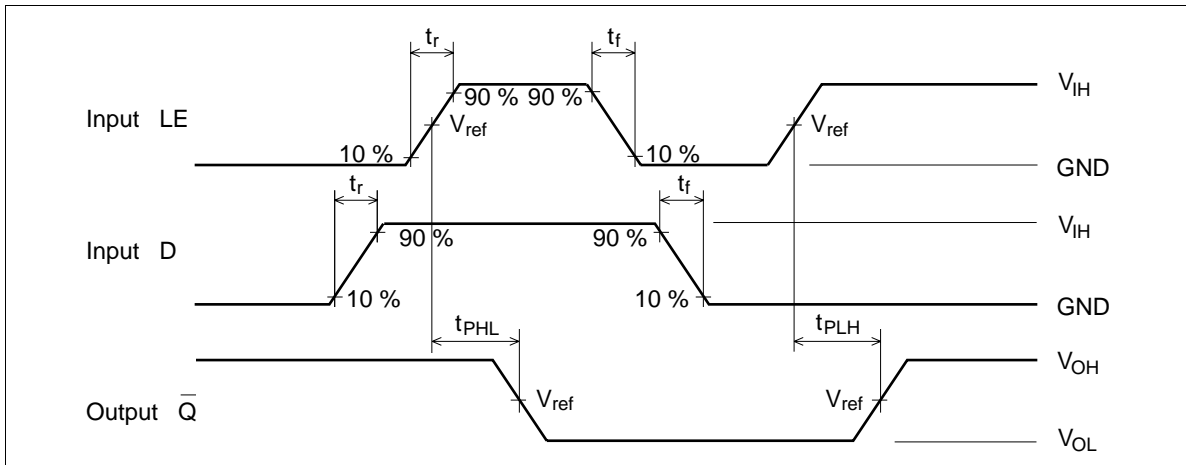
Item	Symbol	V <sub>CC</sub> (V)	Ta = -40 to 85°C			Unit	From (Input)	To (Output)
			Min	Typ	Max			
Propagation delay time	t <sub>PLH</sub>	2.7	—	7.0	9.0	ns	D	$\overline{Q}$
		3.3±0.3	1.5	5.0	8.0	ns		
		5.0±0.5	—	4.0	6.5	ns		
	t <sub>PHL</sub>	2.7	—	7.5	10.5	ns	LE	$\overline{Q}$
		3.3±0.3	1.5	5.5	9.5	ns		
		5.0±0.5	—	4.0	8.0	ns		
Output enable time	t <sub>ZH</sub>	2.7	—	7.5	9.5	ns	$\overline{G}$	$\overline{Q}$
	t <sub>ZL</sub>	3.3±0.3	1.5	5.5	8.5	ns		
		5.0±0.5	—	4.0	7.0	ns		
Output disable time	t <sub>HZ</sub>	2.7	—	5.0	8.5	ns	$\overline{G}$	$\overline{Q}$
	t <sub>LZ</sub>	3.3±0.3	1.5	4.5	7.5	ns		
		5.0±0.5	—	3.5	6.5	ns		
Setup time	t <sub>su</sub>	2.7	2.0	—	—	ns		
		3.3±0.3	2.0	—	—	ns		
		5.0±0.5	2.0	—	—	ns		
Hold time	t <sub>h</sub>	2.7	2.0	—	—	ns		
		3.3±0.3	2.0	—	—	ns		
		5.0±0.5	2.0	—	—	ns		
Pulse width	t <sub>w</sub>	2.7	4.0	—	—	ns		
		3.3±0.3	4.0	—	—	ns		
		5.0±0.5	4.0	—	—	ns		
Input capacitance	C <sub>IN</sub>	2.7	—	3.0	—	pF		
Output capacitance	C <sub>O</sub>	2.7	—	15.0	—	pF		

# HD74LVC533

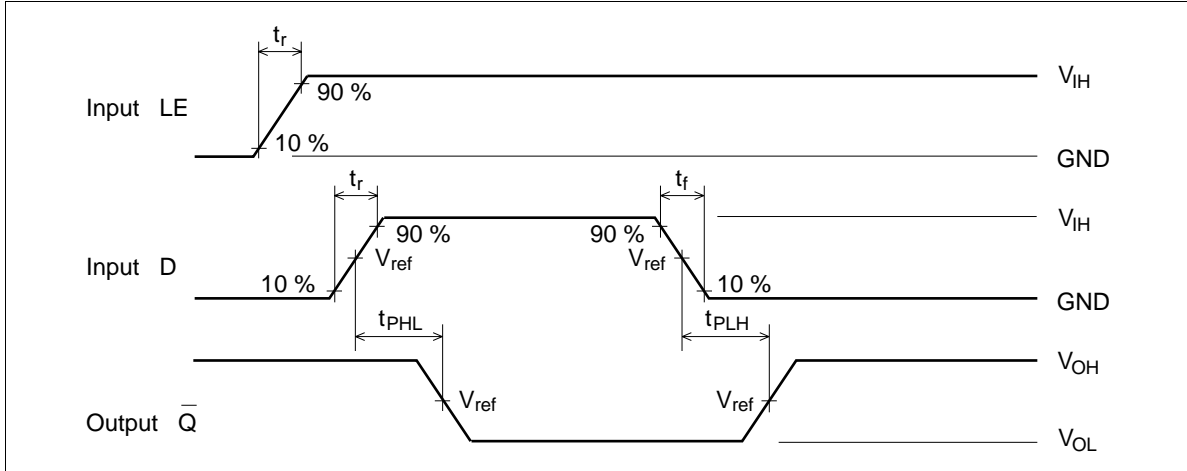
## Test Circuit



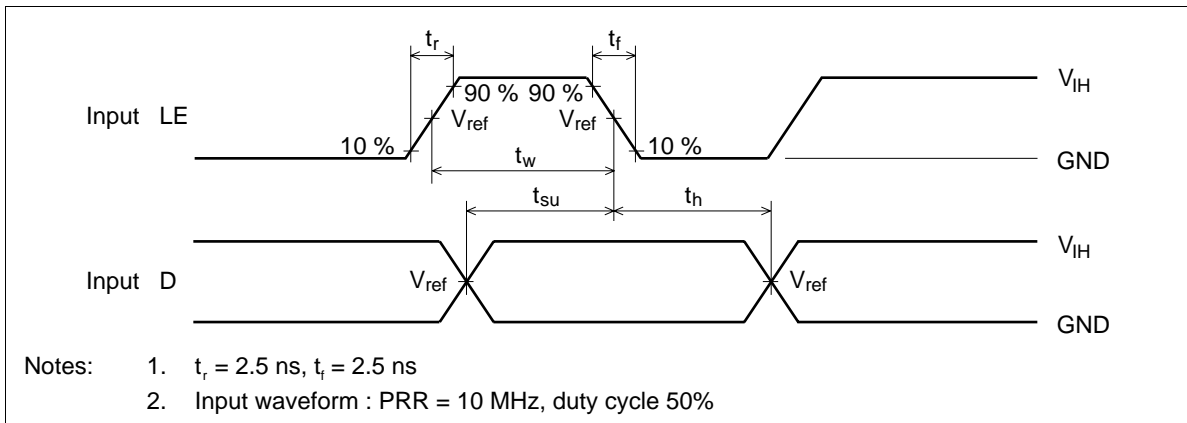
## Waveforms – 1



Waveforms – 2

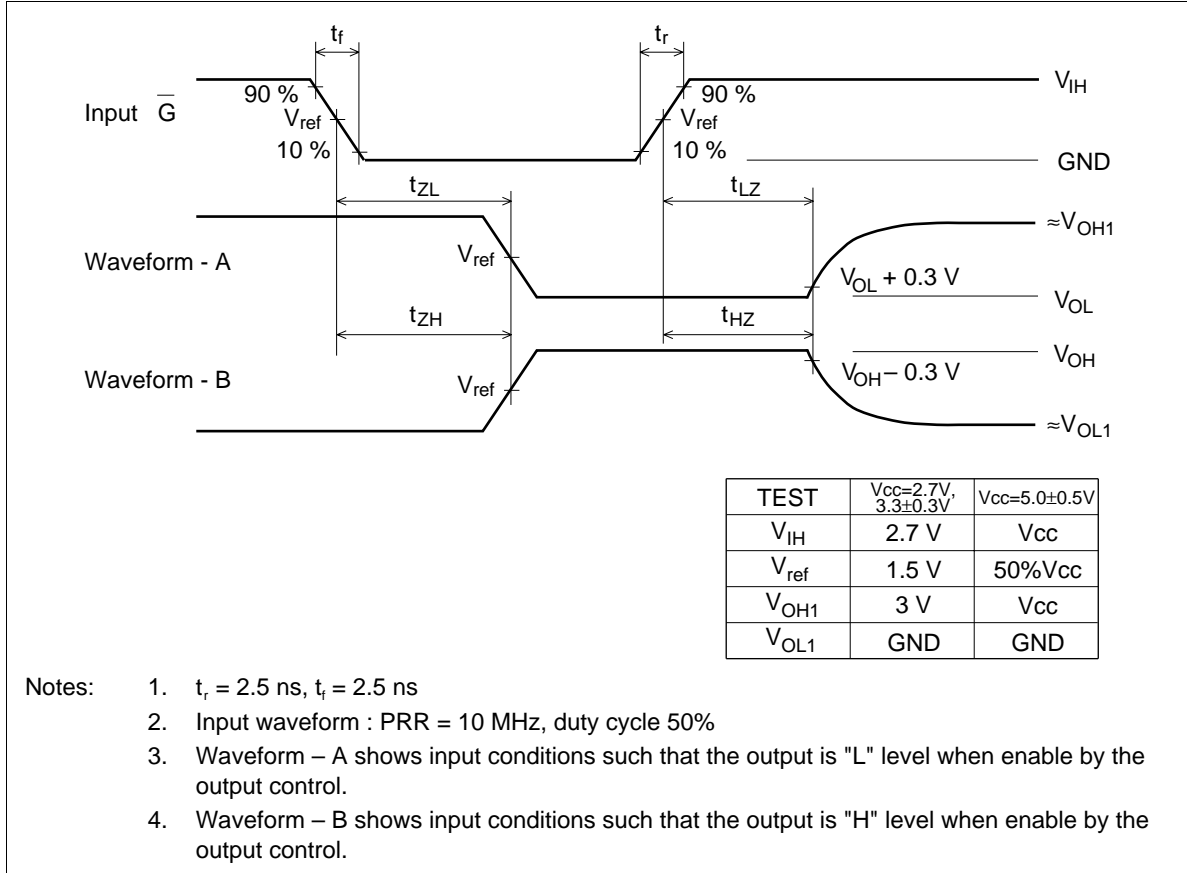


Waveforms – 3



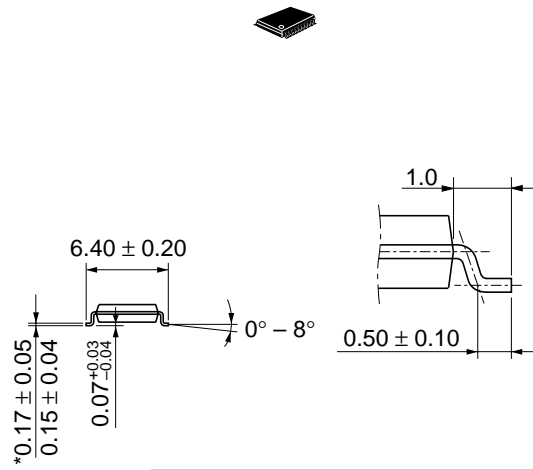
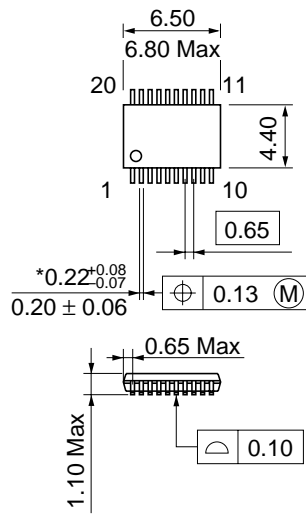
# HD74LVC533

## Waveforms – 4





Unit: mm



\*Dimension including the plating thickness  
Base material dimension

Hitachi Code	TTP-20DA
JEDEC	—
EIAJ	—
Weight (reference value)	0.07 g

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