HD74LVC139

Dual 2-to-4-line Decoders / Demultiplexers

HITACHI

ADE-205-069B(Z) Rev.2 September 1995

Description

The HD74LVC139 has two independent two-to-four-line decoders each with a single active low enable input in a 16 pin package. Data on the select inputs cause one of the four normally high outputs to go low. Low voltage and high speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- Typical V_{OL} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- High output current ± 24 mA (@V_{CC} = 3.0 V to 5.5 V)

Function Table

Input

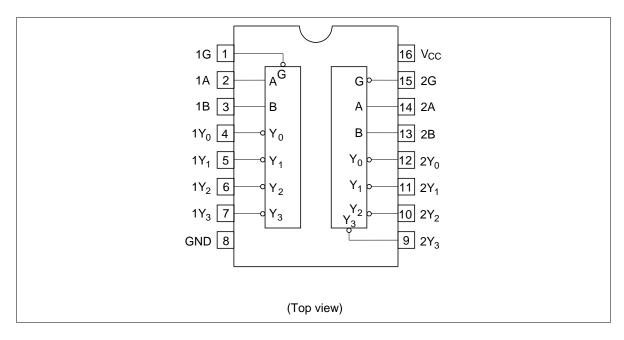
Enable	Select			Outputs			
G	В	А	Y_0	Y ₁	Y ₂	Y ₃	
Н	Х	Х	Н	Н	Н	Н	
L	L	L	L	Н	Н	Н	
L	L	Н	Н	L	Н	Н	
L	Н	L	Н	Н	L	Н	
L	Н	Н	Н	Н	Н	L	

H: High levelL: Low levelX: Immaterial



HD74LVC139

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{cc}	-0.5 to 6.0	V	_
Input diode current	I _{IK}	- 50	mA	$V_1 = -0.5 \text{ V}$
Input voltage	V _i	-0.5 to 6.0	V	
Output diode current	I _{OK}	- 50	mA	V _o = -0.5 V
		50	mA	$V_{o} = V_{cc} + 0.5 \text{ V}$
Output voltage	Vo	-0.5 to V_{cc} +0).5 V	
Output current	Io	±50	mA	
V_{CC} , GND current / pin I_{CC} or I_{GND}		100	mA	
Storage temperature Tstg		-65 to 150	°C	

Note: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{cc}	1.5 to 5.5	V	Data retention
		2.0 to 5.5	V	At operation
Input / output voltage	Vı	0 to 5.5	V	G, A, B
	Vo	0 to V _{cc}	V	Y ₀ to Y ₃
Operating temperature	Та	-40 to 85	°C	
Output current	I _{OH}	-12	mA	V _{CC} = 2.7 V
		-24 ^{*2}	mA	$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
	I _{OL}	12	mA	V _{CC} = 2.7 V
		24 *2	mA	$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
Input rise / fall time *1	t _r , t _f	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

2. duty cycle $\leq 50\%$

Electrical Characteristics

Ta = -40 to 85°C

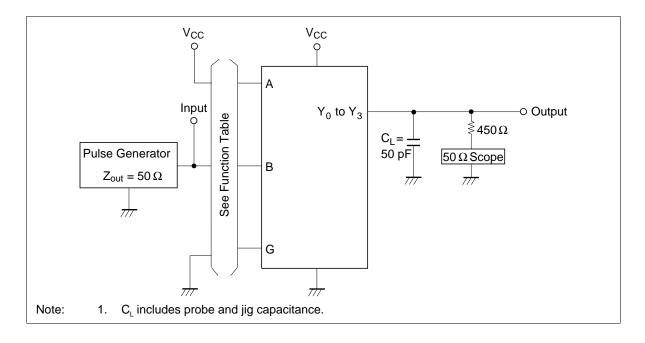
Item	Symbol	V _{cc} (V)	Min	Max	Unit	Test Conditions
Input voltage	V _{IH}	2.7 to 3.6	2.0	_	V	
		4.5 to 5.5	V _{cc} ×0.7	_	V	-
	V _{IL}	2.7 to 3.6	_	0.8	V	
		4.5 to 5.5	_	V _{cc} ×0.3	V	_
Output voltage	V_{OH}	2.7 to 5.5	V _{CC} -0.2	_	V	$I_{OH} = -100 \mu A$
		2.7	2.2	_	V	I _{OH} = -12 mA
		3.0	2.4	_	V	_
		3.0	2.0	_	V	$I_{OH} = -24 \text{ mA}$
		4.5	3.8	_	V	_
	V _{OL}	2.7 to 5.5	_	0.2	V	I _{OL} = 100 μA
		2.7	_	0.4	V	I _{OL} = 12 mA
		3.0	_	0.55	V	I _{OL} = 24 mA
		4.5	_	0.55	V	-
Input current	I _{IN}	0 to 5.5	_	±5.0	μΑ	V _{IN} = 5.5 V or GND
Quiescent supply current	I _{cc}	5.5	_	20	μΑ	$V_{IN} = V_{CC}$ or GND
	ΔI_{CC}	3.0 to 3.6	_	500	μΑ	V_{IN} = one input at $(V_{CC} - 0.6)V$, other inputs at V_{CC} or GND

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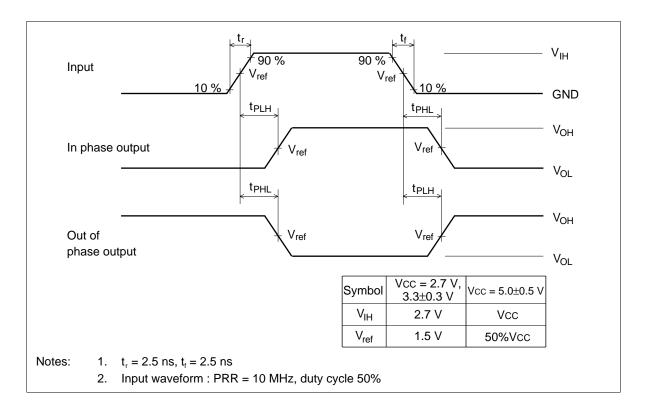
Switching Characteristics

			Ta = $-40 \text{ to } 85^{\circ}\text{C}$			_		
Item	Symbol V _{cc} (V)		Min	Тур	Max	Unit	From (Input)	To (Output)
Propagation delay time	t _{PLH}	2.7	_	7.0	10.0	ns	G, A, B	Y ₀ to Y ₃
	$t_{\tiny PHL}$	3.3±0.3	1.5	5.0	9.0	ns		
		5.0±0.5	_	3.5	7.5	ns		
Input capacitance	C_{IN}	2.7	_	3.0	_	pF		
Output capacitance	Co	2.7	_	15.0	_	pF		

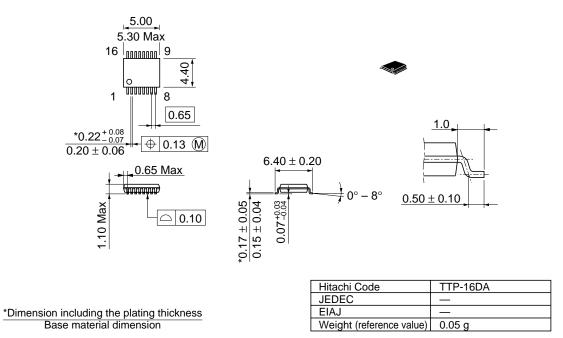
Test Circuit



Waveforms



Unit: mm



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